

**FAIRCHILD**

A Schlumberger Company

# $\mu$ A78MG • $\mu$ A79MG 4-Terminal Adjustable Voltage Regulators

Linear Division Voltage Regulators

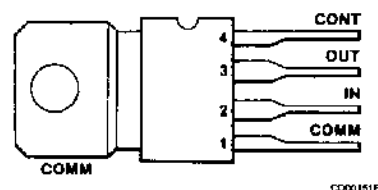
**Description**

The  $\mu$ A78MG and  $\mu$ A79MG are 4-terminal adjustable voltage regulators. They are designed to deliver continuous load currents of up to 500 mA with a maximum input voltage of +40 V for the positive regulator  $\mu$ A78MG and -40 V for the negative regulator  $\mu$ A79MG. Output current capability can be increased to greater than 10 A through use of one or more external transistors. The output voltage range of the  $\mu$ A78MG positive voltage regulator is 5.0 V to 30 V and the output voltage range of the negative  $\mu$ A79MG is -30 to -2.2 V. For systems requiring both a positive and negative, the  $\mu$ A78MG and  $\mu$ A79MG are excellent for use as a dual tracking regulator. These 4-terminal voltage regulators are constructed using the Fairchild Planar process.

- Output Current In Excess Of 0.5 A
- $\mu$ A78MG Positive Output Voltage +5.0 To +30 V
- $\mu$ A79MG Negative Output Voltage -30 V To -2.2 V
- Internal Thermal Overload Protection
- Internal Short Circuit Current Protection
- Output Transistor Safe-Area Protection

**Absolute Maximum Ratings**

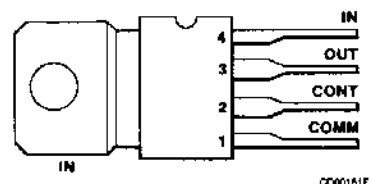
Storage Temperature Range	-65°C to +150°C
Operating Junction Temperature	
Range	0°C to 150°C
Lead Temperature (soldering, 10 s)	265°C
Internal Power Dissipation	Internally Limited
Input Voltage	
$\mu$ A78MGC	+40 V
$\mu$ A79MGC	-40 V
Control Lead Voltage	
$\mu$ A78MGC	$0 \text{ V} \leq V+ \leq V_O$
$\mu$ A79MGC	$V_{O-} \leq -V \leq 0 \text{ V}$

**Connection Diagram**  
 $\mu$ A78MG Power Watt  
 (Top View)


Heat sink tabs connected to input through device substrate. Not recommended for direct electrical connection.

**Order Information**

Device Code	Package Code	Package Description
$\mu$ A78MGU1C	8Z	Molded Power Pack

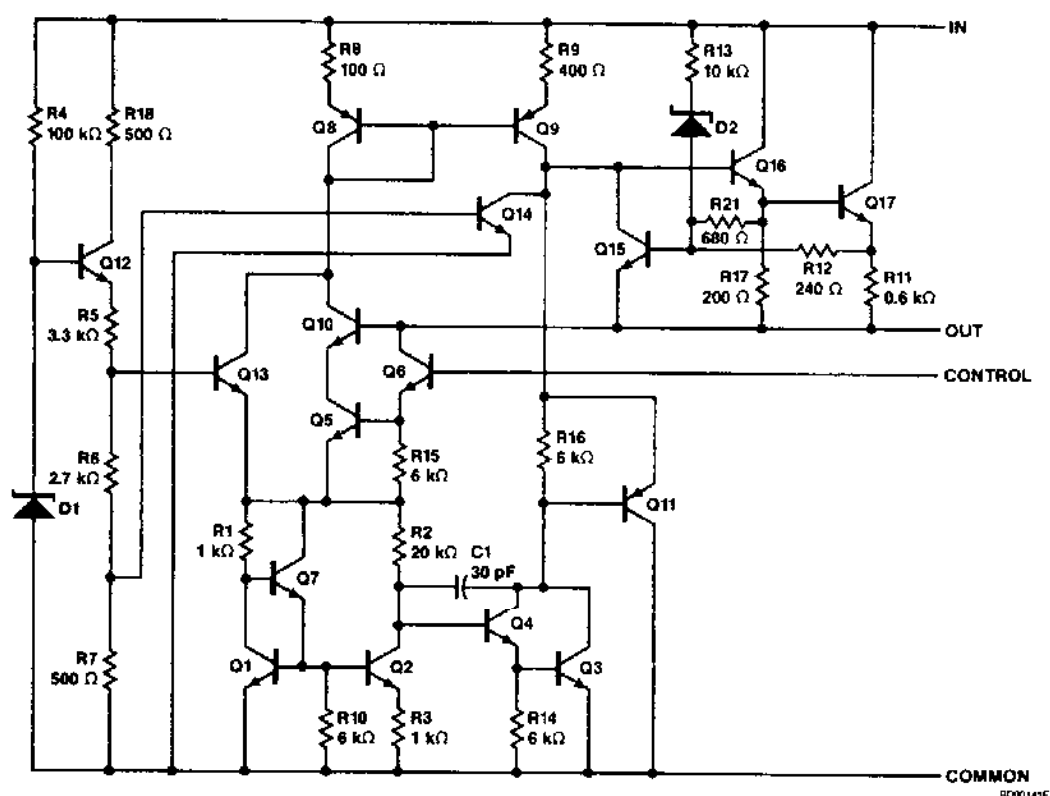
**Connection Diagram**  
 $\mu$ A79MG Power Watt  
 (Top View)


Heat sink tabs connected to input through device substrate. Not recommended for direct electrical connection.

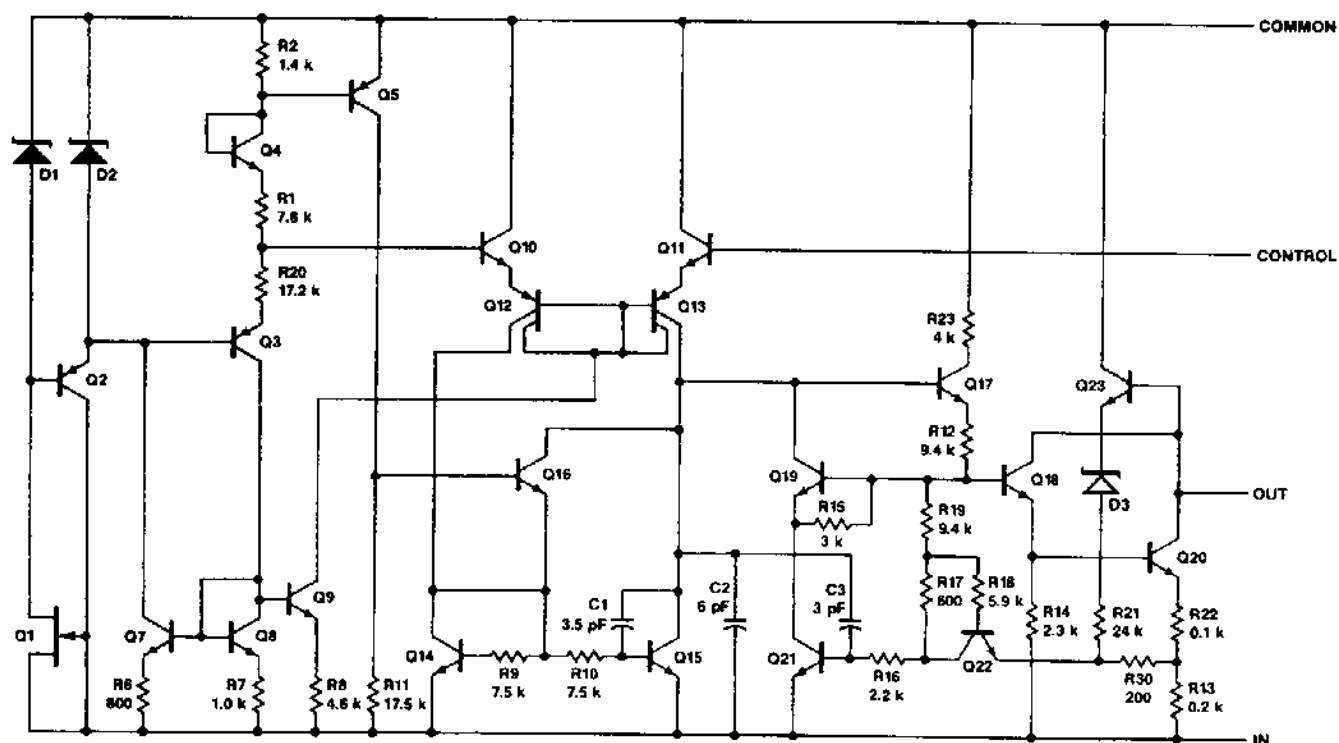
**Order Information**

Device Code	Package Code	Package Description
$\mu$ A79MGU1C	8Z	Molded Power Pack

### μA78MG Equivalent Circuit



**μA79MG Equivalent Circuit (Note 1)**



**Note**

†. Resistor values in  $\Omega$  unless otherwise noted.

# **μA78MG • μA79MG**

## **μA78MGC**

**Electrical Characteristics**  $0^{\circ}\text{C} \leq T_A \leq 125^{\circ}\text{C}$  for μA78MGC,  $V_I = 10\text{ V}$ ,  $I_O = 350\text{ mA}$ ,  $C_I = 0.33\text{ }\mu\text{F}$ ,  $C_O = 0.1\text{ }\mu\text{F}$ , Test Circuit 1, unless otherwise specified.

Symbol	Characteristic	Condition <sup>1,3</sup>	Min	Typ	Max	Unit
$V_{IR}$	Input Voltage Range	$T_J = 25^{\circ}\text{C}$	7.5		40	V
$V_{OR}$	Output Voltage Range	$V_I = V_O + 5.0\text{ V}$	5.0		30	V
$V_O$	Output Voltage Tolerance	$V_O + 3.0\text{ V} \leq V_I \leq V_O + 15\text{ V}$ , $5.0\text{ mA} \leq I_O \leq 350\text{ mA}$ , $P_D \leq 5.0\text{ W}$ , $V_{I\text{ Max}} = 38\text{ V}$			4.0	%( $V_O$ )
					5.0	
$V_{O\text{ LINE}}$	Line Regulation	$T_J = 25^{\circ}\text{C}$ , $I_O = 200\text{ mA}$ , $V_O \leq 10\text{ V}$ , $(V_O + 2.5\text{ V}) \leq V_I \leq (V_O + 20\text{ V})$ , $T_J = 25^{\circ}\text{C}$ , $I_O = 200\text{ mA}$ , $V_O \geq 10\text{ V}$			1.0	%( $V_O$ )
$V_{O\text{ LOAD}}$	Load Regulation	$T_J = 25^{\circ}\text{C}$ , $5.0\text{ mA} \leq I_O \leq 500\text{ mA}$ , $V_I = V_O + 7.0\text{ V}$			1.0	%( $V_O$ )
$I_C$	Control Lead Current	$T_J = 25^{\circ}\text{C}$		1.0	6.0	$\mu\text{A}$
					7.0	
$I_Q$	Quiescent Current	$T_J = 25^{\circ}\text{C}$		2.8	5.0	mA
					6.0	
RR	Ripple Rejection	$I_O = 125\text{ mA}$ , $8.0\text{ V} \leq V_I \leq 18\text{ V}$ , $V_O = 5.0\text{ V}$ , $f = 2400\text{ Hz}$	62	80		dB
$N_O$	Output Noise Voltage	$10\text{ Hz} \leq f \leq 100\text{ kHz}$ , $V_O = 5.0\text{ V}$		8	40	$\mu\text{V}/V_O$
$V_{DO}$	Dropout Voltage <sup>2</sup>			2	2.5	V
$I_{OS}$	Short Circuit Current	$V_I = 35\text{ V}$ , $T_J = 25^{\circ}\text{C}$			600	mA
$I_{pk}$	Peak Output Current	$T_J = 25^{\circ}\text{C}$	0.4	0.8	1.4	A
$\Delta V_O/\Delta T$	Average Temperature Coefficient of Output Voltage	$V_O = 5.0\text{ V}$ , $I_O = 5.0\text{ mA}$	$T_A = -55^{\circ}\text{C}$ to $+25^{\circ}\text{C}$		0.4	$\text{mV}/^{\circ}\text{C}/V_O$
			$T_A = 25^{\circ}\text{C}$ to $125^{\circ}\text{C}$		0.3	
$V_C$	Control Lead Voltage (Reference)	$T_J = 25^{\circ}\text{C}$	4.8	5.0	5.2	V
			4.75		5.25	

## μA78MG • μA79MG

### μA79MGC

**Electrical Characteristics**  $0^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$  for μA79MGC,  $V_I = -14\text{ V}$ ,  $I_O = 350\text{ mA}$ ,  $C_I = 2.0\text{ }\mu\text{F}$ ,  $C_O = 1.0\text{ }\mu\text{F}$ , Test Circuit 2, unless otherwise specified.

Symbol	Characteristic	Condition <sup>1,4,5</sup>	Min	Typ	Max	Unit
$V_{IR}$	Input Voltage Range	$T_J = 25^{\circ}\text{C}$	-40		-7.0	V
$V_{OR}$	Output Voltage Range	$V_I = V_O - 5.0\text{ V}$	-30		-2.23	V
$V_O$	Output Voltage Tolerance	$V_O - 15\text{ V} \leq V_I \leq V_O - 3.0\text{ V}$ , $5.0\text{ mA} \leq I_O \leq 350\text{ mA}$ , $P_D \leq 5.0\text{ W}$ , $V_{I\text{ Max}} = -38\text{ V}$	$T_J = 25^{\circ}\text{C}$		4.0	%( $V_O$ )
					5.0	
$V_{O\text{ LINE}}$	Line Regulation	$T_J = 25^{\circ}\text{C}$ , $I_O = 200\text{ mA}$ , $V_O \leq -10\text{ V}$ , $(V_O - 20\text{ V}) \leq V_I \leq (V_O - 2.5\text{ V})$ , $T_J = 25^{\circ}\text{C}$ , $I_O = 200\text{ mA}$ , $V_O \leq -10\text{ V}$			1.0	%( $V_O$ )
$V_{O\text{ LOAD}}$	Load Regulation	$V_I = V_O - 7.0\text{ V}$ , $5.0\text{ mA} \leq I_O \leq 500\text{ mA}$ , $T_J = 25^{\circ}\text{C}$			1.0	%( $V_O$ )
$I_C$	Control Lead Current	$T_J = 25^{\circ}\text{C}$			2.0	$\mu\text{A}$
					3.0	
$I_Q$	Quiescent Current	$T_J = 25^{\circ}\text{C}$		0.5	2.5	mA
					3.5	
RR	Ripple Rejection	$T_J = 25^{\circ}\text{C}$ , $I_O = 125\text{ mA}$ , $V_I = -13\text{ V}$ , $V_O = -5.0\text{ V}$ , $f = 2400\text{ Hz}$	50			dB
$N_O$	Noise	$10\text{ Hz} \leq f \leq 100\text{ kHz}$ , $V_O = -8.0\text{ V}$ , $I_L = 50\text{ mA}$		25	80	$\mu\text{V}/V_O$
$V_{DO}$	Dropout Voltage			1.1	2.3	V
$I_{OS}$	Short Circuit Current	$V_I = 35\text{ V}$ , $T_J = 25^{\circ}\text{C}$			600	mA
$I_{pk}$	Peak Output Current		0.4	0.65	1.4	mA
$\Delta V_O/\Delta T$	Average Temperature Coefficient of Output Voltage	$V_O = -5.0\text{ V}$ , $I_O = -5.0\text{ mA}$	$T_A = -55^{\circ}\text{C}$ to $+25^{\circ}\text{C}$		0.3	$\text{mV}/^{\circ}\text{C}/V_O$
			$T_A = 25^{\circ}\text{C}$ to $125^{\circ}\text{C}$		0.3	
$V_C$	Control Lead Voltage (Reference)	$T_J = 25^{\circ}\text{C}$	-2.32	-2.23	-2.14	V
			-2.35		-2.11	

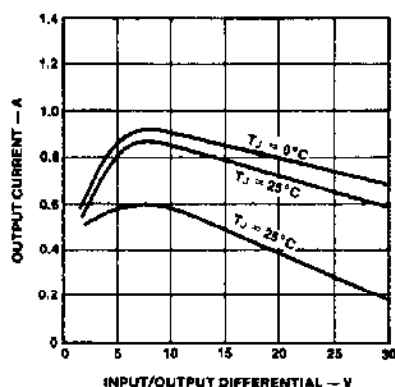
#### Notes

- $V_O$  is defined for the μA78MGC as  $V_O = \frac{R1 + R2}{R2} (5.0)$ . The μA79MGC as  $V_O = \frac{R1 + R2}{R2} (-2.23)$ .
- Dropout voltage is defined as that input/output voltage differential which causes the output voltage to decrease by 5% of its initial value.
- All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ( $t_W \leq 10\text{ ms}$ , duty cycle  $\leq 5\%$ ). Output voltage changes due to changes in internal temperature must be taken into account separately.
- The convention for negative regulators is the Algebraic value, thus  $-15\text{ V}$  is less than  $-10\text{ V}$ .
- All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ( $t_W \leq 10\text{ ms}$ , duty cycle  $\leq 5\%$ ). Output voltage changes due to changes in internal temperature must be taken into account separately.

# $\mu$ A78MG • $\mu$ A79MG

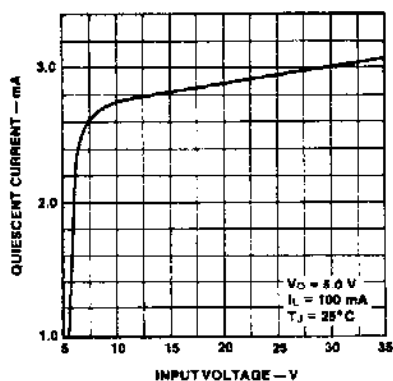
## Typical Performance Curves For $\mu$ A78MG

Peak Output Current vs  
Input/Output Differential Voltage



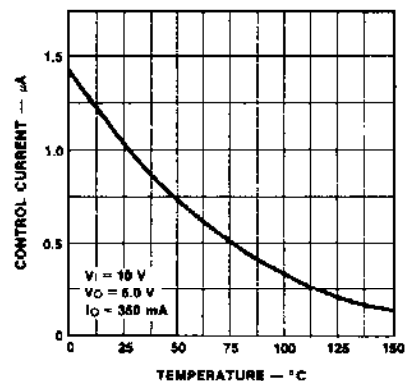
PC01481F

Quiescent Current vs  
Input Voltage



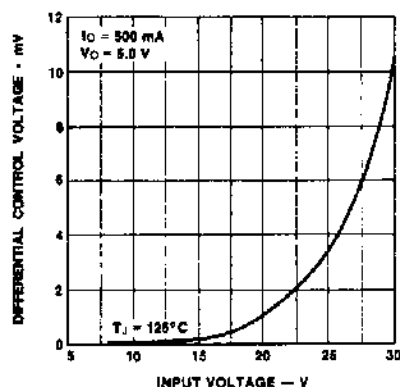
PC01491F

Control Current vs  
Temperature



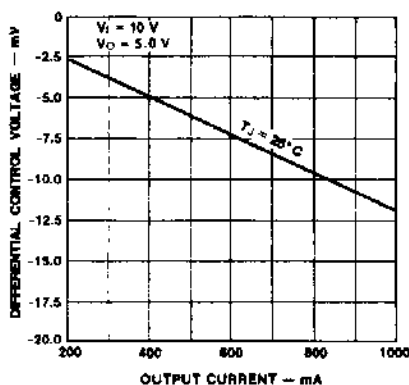
PC01501F

Differential Control Voltage vs  
Input Voltage



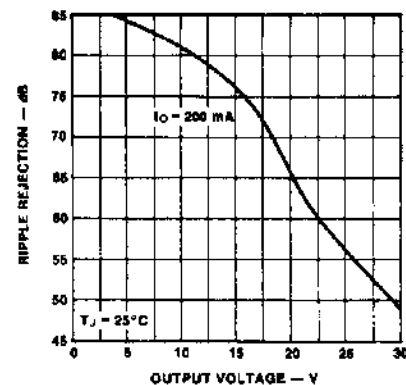
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Differential Control Voltage vs  
Output Current



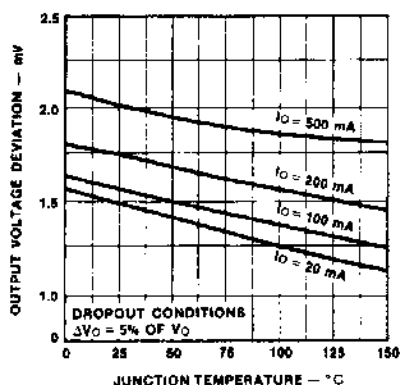
PC01521F

Ripple Rejection vs  
Output Voltage



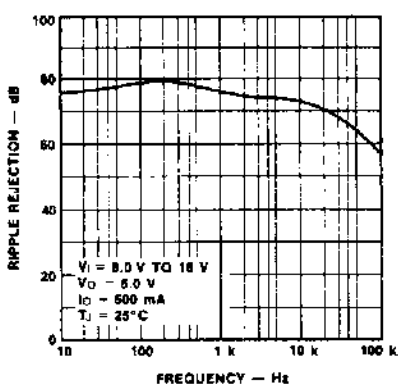
PC01531F

Dropout Voltage vs  
Junction Temperature



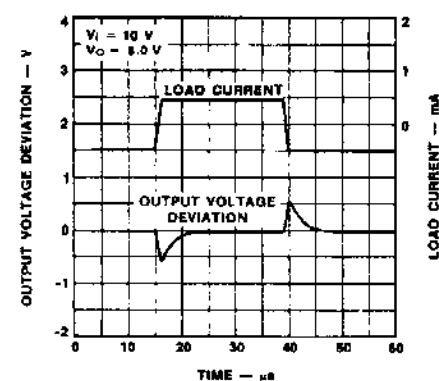
PC01541F

Ripple Rejection vs  
Frequency



PC01551F

Load Transient Response

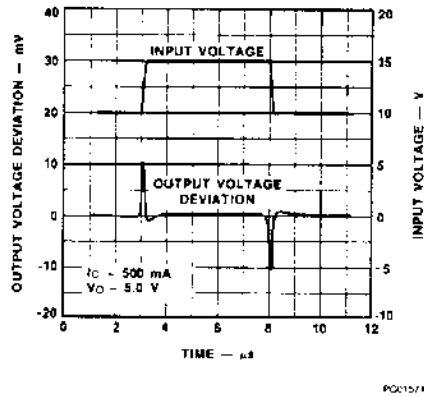


PC01561F

## $\mu$ A78MG • $\mu$ A79MG

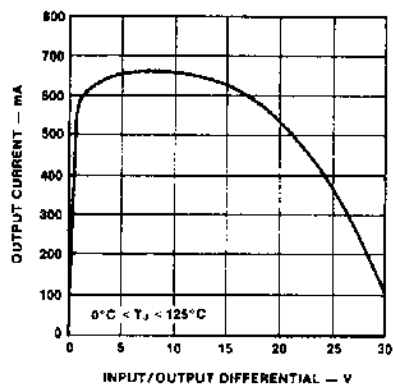
### Typical Performance Curves For $\mu$ A78MG (Cont.)

#### Line Transient Response

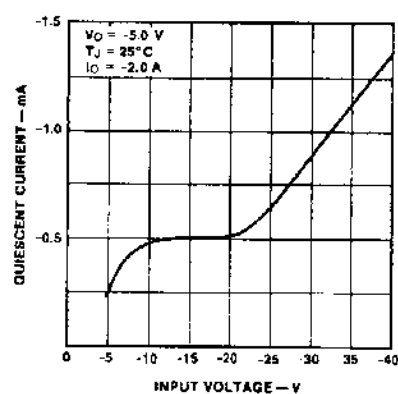


### Typical Performance Curves For $\mu$ A79MG

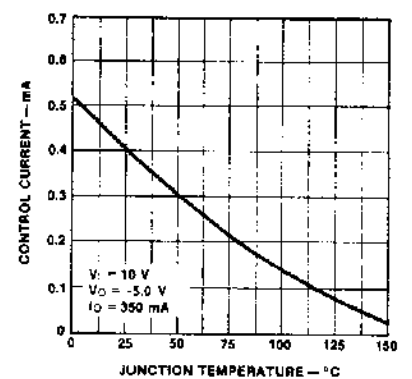
#### Peak Output Current vs Input/Output Differential Voltage



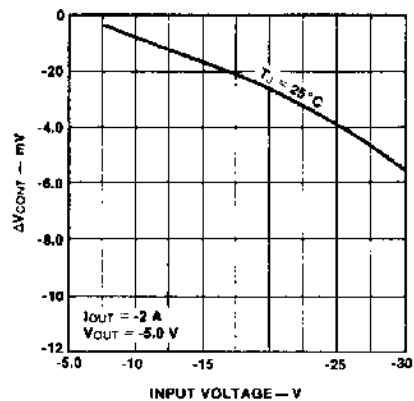
#### Quiescent Current vs Input Voltage



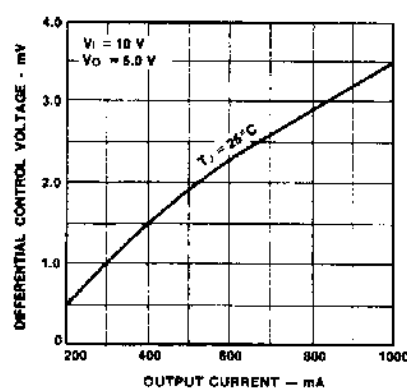
#### Control Current vs Temperature



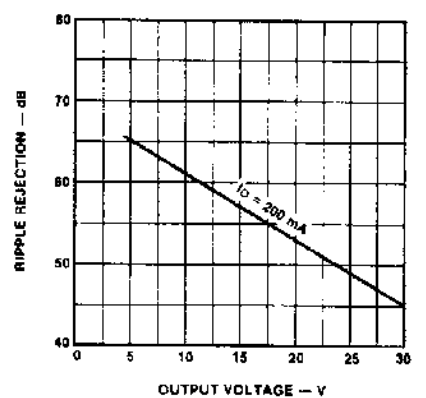
#### Differential Control Voltage vs Input Voltage



#### Differential Control Voltage vs Output Current



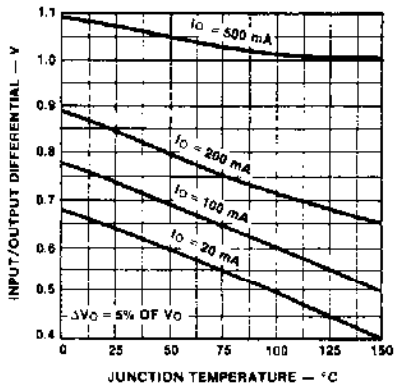
#### Ripple Rejection vs Output Voltage



## μA78MG • μA79MG

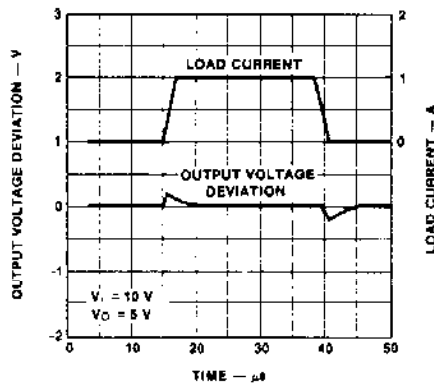
### Typical Performance Curves For μA79MG (Cont.)

**Dropout Voltage vs Junction Temperature**



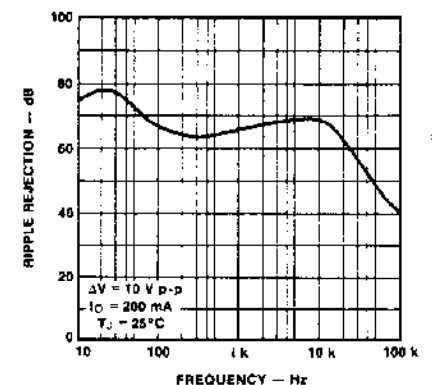
PC01641F

**Load Transient Response**



PC01601F

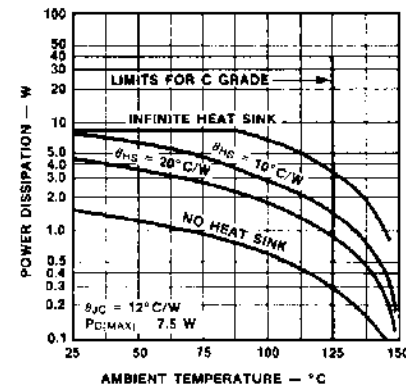
**Ripple Rejection vs Frequency**



PC01651F

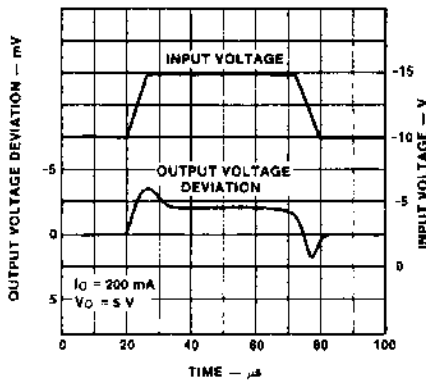
### Typical Performance Curve For μA78MG and μA79MG

**Worst Case Power Dissipation vs Ambient Temperature**



PC01680F

**Line Transient Response**



PC01671F

### Design Considerations

The μA78MG and μA79MG variable voltage regulators have an output voltage which varies from  $V_{CONT}$  to typically

$$V_I - 2.0 \text{ V by } V_O = V_{CONT} \frac{(R_1 + R_2)}{R_2}$$

The nominal reference in the μA78MG is 5.0 V and μA79MG is -2.23 V. If we allow 1.0 mA to flow in the control string to eliminate bias current effects, we can make  $R_2 = 5 \text{ k}\Omega$  in the μA78MG. The output voltage is then:  $V_O = (R_1 + R_2) \text{ Volts}$ , where  $R_1$  and  $R_2$  are in  $\text{k}\Omega$ s.

Example: If  $R_2 = 5.0 \text{ k}\Omega$  and  $R_1 = 10 \text{ k}\Omega$  then  $V_O = 15 \text{ V}$  nominal, for the μA78MG;  
If  $R_2 = 2.2 \text{ k}\Omega$  and  $R_1 = 12.8 \text{ k}\Omega$  then  $V_O = -15.2 \text{ V}$  nominal, for the μA79MG.

By proper wiring of the feedback resistors, load regulation of the devices can be improved significantly.

Both μA78MG and μA79MG regulators have thermal overload protection from excessive power, internal short circuit protection which limits each circuit's maximum current, and output transistor safe-area protection for reducing the

## μA78MG • μA79MG

output current as the voltage across each pass transistor is increased.

Although the internal power dissipation is limited, the junction temperature must be kept below the maximum specified temperature in order to meet data sheet specifications. To calculate the maximum junction temperature or heat sink required, the following thermal resistance values should be used:

Package	Typical $\theta_{JC}$	Max $\theta_{JC}$	Typical $\theta_{JA}$	Max $\theta_{JA}$
Power Watt	8.0	12.0	70	75

$$P_{D \text{ Max}} = \frac{T_{J \text{ Max}} - T_A}{\theta_{JC} + \theta_{CA}} \text{ or}$$

$$\frac{T_{J \text{ Max}} - T_A}{\theta_{JA}} \text{ (Without a heat sink)}$$

$$\theta_{CA} = \theta_{CS} + \theta_{SA}$$

Solving for  $T_J$ :

$$T_J = T_A + P_D(\theta_{JC} + \theta_{CA}) \text{ or}$$

$$T_A + P_D\theta_{JA} \text{ (Without heat sink)}$$

Where

- $T_J$  = Junction Temperature
- $T_A$  = Ambient Temperature
- $P_D$  = Power Dissipation
- $\theta_{JC}$  = Junction-to-case thermal resistance
- $\theta_{CA}$  = Case-to-ambient thermal resistance
- $\theta_{CS}$  = Case-to-heat sink thermal resistance
- $\theta_{SA}$  = Heat sink-to-ambient thermal resistance
- $\theta_{JA}$  = Junction-to-ambient thermal resistance

### Typical Applications for μA78MG (Note 1)

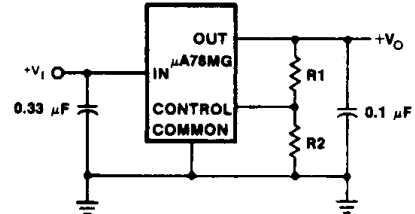
Bypass capacitors are recommended for stable operation of the μA78MG over the input voltage and output current ranges. Output bypass capacitors will improve the transient response of the regulator.

The bypass capacitors, (0.33 μF on the input, 0.1 μF on the output) should be ceramic or solid tantalum which have good high frequency characteristics. The bypass capacitors should be mounted with the shortest leads, and if possible, directly across the regulator terminals.

#### Note

1. All resistor values in ohms.

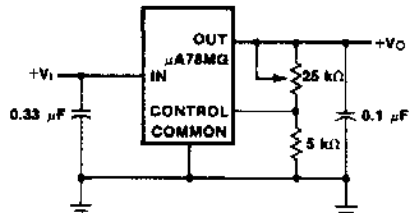
### Basic Positive Regulator



CR00191F

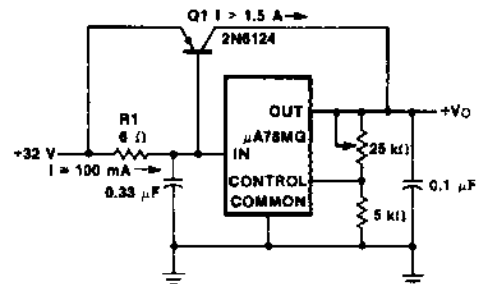
$$V_O = V_{CONT} \left( \frac{R_1 + R_2}{R_2} \right)$$

### Positive 5.0 V to 30 V Adjustable Regulator



CR00201F

### Positive 5.0 V to 30 V Adjustable Regulator $I_O > 1.5 \text{ A}$



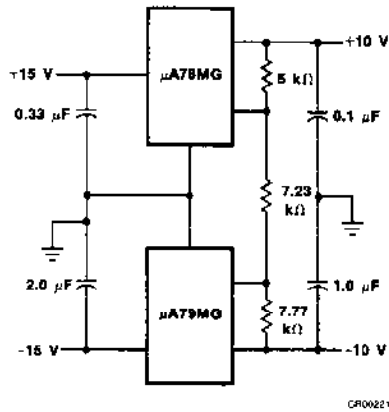
CR00211F

$$R_1 = \frac{\beta V_{BE(01)}}{I_{R \text{ Max}}(\beta) - I_O}$$



## Typical Applications for μA78MG (Note 1) (Cont.)

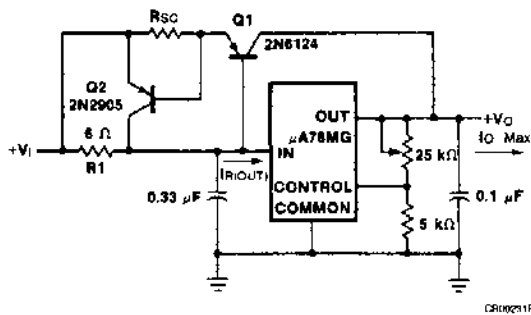
### ± 10 V, 500 mA Dual Tracking Regulator



#### Note

External series pass device is not short circuit protected.

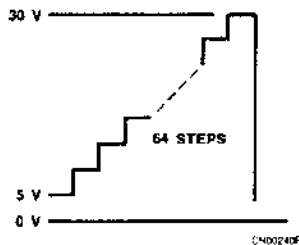
### Positive High Current Short Circuit Protected Regulator



$$R1 = \frac{\beta V_{BE}(Q1)}{V_R \text{ Max}(\beta + 1) - I_O \text{ Max}}$$

If load is not ground referenced, connect reverse biased diodes from outputs to ground.

### Output Waveform

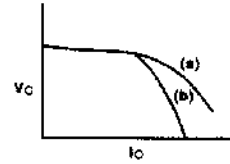
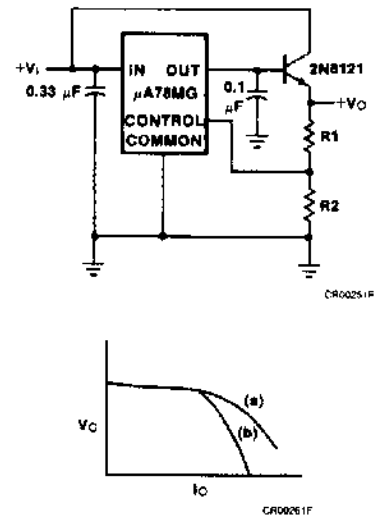


#### Note

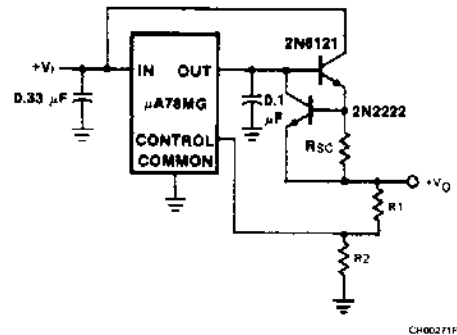
1. All resistor values in ohms.

## Positive High-Current Voltage Regulator

### External Series Pass (a)



### Short-Circuit Limit (b)



## Typical Applications for μA79MG (Note 1)

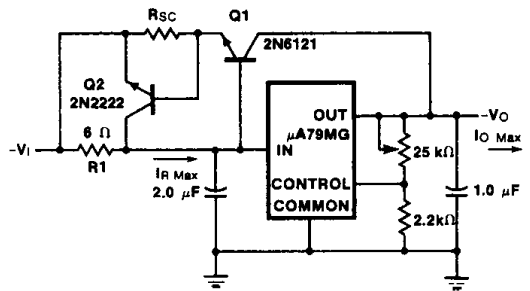
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The bypass capacitors, (2.0 μF on the input, 1.0 μF on the output) should be ceramic or solid tantalum which have good high frequency characteristics. If aluminum electrolytics are used, their values should be 10 μF or larger. The bypass capacitors should be mounted with the shortest leads, and if possible, directly across the regulator terminals.

## $\mu A78MG \bullet \mu A79MG$

### Typical Applications for $\mu A79MG$ (Note 1) (Cont.)

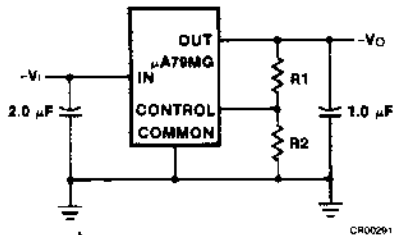
#### Negative High Current Short Circuit Protected Regulator



CR00291F

$$R1 = \frac{\beta V_{BE}(Q1)}{I_{R \text{ Max}}(\beta) - I_{O \text{ Max}}}$$

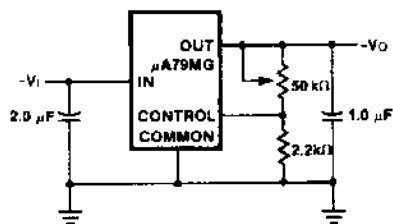
#### Basic Negative Regulator



CR00291F

$$V_O = -V_{CONT} \left( \frac{R1 + R2}{R2} \right)$$

#### -30 V to -2.2 V Adjustable Regulator

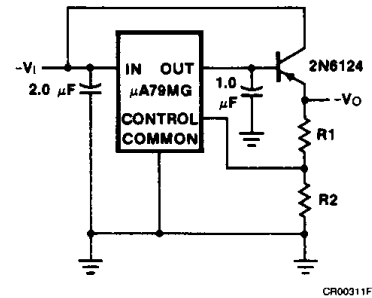


CR00301F

#### Note

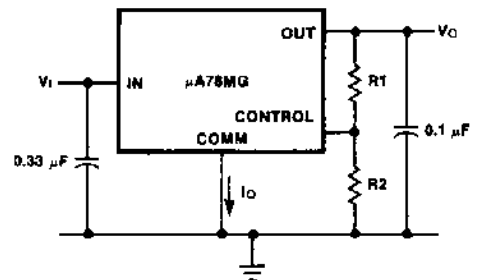
1. All resistor values in ohms.

#### Negative High Current Voltage Regulator External Series Pass



CR00311F

#### $\mu A78MG$ Test Circuit 1

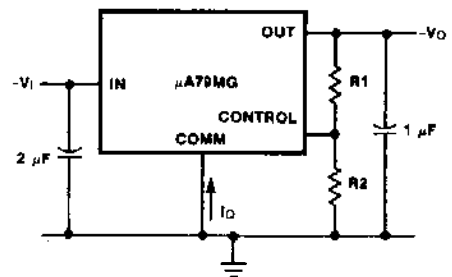


CR00321F

$$V_O = \left( \frac{R1 + R2}{R2} \right) V_{CONT}$$

$V_{CONT}$  Nominally = 5 V

#### $\mu A79MG$ Test Circuit 2



CR00331F

$$V_O = \left( \frac{R1 + R2}{R2} \right) V_{CONT}$$

$V_{CONT}$  Nominally = -2.23 V

Recommended R2 current  $\approx 1$  mA

$\therefore R2 = 5 \text{ k}\Omega$  ( $\mu A78MG$ )

$R2 = 2.2 \text{ k}\Omega$  ( $\mu A79MG$ )