

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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## Customer Notification

# **μPD789850 Subseries μPD789850A Subseries™**

**8-bit Single-Chip Microcontrollers**

**Operating Precautions**

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**μPD789850  
μPD78F9850  
μPD789850A  
μPD78F9850A**

Global Document No. U18096EE2V0IF00 (2nd edition)  
Document No. TPS-LE-OP-9850A-1 (2nd edition)  
Date Published March 2005

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μPD789850 and μPD789850A Subseries

**(A) Table of Operating Precautions**

No.	Outline	$\mu$ PD789850			$\mu$ PD78F9850		
		Rev.	MP		MP		
		Rank <sup>Note</sup>	all		all		
1	16-bit Timer TMO One-shot pulse output (Specification Change)	7			7		
2	DCAN Change of DCAN Controller Control Register names (Specification Change)	7			7		
3	DCAN High speed RX Loss and manipulated TX ID (Specification Change)	7			7		
4	DCAN REDEF Function (Direction of use)	7			7		
5	Flash Programming (Specification Change)	No Flash			7		
6	DCAN RXONLY Mode (Specification Change)	7			7		
7	DCAN Extended Identifier (Direction of use)	7			7		

3: Not applicable

7: applicable

**Note:** The rank is indicated by the letter appearing at the 5<sup>th</sup> position from the left in the lot number, marked on each product.

No.	Outline	$\mu$ PD789850A			$\mu$ PD78F9850A		
		Rev.	MP		MP		
		Rank <sup>Note</sup>	all		all		
1	16-bit Timer TM0 One-shot pulse output (Specification Change)	7			7		
2	DCAN Change of DCAN Controller Control Register names (Specification Change)	7			7		
4	DCAN REDEF Function (Direction of use)	7			7		
5	Flash Programming (Specification Change)	No Flash			7		
7	DCAN Extended Identifier (Specification Change)	7			7		

3: Not applicable

7: applicable

**Note:** The rank is indicated by the letter appearing at the 5<sup>th</sup> position from the left in the lot number, marked on each product.

**(B) Description of Operating Precautions**

No. 1	16-bit Timer TM0 One-shot pulse output (Specification Change)																																										
<p><u>Details</u> The One-shot pulse output function of 16-bit Timer TM0 is deleted.</p>																																											
No. 2	DCAN Change of DCAN Controller Control Register names (Specification Change)																																										
<p><u>Details</u></p>																																											
<table border="1"> <thead> <tr> <th>Address</th> <th>Old name</th> <th>New Name</th> </tr> </thead> <tbody> <tr> <td>FFB1H</td> <td>CANC0</td> <td>CANC</td> </tr> <tr> <td>FFB2H</td> <td>TCR0</td> <td>TCR</td> </tr> <tr> <td>FFB3H</td> <td>RMES0</td> <td>RMES</td> </tr> <tr> <td>FFB4H</td> <td>REDEF0</td> <td>REDEF</td> </tr> <tr> <td>FFB4H.7 (bit)</td> <td>DEFEN</td> <td>DEF</td> </tr> <tr> <td>FFB5H</td> <td>CANES0</td> <td>CANES</td> </tr> <tr> <td>FFB6H</td> <td>TEC0</td> <td>TEC</td> </tr> <tr> <td>FFB7H</td> <td>REC0</td> <td>REC</td> </tr> <tr> <td>FFB8H</td> <td>MCNT0</td> <td>MCNT</td> </tr> <tr> <td>FFB9H</td> <td>BRPRS0</td> <td>BRPRS</td> </tr> <tr> <td>FFBAH</td> <td>SYNC00</td> <td>SYNC0</td> </tr> <tr> <td>FFBBH</td> <td>SYNC01</td> <td>SYNC1</td> </tr> <tr> <td>FFBCH</td> <td>MASKC0</td> <td>MASKC</td> </tr> </tbody> </table>		Address	Old name	New Name	FFB1H	CANC0	CANC	FFB2H	TCR0	TCR	FFB3H	RMES0	RMES	FFB4H	REDEF0	REDEF	FFB4H.7 (bit)	DEFEN	DEF	FFB5H	CANES0	CANES	FFB6H	TEC0	TEC	FFB7H	REC0	REC	FFB8H	MCNT0	MCNT	FFB9H	BRPRS0	BRPRS	FFBAH	SYNC00	SYNC0	FFBBH	SYNC01	SYNC1	FFBCH	MASKC0	MASKC
Address	Old name	New Name																																									
FFB1H	CANC0	CANC																																									
FFB2H	TCR0	TCR																																									
FFB3H	RMES0	RMES																																									
FFB4H	REDEF0	REDEF																																									
FFB4H.7 (bit)	DEFEN	DEF																																									
FFB5H	CANES0	CANES																																									
FFB6H	TEC0	TEC																																									
FFB7H	REC0	REC																																									
FFB8H	MCNT0	MCNT																																									
FFB9H	BRPRS0	BRPRS																																									
FFBAH	SYNC00	SYNC0																																									
FFBBH	SYNC01	SYNC1																																									
FFBCH	MASKC0	MASKC																																									
No. 3	DCAN High speed RX Loss and manipulated TX ID (Specification Change)																																										
<p><u>Details</u> For detailed description, pls. refer to the document EACT-BR-5004-1.0.pdf or later.</p>																																											

No. 4	DCAN REDEF Function (Direction of use)
	<p><u>Details</u> Issue REDEF function only directly after 'bus idle' was detected. Use RXF and TXF bits in CAN Control Register CANC for this purpose and disable all interrupts during these operations. Alternatively the regular initialization mode can be used for re-configuration of the message buffer area or when REDEF was used to provide data consistency, this method needs to be replaced by the normal method using DN and MUC bit.</p> <p>For detailed description, pls. refer to the document EACT-BR-5006-1.0.pdf or later.</p>
No. 5	Flash Programming (Specification Change)
	<p><u>Details</u> When manipulating (erasing, writing) the flash memory of the target product using a flash programmer, the below listed communication conditions cannot longer be used.</p> <p>Communication mode: UART Communication speed: 4800Bd Operating frequency of target device fx: 8MHz</p>
No. 6	DCAN RXONLY Mode (Specification Change)
	<p><u>Details</u> The RYONLY Mode of the DCAN is deleted.</p> <p>For detailed description pls. refer to the document EACT-CN-5001-1.0.pdf or later.</p>
No. 7	DCAN Extended Identifier (Direction of use)
	<p><u>Details</u> Pls. use Extended Identifiers only, if it can be guaranteed, that there are no two Extended Identifiers available on the CAN bus, which are identical within their Standard Identifier part. Otherwise, the data-contents of messages with same Standard ID-part, but differing within the Extended ID-part, can be mixed, may be lost or wrong stored, while error frames or stuff bit errors occur on the CAN-bus within specific time-slots.</p> <p>For detailed description pls. refer to the document EACT-BR-5010-1.2 or later.</p>

**(C) Valid Specification**

<b>Item</b>	<b>Date published</b>	<b>Document No.</b>	<b>Document Title</b>
1	February 2003	U16532E or later	$\mu$ PD789850A Subseries User's Manual
2	August 2002	U14403E or later	$\mu$ PD789850 Subseries User's Manual

**(D) Revision History**

Item	Date published	Document No.	Comment
1	December 13, 2002	TPS-LE-OP-9850A	1 <sup>st</sup> Release
2	March 9, 2005	TPS-LE-OP-9850A-1	1 <sup>st</sup> Update Merging of documents TPS-LE-OP-9850-1 and TPS-LE-OP-9850A $\mu$ PD789850 Subseries: Revision of items 1 to 7 $\mu$ PD789850A Subseries: Revision of items 1 to 7