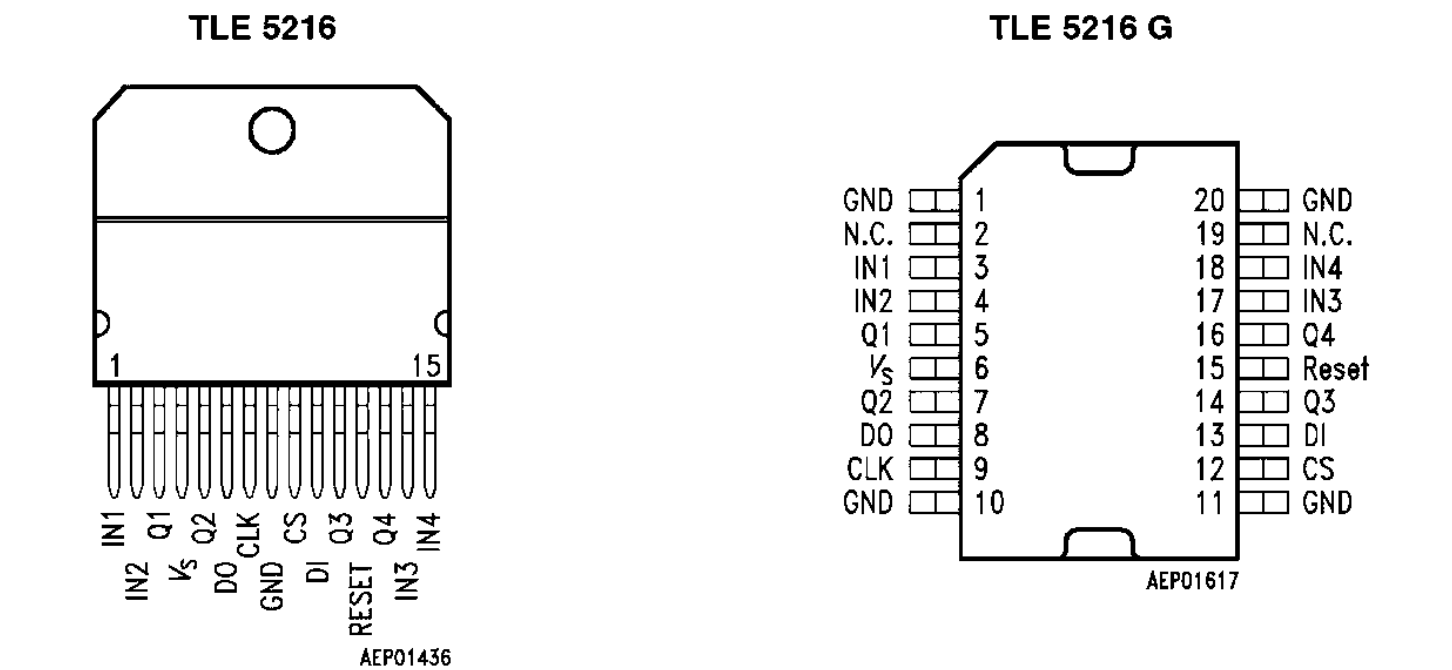


Pin Configuration
(top view)



Pin Definitions and Functions

Pin P-DSO- 20-10	Pin P-SIP- 15-1	Symbol	Function
3	1	IN1	Input switch 1; active low; internal pull up
4	2	IN2	Input switch 2; active low; internal pull up
5	3	Q1	Output switch 1; overload and shorted load protected
6	4	V_s	Supply voltage
7	5	Q2	Output switch 2; overload and shorted load protected
8	6	DO	Data out of serial diagnostic interface; open drain
9	7	CLK	Clock for serial diagnostic interface
1, 10, 11, 20	8	GND	Ground
12	9	CS	Chip select for serial diagnostic interface; internal pull up
13	10	DI	Data in of serial diagnostic interface; internal pull up
14	11	Q3	Output switch 3; overload and shorted load protected
15	12	Reset	Reset; active low; shuts down all outputs and resets the error flags
16	13	Q4	Output switch 4; overload and shorted load protected
17	14	IN3	Input switch 3; active low; internal pull up
18	15	IN4	Input switch 4; active low; internal pull up
2, 19		N.C.	Not connected