

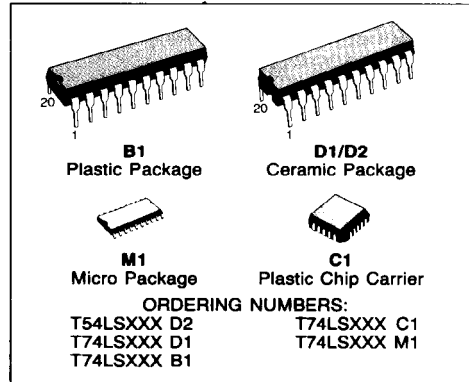


OCTAL BUFFER/LINE DRIVERS WITH 3-STATE OUTPUTS

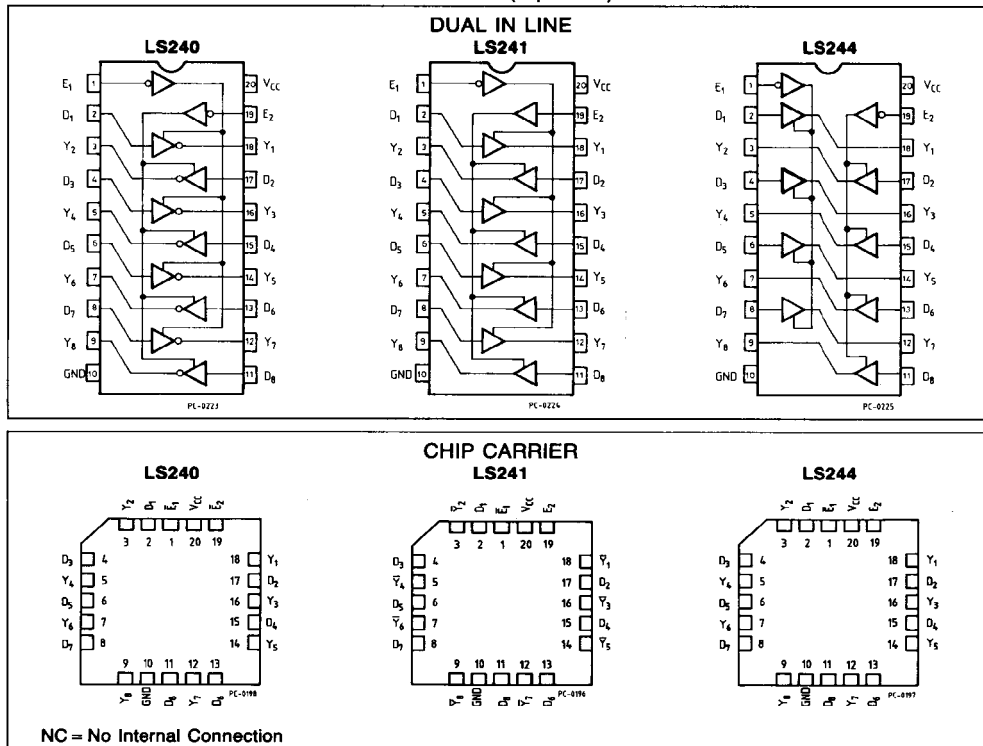
DESCRIPTION

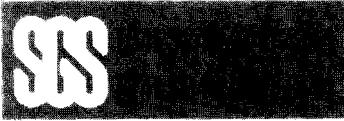
The T54LS/T74LS240/241/244 are Octal Buffers and Line Drivers. These devices are designed to be used with 3-state memory address drivers, etc. They are organized as two lines of 4-bit with inverting or non-inverting data.

- 3-STATE OUTPUTS DRIVE BUS LINES OR BUFFER MEMORY ADDRESS REGISTERS
- HYSTERESIS AT INPUTS TO IMPROVE NOISE MARGINS
- INPUT CLAMP DIODES LIMIT HIGH SPEED TERMINATION EFFECTS



LOGIC DIAGRAMS AND PIN CONNECTION (top view)





TRUTH TABLE

T54LS/T74LS240				T54LS/T74LS244			
INPUTS			OUTPUT	INPUTS			OUTPUT
\bar{E}_1	\bar{E}_2	D		\bar{E}_1	\bar{E}_2	D	
L	L	L	H	L	L	L	L
L	L	H	L	L	H	L	H
H	X	X	(Z)	H	X	X	(Z)

T54LS/T74LS241					
INPUTS		OUTPUT	INPUTS		OUTPUT
\bar{E}_1	D		\bar{E}_2	D	
L	L	L	H	L	L
L	H	H	H	H	H
H	X	(Z)	L	X	(Z)

H = HIGH Voltage Level, L = LOW Voltage Level, X = Don't Care, Z = HIGH Impedance

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CC}	Supply Voltage	-0.5 to 7	V
V_I	Input Voltage, Applied to Input	-0.5 to 15	V
V_O	Output Voltage, Applied to Output	0 to 10	V
I_I	Input Current, Into Inputs	-30 to 5	mA
I_O	Output Current, Into Outputs	50	mA

Stresses in excess of those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions in excess of those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

GUARANTEED OPERATING RANGES

Part Numbers	Supply Voltage			Temperature
	Min	Typ	Max	
T54LS240/241/244D2	4.5 V	5.0 V	5.5 V	-55°C to +125°C
T74LS240/241/244XX	4.75 V	5.0 V	5.25 V	0°C to +70°C

XX = package type.



DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE

Symbol	Parameter	Limits			Test Conditions (Note 1)	Units
		Min.	Typ.	Max.		
V _{IH}	Input HIGH Voltage	2.0			Guaranteed input HIGH Voltage for all Inputs	V
V _{IL}	Input LOW Voltage	54		0.7	Guaranteed input LOW Voltage for all Inputs	V
		74		0.8		
V _{CD}	Input Clamp Diode Voltage		-0.65	-1.5	V _{CC} = MIN, I _{IN} = -18mA	V
V _{OH}	Output HIGH Voltage	54,74	2.4	3.4	V _{CC} = MIN, I _{OH} = -3.0mA	V
		54,74	2.0		I _{OH} = -12mA for 54LS I _{OH} = -15mA for 74LS V _{CC} = MIN	
V _{OL}	Output LOW Voltage	54,74		0.25	I _{OL} = 12mA	V _{CC} = MIN, V _{IN} = V _{IL} or V _{IH} per Truth Table
		74		0.35	I _{OL} = 24mA	
V _{T+} - V _{T-}	Hysteresis	0.2	0.4		V _{CC} = MIN	V
I _{OZH}	Output Off Current HIGH			20	V _{CC} = MAX, V _{OUT} = 2.7V	μA
I _{OZL}	Output Off Current LOW			-20	V _{CC} = MAX, V _{OUT} = 0.4V	μA
I _{IH}	Input HIGH Current			20 0.1	V _{CC} = MAX, V _{IN} = 2.7V V _{CC} = MAX, V _{IN} = 7.0V	μA mA
I _{IL}	Input LOW Current			-0.2	V _{CC} = MAX, V _{IN} = 0.4V	mA
I _{OS}	Output Short Circuit Current (Note 2)	-40		-225	V _{CC} = MAX	mA
I _{CC}	Power Supply Current Total, Output HIGH			27	V _{CC} = MAX	mA
	Total, Out. LOW LS240			44		
	LS241/244			46		
	Total at HIGH Z LS240			50		
LS241/244			54			

AC CHARACTERISTICS: T_A = 25°C

Symbol	Parameter	Limits			Test Conditions	Units
		Min.	Typ.	Max.		
t _{PLH}	Propagation Delay, Data to Output LS240		9	14	C _L = 45pF R _L = 667Ω	ns
t _{PHL}			12	18		
t _{PLH}	Propagation Delay, Data to Output LS240/241/244		12	18		ns
t _{PHL}			12	18		
t _{PZH}	Output Enable Time to HIGH Level		15	23		ns
t _{PZL}	Output Enable Time to LOW Level		20	30		ns
t _{PLZ}	Output Disable Time from LOW Level		15	25	C _L = 5.0pF	ns
t _{PHZ}	Output Disable Time from HIGH Level		10	18		ns

Notes:

- 1) For conditions shown as MIN or MAX, use the appropriate value specified under guaranteed operating ranges.
- 2) Not more than one output should be shorted at a time.
- 3) Typical values are at V_{CC} = 5.0V, T_A = 25°C



AC WAVEFORMS

