

Analog Switches Product Information (Cont'd)

Drivers and Gates (Cont'd)

Basic Part No. (Notes 1 & 2)	Switch Type	r _{DS(on)} Max. (Ω) (Note 3)	Analog Voltage Range (p-p V) (Note 3)	Switching Time (μs)		Logic Input for ON Switch	Logic Levels (V)		Opt. Sup. Voltage (V)			Comments
				t _{ON}	t _{OFF}		V _{INL}	V _{INH}	(+) Sup. V ₁	(-) Sup. V ₂	Logic Sup. V ₁	
Eight Channel MUX + Enable (Cont'd)												
*DG508A	CMOS Plus-40	400	30	1.0	(Note 8)	(Note 7)	0.8	2.4	15	-15	—	
*DG528	CMOS Plus-40	400	30	1.0	(Note 8)	(Note 7)	0.8	2.4	15	-15	—	With Input Latches
SI3705	PMOS	150-400	10	1.5	(Note 8)	(Note 7)	0.6	3.5	5	-20	—	See DG501/No Pullup Resistors
Sixteen Channel MUX + Enable												
DG506	CMOS	400	30	1.0	(Note 8)	(Note 7)	0.8	2.4	15	-15	—	Break-Before-Make
*DG506A	CMOS Plus-40	400	30	1.0	(Note 8)	(Note 7)	0.8	2.4	15	-15	—	
Four Channel Differential MUX												
DG509	CMOS	400	30	1.0	(Note 8)	(Note 7)	0.8	2.4	15	-15	—	Break-Before-Make
*DG509A	CMOS Plus-40	400	30	1.0	(Note 8)	(Note 7)	0.8	2.4	15	-15	—	
*DG529	CMOS Plus-40	400	30	1.0	(Note 8)	(Note 7)	0.8	2.4	15	-15	—	With Input Latches
Eight Channel Differential MUX + Enable												
DG507	CMOS	400	30	1.0	(Note 8)	(Note 7)	0.8	2.4	15	-15	—	Break-Before-Make
*DG507A	CMOS Plus-40	400	30	1.0	(Note 8)	(Note 7)	0.8	2.4	15	-15	—	
Four Channel SPDT D/A Converter Summing Node Switches												
DG515	NMOS	See Comments	—	0.120	0.170	(Note 5)	0.5	7.5	8.0	0	—	R ₁ = 6.25Ω, R ₂ = 12.5Ω, R ₃ = 25Ω, R ₄ = 50Ω
Ten Channel SPDT D/A Converter Summing Node Switches												
DG516	NMOS	See Comments	—	0.120	0.170	(Note 5)	0.5	7.5	8.0	0	—	R ₁ = 100Ω, R ₂ = 200Ω, R ₃ = 400Ω, R ₄ = 800Ω, R ₅ = 1600Ω, R ₆₋₁₀ = 3200Ω

Multiple FET Switches

Siliconix P-Channel MOSFET & DMOS Switches are available for such applications as sequential switching (commutation), signal processing, modulation, and A-to-D conversion. The MOSFET is normally OFF. These devices are also available with Siliconix drivers in a single package.

Basic Part Number (Note 2)	Circuit Function			Pull Up On Gate	r _{DSMax.} (V)		BV _{DSS}	I _{S(off)} (nA)	V _{GS(th)}		C _{gs} Typ. (pF)	C _{ds} Typ. (pF)	C _{sb} Typ. (pF)	
	S	D	R		@ V _S = +10V	@ V _S = -10V			Min.	Max.				
G115	6	1	6	SP6T	Yes	100	450	-30	0.5	-1.5	-4.0	0.9	0.4	2
G118	6	1	8	SP6T	No	100	450	-30	0.5	-1.5	-4.0	0.9	0.4	2
G119	6	2	3	DP3T	Yes	100	450	-30	0.5	-1.5	-4.0	1.8	0.4	2
G122	4	2	2	DPDT	Yes	100	450	-30	0.5	-1.5	-4.0	1.8	0.4	2
G123	4	2	4	2x SPDT	Yes	100	450	-30	0.5	-1.5	-4.0	1.8	0.4	2
*SD5000	4	4	4	4x SPST	No	50	20	10.0	0.1	2.0	3.5	0.5	4	
*SD5001	4	4	4	4x SPST	No	50	10	10.0	0.1	2.0	3.5	0.5	4	
*SD5002	4	4	4	4x SPST	No	50	15	10.0	0.1	2.0	3.5	0.5	4	
*SD5200	4	4	4	4x SPST	No	80	30	1000	0.5	2.0	3.5	0.5	4	

* Devices recommended for new designs are indicated in bold face type.