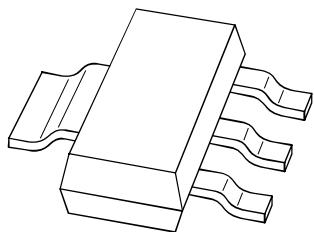


# **DATA SHEET**



## **PZT3904**

### **NPN switching transistor**

Product specification  
Supersedes data of 1997 Jul 04

1999 Apr 14

**NPN switching transistor****PZT3904****FEATURES**

- Low current (max. 200 mA)
- Low voltage (max. 40 V).

**APPLICATIONS**

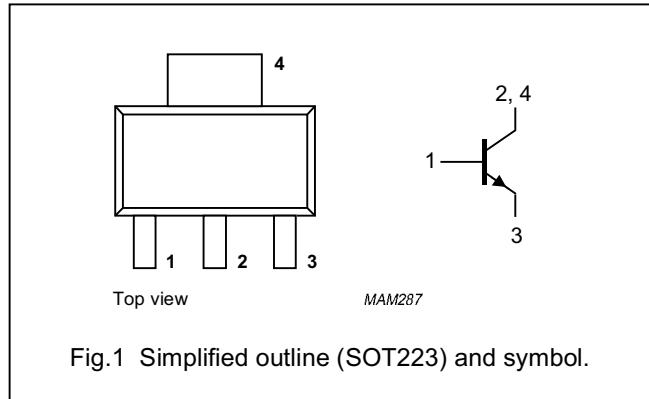
- High-speed saturated switching.

**DESCRIPTION**

NPN switching transistor in a SOT223 plastic package.  
PNP complement: PZT3906.

**PINNING**

PIN	DESCRIPTION
1	base
2, 4	collector
3	emitter

**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	–	60	V
$V_{CEO}$	collector-emitter voltage	open base	–	40	V
$V_{EBO}$	emitter-base voltage	open collector	–	6	V
$I_C$	collector current (DC)		–	200	mA
$I_{CM}$	peak collector current		–	300	mA
$I_{BM}$	peak base current		–	100	mA
$P_{tot}$	total power dissipation	$T_{amb} \leq 25^\circ\text{C}$ ; note 1	–	1.05	W
$T_{stg}$	storage temperature		–65	+150	°C
$T_j$	junction temperature		–	150	°C
$T_{amb}$	operating ambient temperature		–65	+150	°C

**Note**

1. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>.  
For other mounting conditions, see "Thermal considerations for SOT223 in the General Part of associated Handbook".

## NPN switching transistor

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## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	117	K/W
$R_{th\ j-s}$	thermal resistance from junction to soldering point		36	K/W

## Note

1. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>.  
For other mounting conditions, see "Thermal considerations for SOT223 in the General Part of associated Handbook".

## CHARACTERISTICS

 $T_j = 25^\circ\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$I_{CBO}$	collector cut-off current	$I_E = 0$ ; $V_{CB} = 30$ V	–	50	nA
$I_{EBO}$	emitter cut-off current	$I_C = 0$ ; $V_{EB} = 6$ V	–	50	nA
$h_{FE}$	DC current gain	$V_{CE} = 1$ V; (see Fig.2) $I_C = 0.1$ mA $I_C = 1$ mA $I_C = 10$ mA $I_C = 50$ mA $I_C = 100$ mA	60 80 100 60 30	– – 300 – –	
$V_{CEsat}$	saturation voltage	$I_C = 10$ mA; $I_B = 1$ mA	–	200	mV
		$I_C = 50$ mA; $I_B = 5$ mA	–	200	mV
$V_{BEsat}$	saturation voltage	$I_C = 10$ mA; $I_B = 1$ mA	–	850	mV
		$I_C = 50$ mA; $I_B = 5$ mA	–	950	mV
$C_c$	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 5$ V; $f = 1$ MHz	–	4	pF
$C_e$	emitter capacitance	$I_C = i_c = 0$ ; $V_{EB} = 500$ mV; $f = 1$ MHz	–	8	pF
$f_T$	transition frequency	$I_C = 10$ mA; $V_{CE} = 20$ V; $f = 100$ MHz	300	–	MHz
$F$	noise figure	$I_C = 100$ $\mu$ A; $V_{CE} = 5$ V; $R_S = 1$ k $\Omega$ ; $f = 10$ Hz to 15.7 kHz	–	5	dB

## Switching times (between 10% and 90% levels); (see Fig.3)

$t_{on}$	turn-on time	$I_{Con} = 10$ mA; $I_{Bon} = 1$ mA; $I_{Boff} = -1$ mA	–	65	ns
$t_d$	delay time		–	35	ns
$t_r$	rise time		–	35	ns
$t_{off}$	turn-off time		–	240	ns
$t_s$	storage time		–	200	ns
$t_f$	fall time		–	50	ns

## NPN switching transistor

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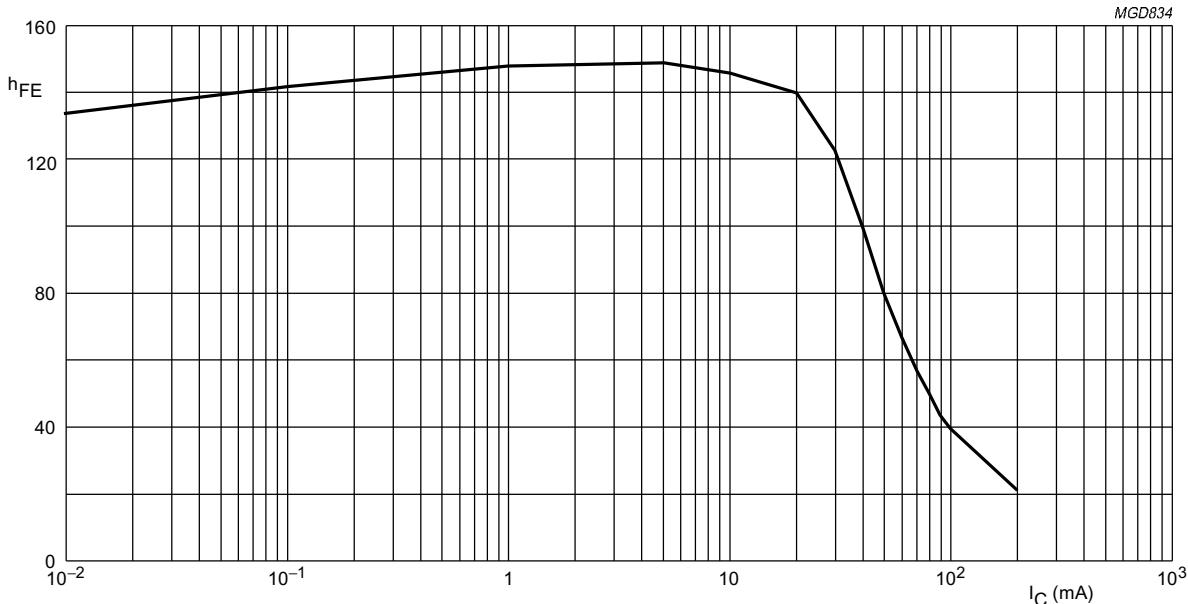
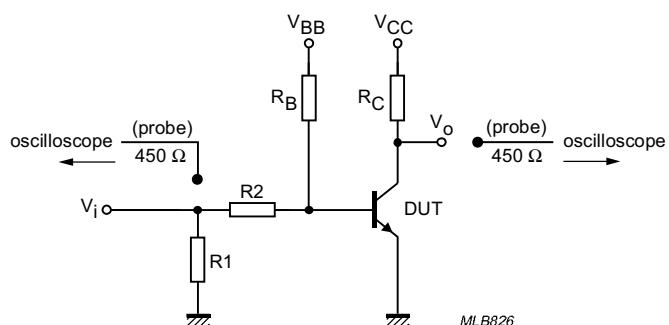
 $V_{CE} = 1$  V.

Fig.2 DC current gain; typical values.



$V_i = 5$  V;  $T = 500$   $\mu$ s;  $t_p = 10$   $\mu$ s;  $t_r = t_f \leq 3$  ns.  
 R1 = 56  $\Omega$ ; R2 = 2.5 k $\Omega$ ; R<sub>B</sub> = 3.9 k $\Omega$ ; R<sub>C</sub> = 270  $\Omega$ .  
 $V_{BB} = -1.9$  V;  $V_{CC} = 3$  V.  
 Oscilloscope input impedance  $Z_i = 50 \Omega$ .

Fig.3 Test circuit for switching times.

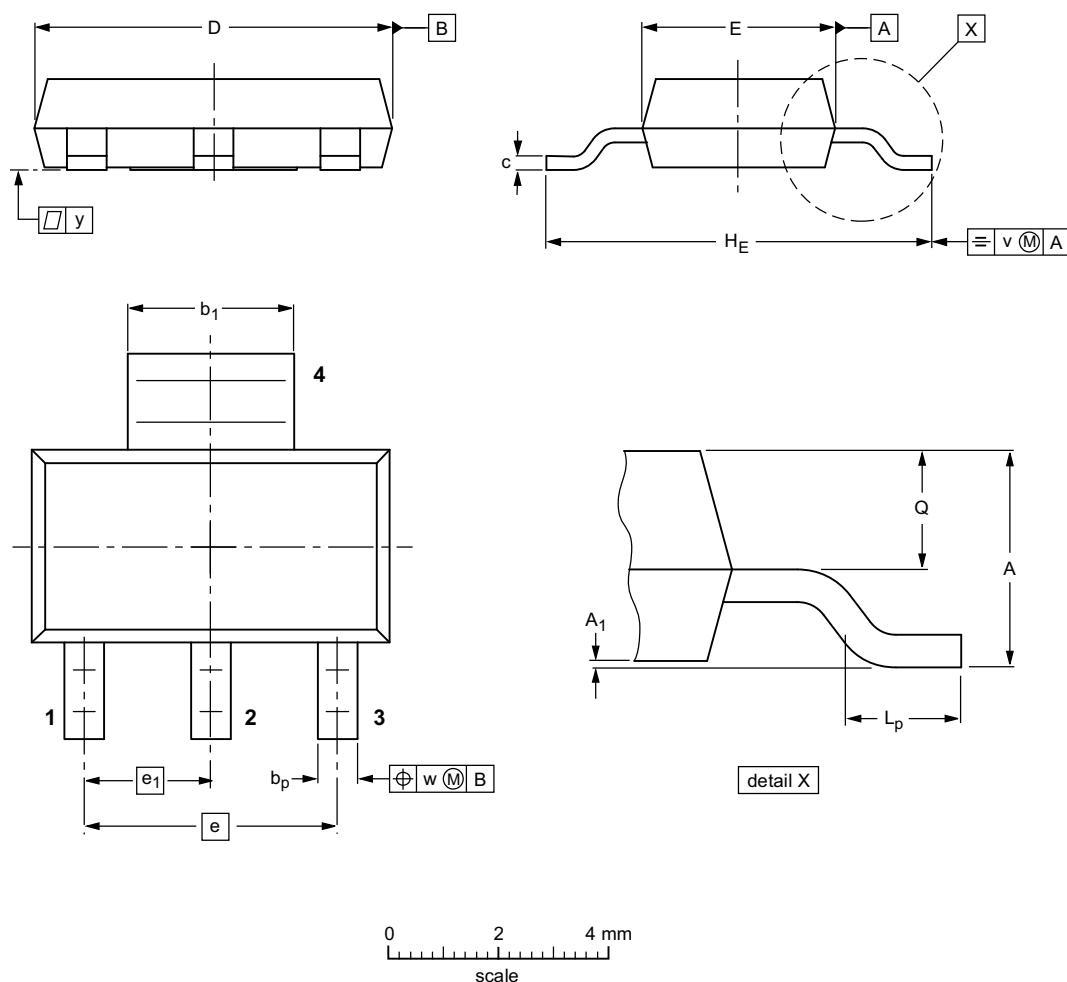
## NPN switching transistor

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## PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 4 leads

SOT223



DIMENSIONS (mm are the original dimensions)

UNIT	A	A <sub>1</sub>	b <sub>p</sub>	b <sub>1</sub>	c	D	E	e	e <sub>1</sub>	H <sub>E</sub>	L <sub>p</sub>	Q	v	w	y
mm	1.8 1.5	0.10 0.01	0.80 0.60	3.1 2.9	0.32 0.22	6.7 6.3	3.7 3.3	4.6	2.3	7.3 6.7	1.1 0.7	0.95 0.85	0.2	0.1	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT223						-96-11-11 97-02-28

**NPN switching transistor****PZT3904****DEFINITIONS**

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

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NPN switching transistor

PZT3904

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Printed in The Netherlands

115002/00/03/pp8

Date of release: 1999 Apr 14

Document order number: 9397 750 05639

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