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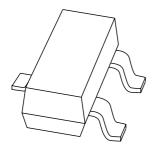
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Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



PMMT591A PNP BISS transistor

Product data sheet Supersedes data of 2001 Jun 11 2004 Jan 13



NXP Semiconductors Product data sheet

PNP BISS transistor

PMMT591A

FEATURES

- High current (max. 1 A)
- Low collector-emitter saturation voltage ensures reduced power consumption.

APPLICATIONS

• Battery powered units where high current and low power consumption are important.

DESCRIPTION

PNP BISS (Breakthrough In Small Signal) transistor in a SOT23 plastic package. NPN complement: PMMT491A.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
PMMT591A	9B*

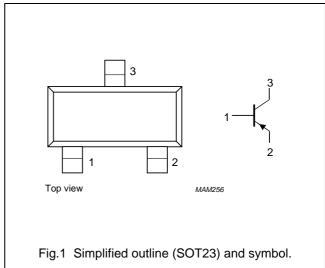
Note

* = p : Made in Hong Kong. * = t : Made in Malaysia.

* = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE	PACKAGE		
NUMBER	NAME	DESCRIPTION	VERSION
PMMT591A	_	plastic surface mounted package; 3 leads	SOT23

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-40	V
V _{CEO}	collector-emitter voltage	open base	_	-40	V
V _{EBO}	emitter-base voltage	open collector	_	-5	V
I _C	collector current (DC)		_	-1	Α
I _{CM}	peak collector current		_	-2	Α
I _{BM}	peak base current		_	-1	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

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PNP BISS transistor

PMMT591A

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = -30 V	_	-100	nA
I _{CEO}	collector cut-off current	$I_B = 0$; $V_{CE} = -30 \text{ V}$	_	-100	nA
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = -5 V$	_	-100	nA
h _{FE}	DC current gain	V _{CE} = −5 V; note 1			
		$I_C = -1 \text{ mA}$	300	_	
		$I_{C} = -100 \text{ mA}$	300	800	
		$I_{C} = -500 \text{ mA}$	250	_	
		$I_C = -1 A$	160	_	
V _{CEsat}	collector-emitter saturation voltage	note 1			
		$I_C = -100 \text{ mA}; I_B = -1 \text{ mA}$	_	-200	mV
		$I_C = -500 \text{ mA}; I_B = -20 \text{ mA}$	_	-350	mV
		$I_C = -1 \text{ A}; I_B = -100 \text{ mA}$	_	-500	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = -1 \text{ A}$; $I_B = -50 \text{ mA}$; note 1	_	-1.1	٧
V_{BE}	base-emitter voltage	$V_{CE} = -5 \text{ V}; I_{C} = -1 \text{ A}; \text{ note 1}$	-	-1	٧
C _c	collector capacitance	$I_E = I_e = 0$; $V_{CB} = -10 \text{ V}$; $f = 1 \text{ MHz}$	_	12	pF
f _T	transition frequency	$I_C = -50 \text{ mA}; V_{CE} = -10 \text{ V}; f = 100 \text{ MHz}$	150	-	MHz

Note

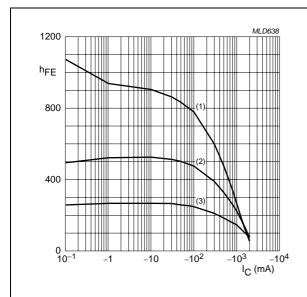
1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

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PNP BISS transistor

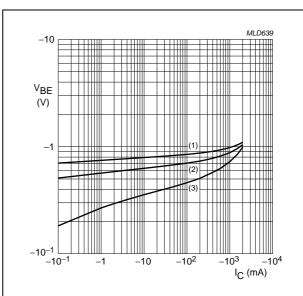
PMMT591A



 $V_{CE} = -5 \text{ V}.$

- (1) $T_{amb} = 150 \, ^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

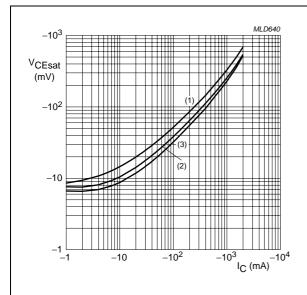
Fig.2 DC current gain as a function of collector current; typical values.



 $V_{CE} = -5 \text{ V}.$

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) T_{amb} = 150 °C.

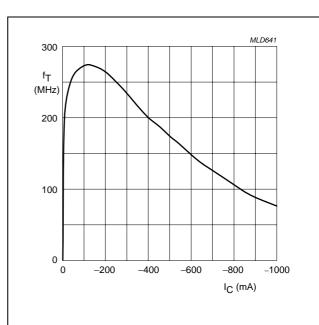
Fig.3 Base-emitter voltage as a function of collector current; typical values.



 $I_{\rm C}/I_{\rm B}=10.$

- (1) T_{amb} = 150 °C.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

Fig.4 Collector-emitter saturation voltage as a function of collector current; typical values.



 $V_{CE} = -10 \text{ V}.$

Fig.5 Transition frequency as a function of collector current; typical values.

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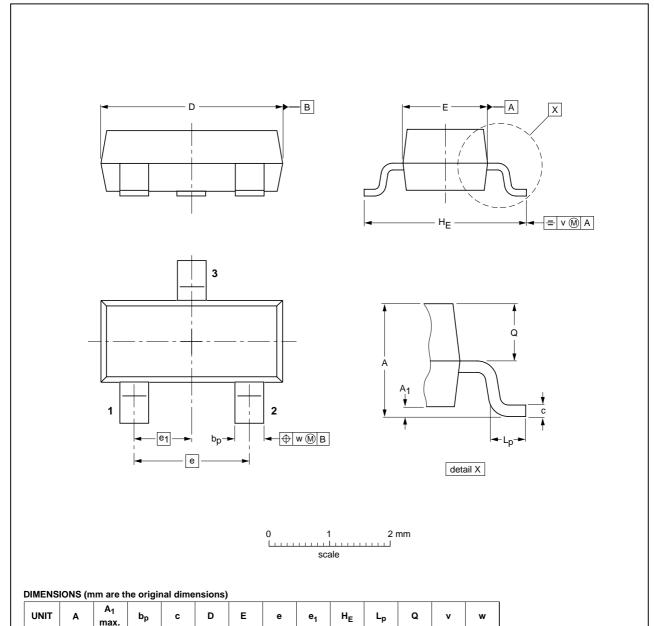
PNP BISS transistor

PMMT591A

PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	REFERENCES		EUROPEAN	ICCUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04- 06-03-16

0.95

1.9

0.45

0.55

0.2

0.1

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max

0.48

0.38

0.15

1.1

0.9

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PNP BISS transistor

PMMT591A

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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Printed in The Netherlands R75/05/pp7 Date of release: 2004 Jan 13 Document order number: 9397 750 12529

