

MOS FIELD EFFECT TRANSISTOR NP55N06CLD, NP55N06DLD, NP55N06ELD

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

This product is N-Channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Channel Temperature 175 degree rated
- Super Low On-state Resistance
 $R_{DS(on)1} = 17 \text{ m}\Omega \text{ (MAX.) (} V_{GS} = 10 \text{ V, } I_D = 28 \text{ A)}$
 $R_{DS(on)2} = 23 \text{ m}\Omega \text{ (MAX.) (} V_{GS} = 5 \text{ V, } I_D = 28 \text{ A)}$
- Low C_{iss} : $C_{iss} = 1920 \text{ pF (TYP.)}$
- Built-in Gate protection diode

ORDERING INFORMATION

PART NUMBER	PACKAGE
NP55N06CLD	TO-220AB
NP55N06DLD	TO-262
NP55N06ELD	TO-263

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Drain to Source Voltage	V_{DSS}	60	V
Gate to Source Voltage	V_{GSS}	± 20	V
Drain Current (DC)	$I_{D(DC)}$	± 55	A
Drain Current (Pulse) ^{Note1}	$I_{D(pulse)}$	± 165	A
Total Power Dissipation ($T_A = 25^\circ\text{C}$)	P_T	1.8	W
Total Power Dissipation ($T_{ch} = 25^\circ\text{C}$)	P_T	77	W
Single Avalanche Current	I_{AS}	TBD	A
Single Avalanche Energy ^{Note2}	E_{AS}	TBD	mJ
Channel Temperature	T_{ch}	175	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to + 175	$^\circ\text{C}$

Notes 1. $PW \leq 10 \mu\text{s}$, Duty cycle $\leq 1\%$

2. Starting $T_{ch} = 25^\circ\text{C}$, $R_G = 25 \Omega$, $V_{GS} = 20 \text{ V} \rightarrow 0$

THERMAL RESISTANCE

Channel to Case	$R_{th(ch-C)}$	1.95	$^\circ\text{C/W}$
Channel to Ambient	$R_{th(ch-A)}$	83.3	$^\circ\text{C/W}$

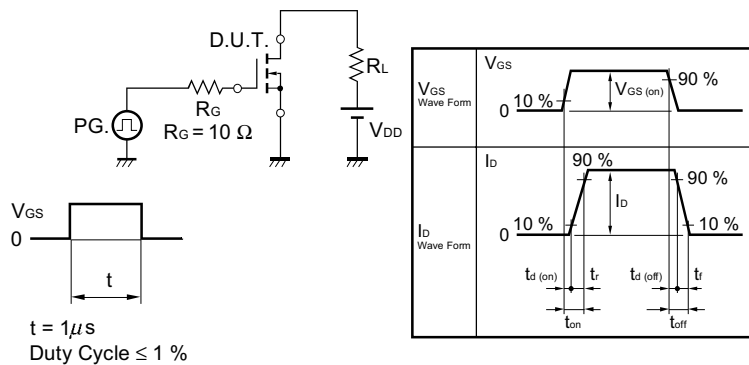
The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

The information contained in this document is being issued in advance of the production cycle for the device. The parameters for the device may change before final production or NEC Corporation, at its own discretion, may withdraw the device prior to its production.

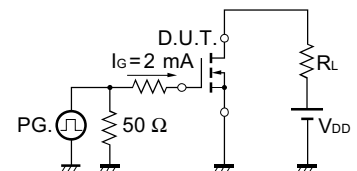
ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain to Source On-state Resistance	R _{DS(on)1}	V _{GS} = 10 V, I _D = 28 A		13	17	mΩ
	R _{DS(on)2}	V _{GS} = 5 V, I _D = 28 A		17	23	mΩ
	R _{DS(on)3}	V _{GS} = 4 V, I _D = 28 A		20	27	mΩ
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 250 μA	1.0	1.5	2.0	V
Forward Transfer Admittance	y _{fs}	V _{DS} = 10 V, I _D = 28 A	13	42		S
Drain Leakage Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V			10	μA
Gate to Source Leakage Current	I _{GSS}	V _{GS} = ±20 V, V _{DS} = 0 V			±10	μA
Input Capacitance	C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		1920	2880	pF
Output Capacitance	C _{oss}			380	570	pF
Reverse Transfer Capacitance	C _{rss}			140	250	pF
Turn-on Delay Time	t _{d(on)}	I _D = 28 A, V _{GS(on)} = 10 V, V _{DD} = 30 V, R _G = 10 Ω		35	80	ns
Rise Time	t _r			440	1100	ns
Turn-off Delay Time	t _{d(off)}			110	220	ns
Fall Time	t _f			230	500	ns
Total Gate Charge 1	Q _{G1}	I _D = 55 A, V _{DD} = 48 V, V _{GS} = 10 V		44	66	nC
Total Gate Charge 2	Q _{G2}	I _D = 55 A, V _{DD} = 48 V, V _{GS} = 5 V		25	38	nC
Gate to Source Charge	Q _{GS}			8		nC
Gate to Drain Charge	Q _{GD}			13		nC
Body Diode Forward Voltage	V _{F(S-D)}	I _F = 55 A, V _{GS} = 0 V		1.0		V
Reverse Recovery Time	t _{rr}	I _F = 55 A, V _{GS} = 0 V, di/dt = 100 A/μs		60		ns
Reverse Recovery Charge	Q _{rr}			100		nC

TEST CIRCUIT 1 SWITCHING TIME

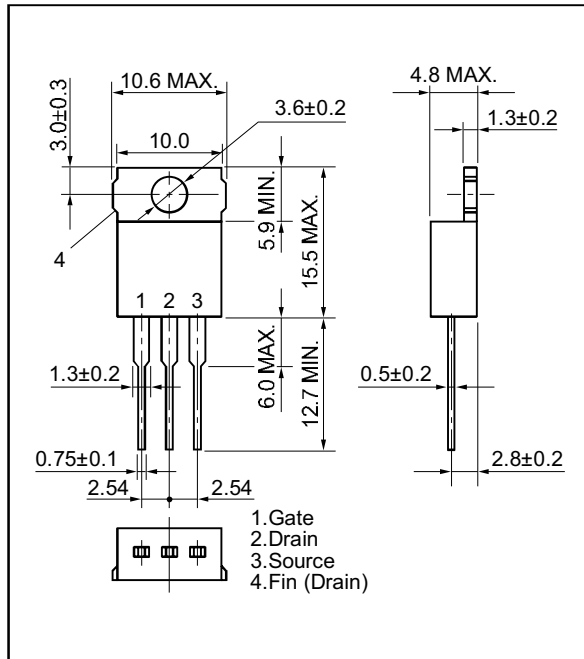


TEST CIRCUIT 2 GATE CHARGE

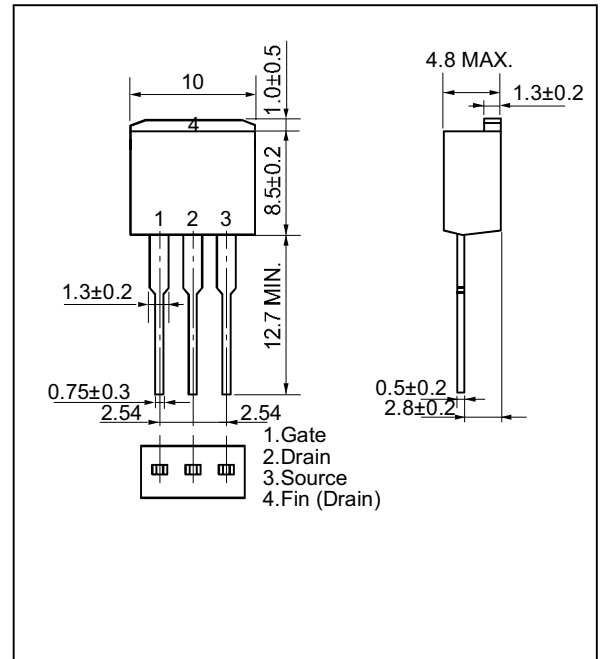


PACKAGE DRAWINGS (Unit : mm)

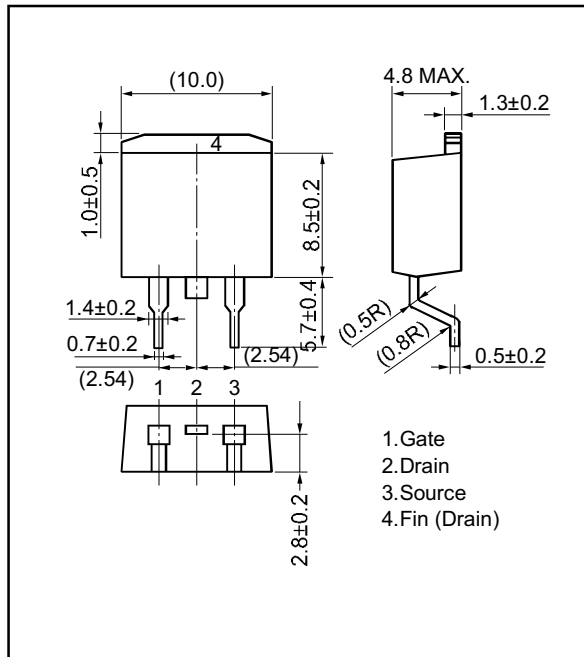
1. TO-220AB (MP-25)



2. TO-262 (MP-25 Fin Cut)



3. TO-263 (JEDEC type : MP-25ZJ)



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Standard: Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots

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Anti-radioactive design is not implemented in this product.