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#### ■ GENERAL DESCRIPTION

**NJM2284** is a switching IC for switching over from one audio or video input signal to another. Internalizing 2 inputs, 1 output, and then each set of 3 can be operated independently. One of them is a Clamp type" and it can be operated while DC level fixed in position of the video signal. It is a higher efficiency video switch, featuring the operating supply voltage 4.75 to 13.0V, the frequency feature 10MHz, and then the Crosstalk 75dB (at 4.43MHz).

#### ■ FEATURES

- 2 Input-1 Output Internalizing 3 Circuits (one of them is a Clamp type).
- Wide Operating Voltage
- Crosstalk 75dB (at 4.43MHz)
- Wide Bandwidth Frequency Feature 10MHz ( $2V_{P-P}$  Input)
- Package Outline DIP-16, DMP-16, SSOP-16

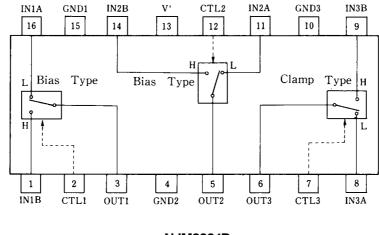
#### ■ RECOMMENDED OPERATING CONDITION

• Supply Voltage  $V^+$  4.75 to 13.0V

#### ■ APPLICATIONS

• VCR, Video Camera, AV-TV, Video Disk Player.

#### BLOCK DIAGRAM



NJM2284D NJM2284M NJM2284V

#### PACKAGE OUTLINE



NJM2284D



NJM2284M



NJM2284V

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MAXIMUM RATINGS			(T <sub>a</sub> = 25°C)
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V*	14	V
Power Dissipation	PD	(DIP16) 700 (DMP16) 350 (SSOP16) 300	mW mW mW
Operating Temperature Range	T <sub>opr</sub>	-40 to +85	℃
Storage Temperature Range	T <sub>stg</sub>	-40 to +125	°C

# ■ ELECTRICAL CHARACTERISTICS

 $(V^+ = 5V, T_a = 25^{\circ}C)$ PARAMETER SYMBOL TEST CONDITION MIN. TYP. MAX. UNIT V<sup>+</sup> = 5V (Note1) Operating Current (1) 8.1 11.6 15.1 mΑ ICC1  $V^{+} = 9V$  (Note1) 10.2 19.0 **Operating Current (2)** I<sub>CC2</sub> 14.6 mΑ +0.4 Voltage Gain Gv  $V_{I} = 100 \text{kHz}, 2V_{P-P}, V_{O} / V_{I}$ -0.6 -0.1 dB Frequency Gain  $V_1 = 2V_{P-P}, V_0 (10MHz) / V_0 (100kHz)$ -1.0 0 +1.0 dB GF **Differential Gain** DG  $V_{I} = 2V_{P-P}$ , Standard Staircase Signal 0.3 % DP **Differential Phasa** VI = 2VP-P, Standard Staircase Signal 0.3 deg \_ Output Offset Voltage Vos (Note2) -10 0 +10 mV Crosstalk CT  $V_1 = 2V_{P-P}, 4.43MHz, V_0 / V_1$ -75 dB \_ Switch Change Over Voltage All inside Switch ON V<sub>CH</sub> 2.5 \_ ٧ \_ All inside Switch OFF Switch Change Over Voltage 1.0 V VCL \_

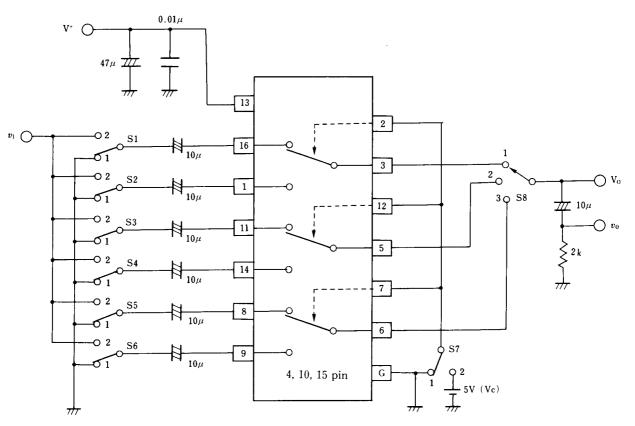
(Note1) S1 = S2 = S3 = S4 = S5 = S6 = S7 = 1

(Note2) S1 = S2 = S3 = S4 = S5 = S6 =1, S7= 1→2 Measure the output DC voltage difference

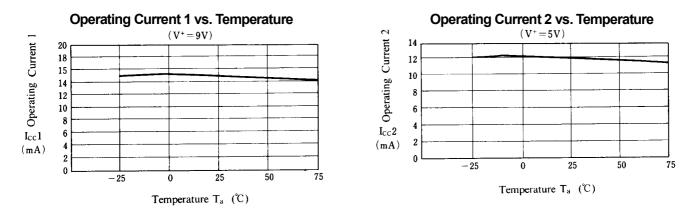
## ■ TERMINAL EXPLANATION

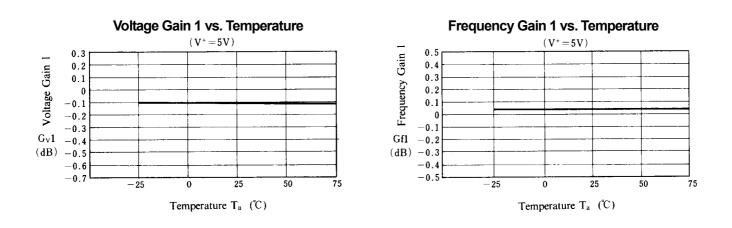
PIN No.	PIN NAME	VOLTAGE	INSIDE EQUIVALENT CIRCUIT
16 1 11 14	IN 1 A IN 1 B IN 2 A IN 2 B [Input]	2.5V	IN 500 15k 777 2.5V
8 9	IN 3 A IN 3 B [Input]	1.5V	
2 12 7	CTL 1 CTL 2 CTL 3 [Switching]		$2.3V \xrightarrow{1.9V} 1.9V \xrightarrow{1.9V} 777 777 8k$
3 5	OUT1 OUT2	1.8V	O OUT
6	OUT3 [Output]	0.8V	
13	V <sup>+</sup>	5V	
15 4 10	GND 1 GND 2 GND 3		

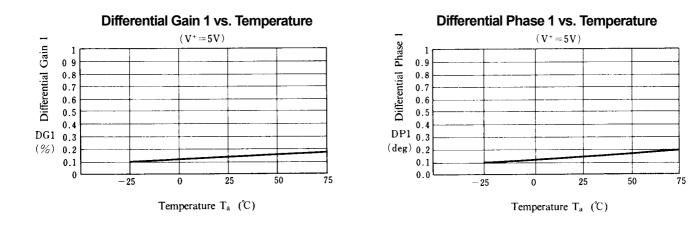
# ■ TEST CIRCUIT



Parameter	S1	S2	S3	S4	S5	S6	S7	S8	Test Part
I <sub>CC1</sub>	1	1	1	1	1	1	1	1	V <sup>+</sup>
I <sub>CC2</sub>	1	1	1	1	1	1	1	1	
G <sub>v1</sub>	2	1	1	1	1	1	1	1	Vo
G <sub>f1</sub>	2	1	1	1	1	1	1	1	
DG <sub>1</sub>	2	1	1	1	1	1	1	1	
DP <sub>1</sub>	2	1	1	1	1	1	1	1	
CT 1	2	1	1	1	1	1	2	1	Vo
CT 2	1	2	1	1	1	1	1	1	
CT 3	1	1	2	1	1	1	2	2	
CT 4	1	1	1	2	1	1	1	2	
CT 5	1	1	1	1	2	1	2	3	
CT 6	1	1	1	1	1	2	1	3	
V <sub>OS1</sub>	1	1	1	1	1	1	1/2	1	Vo
V <sub>C1</sub>	1/2	2/1	1	1	1	1	Vc	1	Vc
THD	2	1	1	1	1	1	1	1	Vo



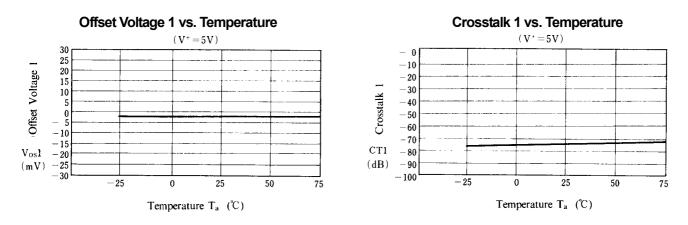


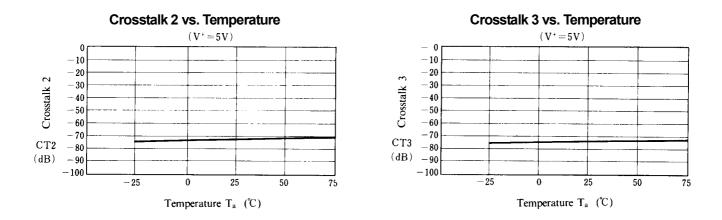


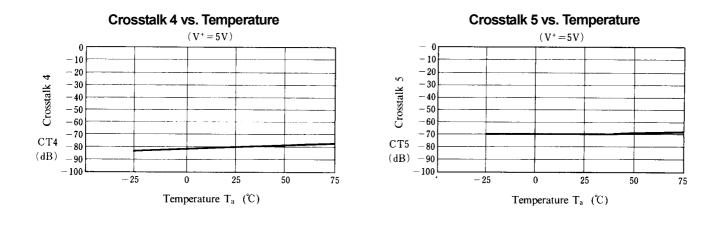
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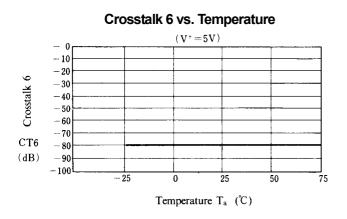
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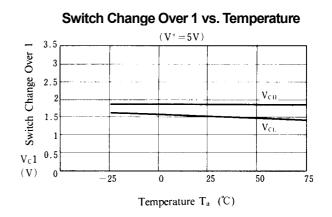


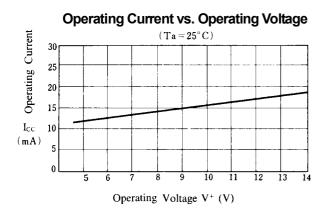




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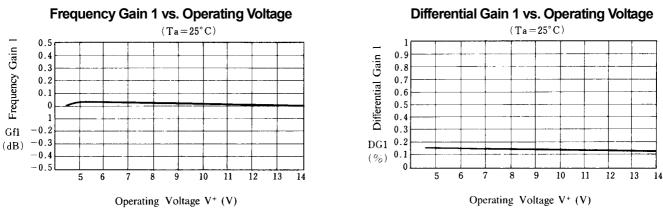




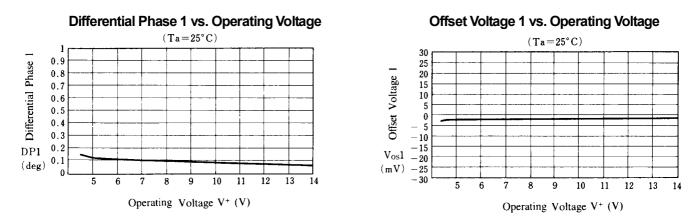


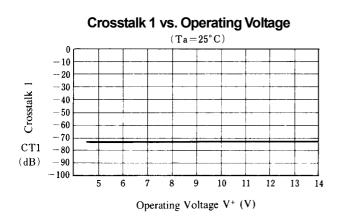
 $(Ta = 25^{\circ}C)$ 0.3 0.2 Voltage Gain 1 0.1 0 --0.1 --0.2 -0.3-0.4  $G_{v1}$ -0.5(dB) = 0.6-0.75 6 7 8 9 10 11 12 13 14 Operating Voltage V+ (V)

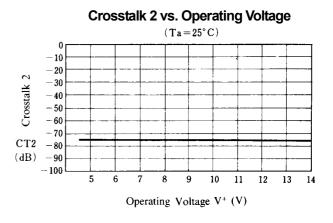
Voltage Gain 1 vs. Operating Voltage

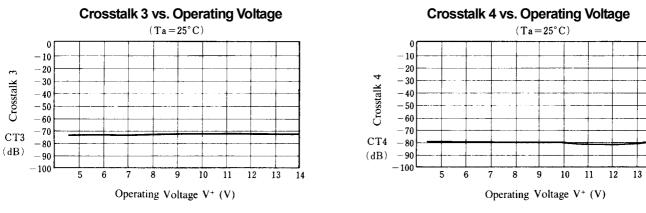


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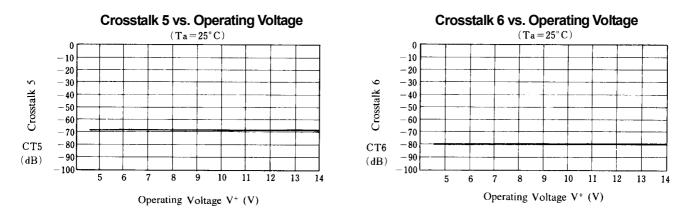


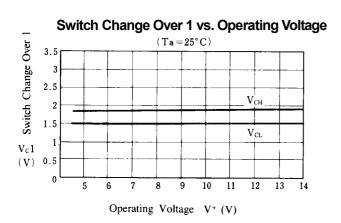


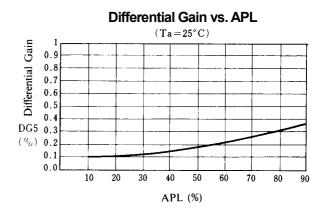


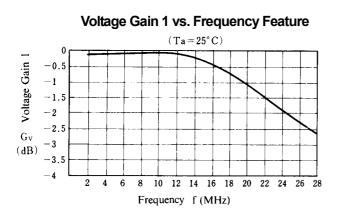
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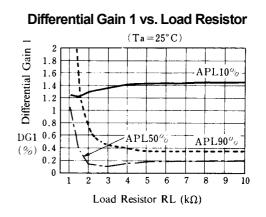
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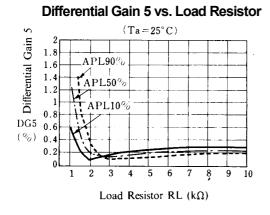


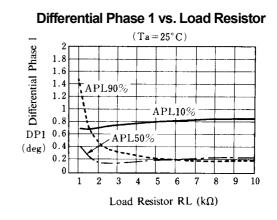


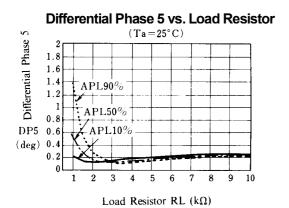


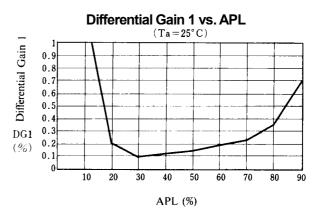


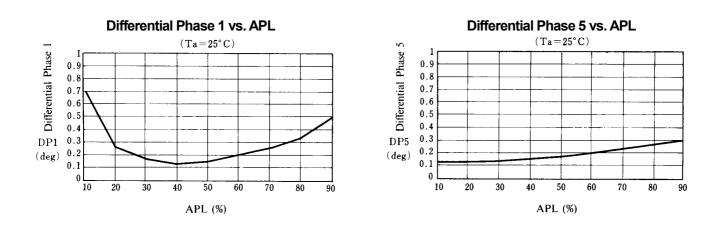


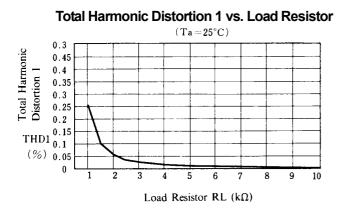






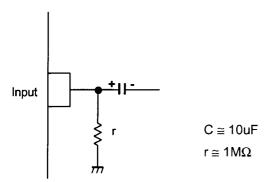




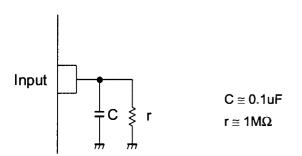


# ■ APPLICATION

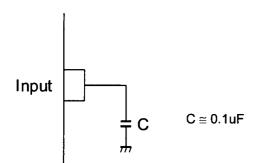
This IC requires 1MΩ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.



This IC requires 0.1μF capacitor between INPUT and GND, 1MΩ resistance between INPUT and GND for clamp type input at mute mode.



This IC requires 0.1µF capacitor between INPUT and GND for bias type input at mute mode.



[CAUTION]
The specifications on this databook are only
given for information, without any guarantee
as regards either mistakes or omissions. The
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