



Micro Power Systems

MP5010

Very Low Tempco
1.2 Volt Reference

FEATURES

- Tested and Guaranteed as low as 5 ppm/ $^{\circ}\text{C}$ Max Tempco
- Wide Operating Range: 50 μA - 5 mA
- Low Output Impedance: 0.6 Ω Typical

BENEFITS

- Lower Sensitivity to Capacitive Loading
- No Frequency Compensation Required
- Accurate Stable Reference over Temp

APPLICATIONS

- Building Block for Custom References
- Low Current Voltage Reference for Hand Held Multimeters
- Voltage Reference for Video Flash Converters
- Voltage Reference for D/A and A/D Converters
- Precision Analog Control Circuits

GENERAL DESCRIPTION

The MP5010 is a 2 terminal, band-gap voltage reference which provides a fixed 1.2 V nominal output voltage. Micro Power Systems design and process enables us to provide guaranteed tempcos as low as 5 ppm/ $^{\circ}\text{C}$ max. We provide this with a

wide input current range of 50 μA to 5mA, lower sensitivity to load capacitances, and a low output impedance of 0.6 Ω (typ).

Specified for operation over the commercial (0 to +70 $^{\circ}\text{C}$), industrial (-40 to +85 $^{\circ}\text{C}$), and military (-55 to +125 $^{\circ}\text{C}$) temperature ranges, the MP5010 is available in Plastic TO-92, Metal Can TO-52, and Surface Mount (SOIC) packages.

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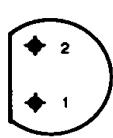
ORDERING INFORMATION

| Part No. | Max Tempco | Temperature Range | Package Type |
|----------|------------|--------------------------------|---------------|
| MP5010GN | 100 | -40 to +85 $^{\circ}\text{C}$ | Plastic TO-92 |
| MP5010HN | 50 | -40 to +85 $^{\circ}\text{C}$ | Plastic TO-92 |
| MP5010LN | 25 | -40 to +85 $^{\circ}\text{C}$ | Plastic TO-92 |
| MP5010MN | 10 | 0 to 70 $^{\circ}\text{C}$ | Plastic TO-92 |
| MP5010JT | 100 | -55 to +125 $^{\circ}\text{C}$ | TO-52 |
| MP5010KT | 50 | -55 to +125 $^{\circ}\text{C}$ | TO-52 |
| MP5010LT | 25 | -55 to +125 $^{\circ}\text{C}$ | TO-52 |
| MP5010MT | 10 | -40 to +85 $^{\circ}\text{C}$ | TO-52 |
| MP5010NT | 5 | -40 to +85 $^{\circ}\text{C}$ | TO-52 |
| MP5010JR | 100 | -40 to +85 $^{\circ}\text{C}$ | SO-8 |
| MP5010GR | 100 | 0 to 70 $^{\circ}\text{C}$ | SO-8 |
| MP5010HR | 50 | -40 to +85 $^{\circ}\text{C}$ | SO-8 |
| MP5010LR | 25 | -40 to +85 $^{\circ}\text{C}$ | SO-8 |
| MP5010MR | 10 | -40 to +85 $^{\circ}\text{C}$ | SO-8 |
| MP5010NR | 5 | -40 to +85 $^{\circ}\text{C}$ | SO-8 |

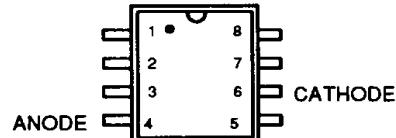
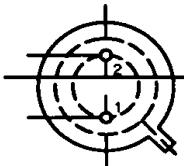
MP5010



PIN CONFIGURATIONS



ANODE (2)
CATHODE (1)



TO-92 PLASTIC

TO-52 (Metal Can)

8 Lead SOIC (0.150")

ELECTRICAL CHARACTERISTICS

| Parameter | Symbol | Min | 25°C Typ | Max | Tmin to Tmax Min Max | Units | Test Conditions/Comments |
|--|------------------|-------|-------------|-------|-------------------------|--------|--|
| Reference Current | I _R | 50 | 5000 | | | μA | |
| Reference Voltage | V _{REF} | 1.200 | 1.220 | 1.250 | | V | I _R = 500μA |
| Output Impedance (1) | Z _{OUT} | | .6 | 2 | | Ω | I _R = 500μA |
| RMS Noise Voltage (1) | | | 5 | | | μV | 10Hz ≤ f ≤ 10 kHz I _R = 500μA |
| BREAKDOWN VOLTAGE TEMPERATURE COEFFICIENT | | | | | | | |
| G-S | | 30 | 100 | | | ppm/°C | |
| H-K | | 25 | 50 | | | | I _R = 500μA |
| L | | 10 | 25 | | | | T _{min} ≤ T _A ≤ T _{max} |
| M | | 5 | 10 | | | | |
| N | | 3 | 5 | | | | |
| Reverse Current | | 50 | 5000 | | | μA | To rated specs |

ABSOLUTE MAXIMUM RATINGS (1, 3)

Maximum Temperature

- Storage (J_T, K_T, L_T, M_T, N_T) -65 to +200°C
- Storage (G_N, H_N, L_N, J_R, G_R, R_R, L_R) -65 to +125°C
- Operating Range (J_T, K_T, L_T) -55 to +125°C
- Operating Range (G_N, H_N, L_N, N_T, M_T, J_R, R_R, L_R) -40 to +85°C
- Operating Range (M_N, G_R) 0 to 70°C

Lead Temperature (soldering, 10 sec) +260°C

- Maximum Power Dissipation (all packages) (2)
- Power Dissipation (25°C) 13mW
- Maximum Current
- Forward Current 10mA
- Reverse Current 10mA

NOTES:

- (1) Guaranteed, not tested.
- (2) Limited by max forward/reverse current.
- (3) Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation at or above this specification is not implied. Exposure to above maximum rating conditions for extended periods may affect device reliability.