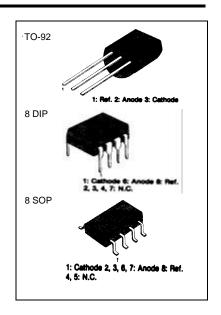
#### PROGRAMMABLE SHUNT REGULATOR

The LM431 Series are three-terminal adjustable regulator series with a guaranteed thermal stability over applicable temperature ranges. The output voltage may be set to any value between  $V_{\text{REF}}$  (approximately 2.5 volts) and 36 volts with two external resistors These devices have a typical dynamic output impedance of  $0.2\Omega$  Active output circuitry provides a very sharp turn-on characteristic, making these devices excel lent replacement for zener diodes in many applications.

# **FEATURES**

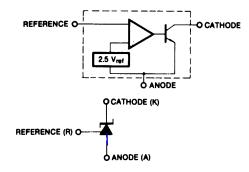
- Programmable output voltage to 36 volts
- Low dynamic output impedance 0.20 typical
- Sink currant capability of 1.0 to 100mA
- Equivalent full-range temperature coefficient of 50ppm/°C typical
- Temperature compensated for operation over full rated operating temperature range
- Low output noise voltage
- Fast turn-on response



### **ORDERING INFORMATION**

Device	Operating Temperature	Package		
LM431ACZ (TL431CLP) (KA431Z)	-25 ~ + 85 °C	TO-92		
TL431CP (KA431)	-25 ~ + 85 °C	8 DIP		
LM431ACM (TL431CD) (KA431D)	-25 ~ + 85 °C	8 SOP		
LM431BCZ (TL431ACLP) (KA431AZ)	-25 ~ + 85 °C	TO-92		
LM431BCM (TL431ACD) (KA431AD)	-25 ~ + 85 °C	8 SOP		
LM431CCZ (KA431LZ)	-25 ~ + 85 °C	TO-92		

### **BLOCK DIAGRAM**





### **ABSOLUTE MAXIMUM RATINGS**

(Operating temperature range applies unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Cathode Voltage	$V_{KA}$	37	V
Cathode current Range (Continuous)	I <sub>KA</sub>	-100~ + 150	mA
Reference Input Current Range	I <sub>REF</sub>	0.05~ + 10	mA
Power Dissipation	$P_D$		
D, Z Suffix Package		770	mW
N Suffix Package		1000	mW
Operating Temperature Range	$T_OPR$	-25 ~ + 85	°C
Storage Temperature Range	T <sub>STG</sub>	-65 ~ + 150	°C

# **RECOMMENDED OPERATING CONDITIONS**

Characteristic	Symbol	Min	Тур	Max	Unit
Cathode Voltage	V <sub>KA</sub>	$V_{REF}$		36	V
Cathode Current	I <sub>KA</sub>	1.0		100	mA

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> =+ 25 °C, unless otherwise specified)

Characteristic Symbol Test Conditions		0	TL431		TL431A		TL431L						
Characteristic	Syllibol	Test Conditions		Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
Reference Input Voltage	$V_{REF}$	V <sub>KA</sub> =V <sub>REF</sub> ,	I <sub>KA</sub> =10mA	2.440	2.495	2.550	2.470	2.495	2.520	2.482	2.495	2.508	V
Deviation of Reference Input Voltage Over- Temperature (Note 1)	<b>D</b> ∨ <sub>REF</sub> / <b>D</b> T	$V_{KA}=V_{REF},$ $T_{MIN}\leq T_{A}\leq T$			4.5	17		4.5	17		4.5	17	mV
Ratio of Change in Reference Input Voltage	ge <b>D</b> V <sub>REF</sub> / <b>D</b> I <sub>KA</sub>		<b>D</b> V <sub>KA</sub> =10V-V <sub>REF</sub>		- 10	- 2.7		- 1.0	- 2.7		- 1.0	- 2.7	>//04/
to the Change in Cathode Voltage		II <sub>KA</sub> =10mA	<b>D</b> V <sub>KA</sub> =36V-10V		-0.5	-2.0		-0.5	-2.0		-0.5	-2.0	mV/W
Reference Input Current	I <sub>REF</sub>	I <sub>KA</sub> =10mA,	$R_1=10K\Omega, R_2=\infty$		1.5	4		1.5	4		1.5	4	μΑ
Deviation of Reference Input Current Over Full Temperature Range	<b>D</b> I <sub>REF</sub> / <b>D</b> T	$I_{KA}$ =10mA,R <sub>1</sub> =10K $\Omega$ ,R <sub>2</sub> = $\infty$ T <sub>A</sub> =Full Range			0.4	1.2		0.4	1.2		0.4	1.2	μА
Minimum Cathode Cur- rent for Regulation	I <sub>KA(MIN)</sub>	V <sub>KA</sub> =V <sub>REF</sub>			0.45	1.0		0.45	1.0		0.45	1.0	mA
Off - Stage Cathode Current	I <sub>KA(OFF)</sub>	V <sub>KA</sub> =36V,V <sub>REF</sub> =0			0.05	1.0		0.05	1.0		0.05	1.0	μА
Dynamic Impedance (Note 2)	Z <sub>KA</sub>	$V_{KA}=V_{REF}$ , $I_{KA}=1$ to 100mA f 1.0K $\Omega$			0.15	0.5		0.15	0.5		0.15	0.5	Ω

 $T_{MIN}$ = -25 °C,  $T_{MAX}$ = +85 °C



# **TEST CIRCUITS**

Fig. 1 Test Circuit for  $V_{\text{KA}} = V_{\text{REF}}$ 

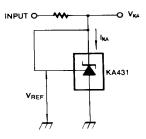


Fig. 3 Test Circuit for  $I_{\text{KA(OFF)}}$ 

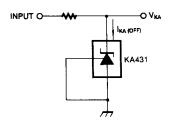
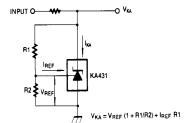


Fig. 2 Test Circuit for  $V_{KA} \ge V_{REF}$ 





#### TYPICAL PERFORMANCE CHARACTERISTICS

Fig. 4 Cathode Current vs. Cathode Voltage

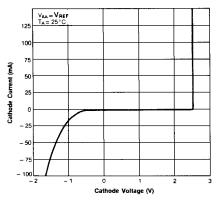


Fig. 6 Change In Reference Input Voltage vs. Cathode Voltage

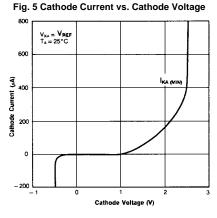


Fig. 7 Dynamic Impedance Frequency

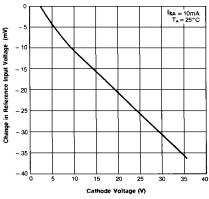


Fig. 8 Small Signal Voltage Amplification vs. Frequency

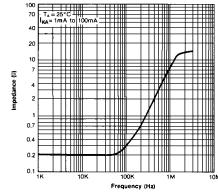
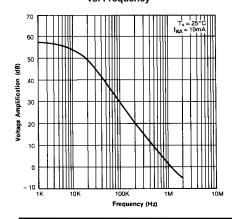
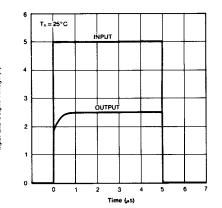


Fig. 9 Pulse Response

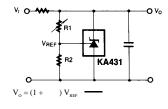






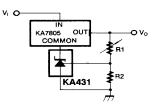
# **TYPICAL APPLICATIONS**

Fig. 10 Shunt Regulator



 $V_{\scriptscriptstyle O} = V_{\scriptscriptstyle REF} (1 + )$ 

Fig.11 Output Control for a Three-Terminal Fixed Regulator



 $V_0 = (1 + ) V_{REF} \frac{R1}{R2}$ 

Fig.12 High Current Shunt Regulator

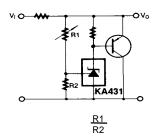


Fig. 13 Current Limit or Current Source

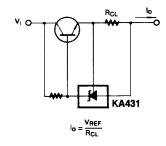
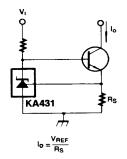


Fig. 14 Constant-Current Sink



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