

## SEMTECH Low Capacitance Surface Mount TVS for **High-Speed Data Interfaces**

#### PROTECTION PRODUCTS

#### Description

The LC03-6 transient voltage suppressor is designed to protect components which are connected to high speed telecommunication lines from voltage surges caused by lightning, electrostatic discharge (ESD), and electrical fast transients (EFT).

TVS diodes are ideal for use as board level protection of sensitive semiconductor components. The LC03-6 combines a TVS diode with a rectifier bridge to provide transient protection in both common and differential mode with a single device. The capacitance of the device is minimized (< 25pF) to ensure correct signal transmission on high speed lines . The LC03-6 meets the short-haul (intra-building) transient immunity requirements of Bellcore 1089 for telecommunications applications.

The SO-8 surface mount package allows flexibility in the design of crowded PC boards.

#### Features

- ◆ 2000 watts peak pulse power (tp = 8/20µs)
- Transient protection for high-speed data lines to Bellcore 1089 (Intra-Building) 100A (2/10µs) ITU K.20 I<sub>pp</sub>=40A (5/310μs) IEC 61000-4-2 (ESD) 15kV (air), 8kV (contact) IEC 61000-4-4 (EFT) 40A (5/50ns) IEC 61000-4-5 (Lightning) 24A (1.2/50μs)
- Protects two lines in common and differential mode
- Low capacitance for high speed interfaces
- Low clamping and operating voltage
- Integrated structure saves board space and increases reliability
- Solid-state silicon avalanche technology

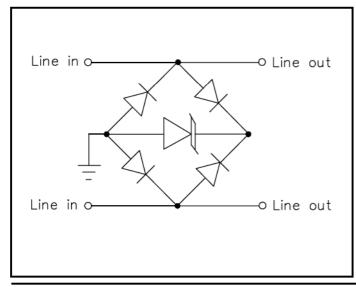
#### Mechanical Characteristics

- JEDEC SO-8 package
- UL 497B listed
- Molding compound flammability rating: UL 94V-0
- Marking: Part number, date code, logo
- Packaging: Tube or Tape and Reel per EIA 481

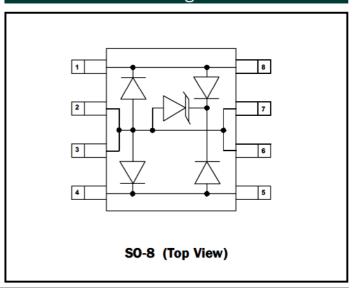
#### Applications

- T1/E1 Line Cards
- T3/E3 and DS3 Interfaces
- STS-1 Interfaces
- ISDN S/T-Interfaces
- ISDN U-Interfaces
- 10/100 Ethernet

### Circuit Diagram



### Schematic & PIN Configuration





# Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20µs)	$P_{pk}$	2000	Watts
Peak Pulse Current (tp = 8/20µs)	I <sub>PP</sub>	100	Α
Lead Soldering Temperature	Τ <sub>ι</sub>	260 (10 sec.)	°C
Operating Temperature	T <sub>j</sub>	-55 to +125	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

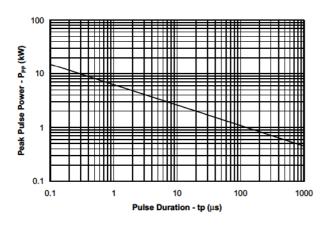
## Electrical Characteristics

LC03-6						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>				6	٧
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>t</sub> = 1mA	6.8			٧
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 6V, T=25°C			25	μA
Clamping Voltage	V <sub>c</sub>	$I_{pp} = 50A$ , $tp = 8/20\mu s$			15	٧
Clamping Voltage	V <sub>c</sub>	$I_{pp} = 100A, tp = 8/20\mu s$			20	٧
Junction Capacitance	C <sub>j</sub>	Between I/O pins and Gnd V <sub>R</sub> = OV, f = 1MHz	16 25		pF	
		Between I/O pins V <sub>R</sub> = OV, f = 1MHz		8	12	pF

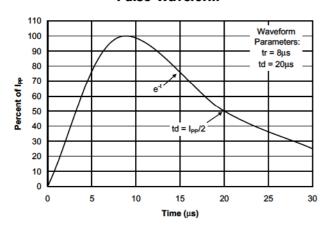


### Typical Characteristics

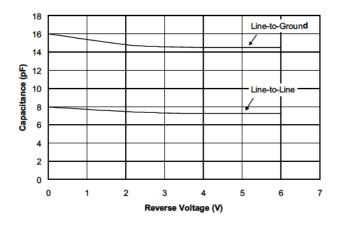
#### Non-Repetitive Peak Pulse Power vs. Pulse Time



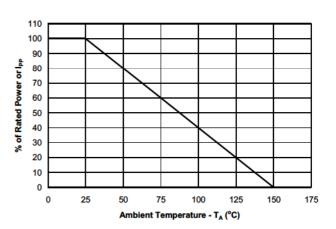
#### **Pulse Waveform**



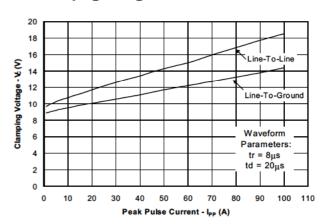
#### Capacitance vs. Reverse Voltage



#### **Power Derating Curve**



#### Clamping Voltage vs. Peak Pulse Current





#### Applications Information

#### Device Connection Options for Protection of Two High-Speed Data Lines

The LCO3-6 is designed to protect two high-speed data lines (one differential pair) from transient over-voltages which result from lightning and ESD. The device can be configured to protect in differential (Line-to-Line) and common (Line-to-Ground) mode. Data line inputs/outputs are connected at pins 1 to 8, and 4 to 5 as shown. Pins 2, 3, 6, and 7 are connected to ground. These pins should be connected directly to a ground plane on the board for best results. The path length is kept as short as possible to minimize parasitic inductance. In applications where high common mode voltages are present, differential protection is achieved by leaving pins 2, 3, 6, and 7 not connected.

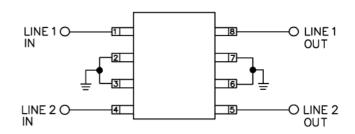
#### T1/E1 Linecard Protection (Intra-Building)

A typical T1/E1 linecard protection circuit is shown below. The LCO3-6 is connected between Tip & Ring on the transmit and receive line pairs. It provides protection to metallic and common mode lightning surges per Bellcore 1089. This design takes advantage of the isolation of the transformer to suppress common mode surges. To complete the protection circuit, the SRDA05-4 (or SRDA3.3-4 for 3.3V supplies) is employed as the IC side protection element. This device helps prevent the transceiver from latching up by providing fine clamping of transients that are coupled through the transformer. For further information, reference Semtech application note AN97-10

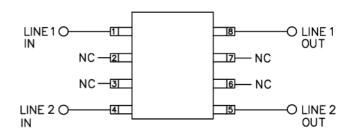
#### T3/E3 & STS-1 Protection

The LCO3-6 can also be used to protect T3/E3 and STS-1 interfaces. The data lines from the BNC interface are run through the LCO3-6 (i.e. enters at pin 1 and exits at pin 8) with the ground connection made at the other side of the device (pins 4 & 5). The center pins (2, 3, 6, and 7) are not connected. In this configuration, the LCO3-6 adds less than 12pF of capacitance to each line and provides surge protection to 100A ( $tp=8/20\mu s$ ).

# Connection for Differential (Line-to-Line) and Common Mode Protection (Line-to-Ground)

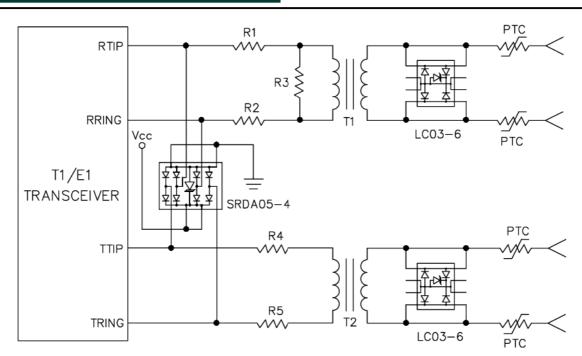


# Connection for Differential Protection (Line-to-Line)

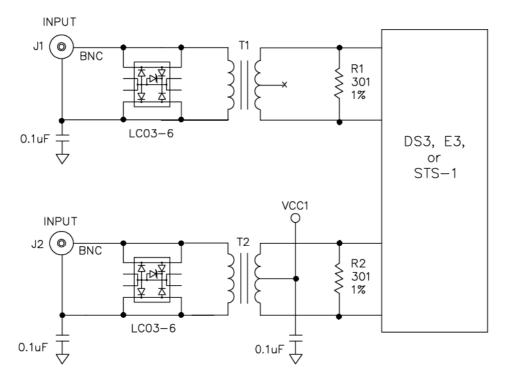




### Typical Applications



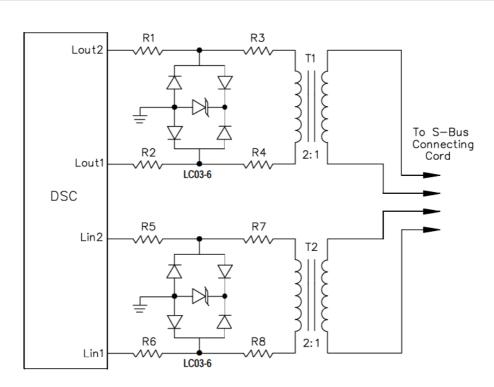
**T1 Line Card Protection (Short-Haul Applications)** 



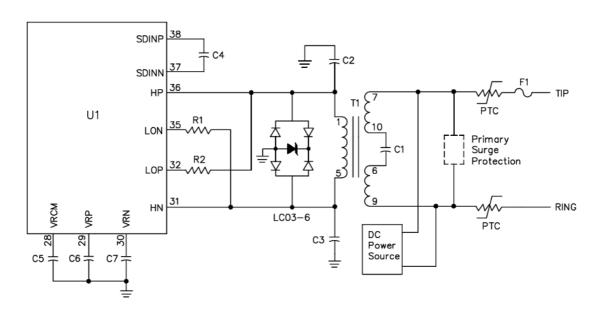
T3/E3 & STS-1 Protection



### Typical Applications (Continued)



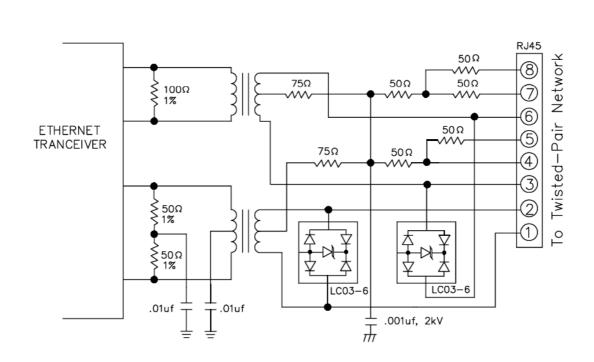
**ISDN S-Interface Protection** 



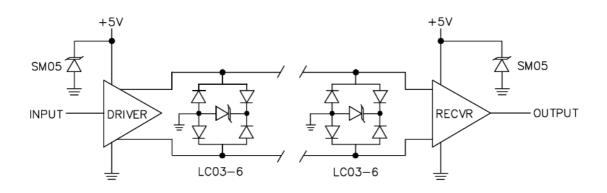
**ISDN U-Interface Secondary Protection** 



### Typical Applications (Continued)



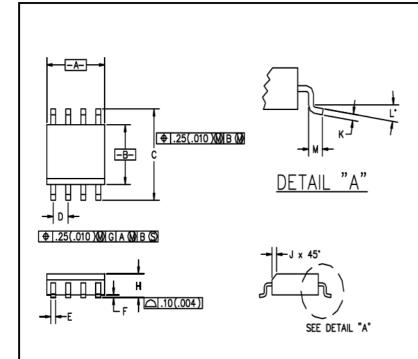
#### 10/100 Ethernet Protection



**High Speed Driver/Receiver Protection** 

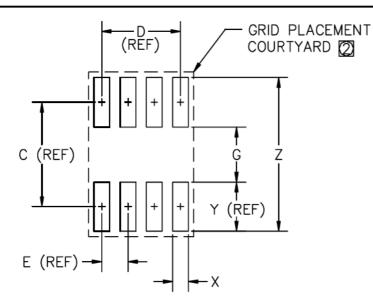


### Outline Drawing - SO-8



	DIMENSIONS					
DIMN	INCHES		М	NOTE		
DIN	MIN	MAX	MIN	MAX	INOIL	
Α	.188	.197	4.80	5.00		
В	.149	.158	3.80	4.00		
С	.228	.244	5.80	6.20		
D	.050	BSC	1.27	BSC		
Ε	.013	.020	0.33	0.51		
F	.004	.010	0.10	0.25		
Н	.053	.069	1.35	1.75		
J	.011	.019	0.28	0.48		
K	.007	.010	.19	.25		
L	0.	8°	0°	8°		
M	.016	.050	0.40	1.27		

### Land Pattern - SO-8



DIMENSIONS (1)					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	NOTE
С	ı	.19	1	5.00	_
D	ı	.15	ı	3.81	-
Ε	-	.05	_	1.27	_
G	.10	.11	2.60	2.80	_
Χ	.02	.03	.60	.80	_
Y	_	.09	_	2.40	_
Z	_	.29	7.20	7.40	_

- GRID PLACEMENT COURTYARD IS 12x16 ELEMENTS (6 mm X 8mm) IN ACCORDANCE WITH THE INTERNATIONAL GRID DETAILED IN IEC PUBLICATION 97.
- CONTROLLING DIMENSION: MILLIMETERS



### Ordering Information

Part Number	Working Voltage	Qty per Reel	Reel Size
LC03-6.TB	6V	500	7 Inch
LC03-6.TE	6V	2500	13 Inch

#### Note:

(1) No suffix indicates tube pack.

# Contact Information

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