

# FMS6418A

## Triple Video Driver with Selectable HD/SD Video Filters for RGB or YUV Signals

### Features

- Three video anti-aliasing or reconstruction filters
- YUV/RGB signal support
- 2:1 Mux inputs for multiple RGB/YUV inputs
- Selectable 8MHz or 30MHz 6th order filters for SD/HD applications
- DC coupled input, AC coupled output
- All outputs can drive AC coupled 150Ω loads and provide 6dB of gain
- 0.6% differential gain with 0.15° differential phase
- 36dB/octave roll-off on all channels

### Applications

- Cable Set top boxes
- Satellite Set top boxes
- DVD players
- HDTV
- Personal Video Recorders (PVR)
- Video On Demand (VOD)

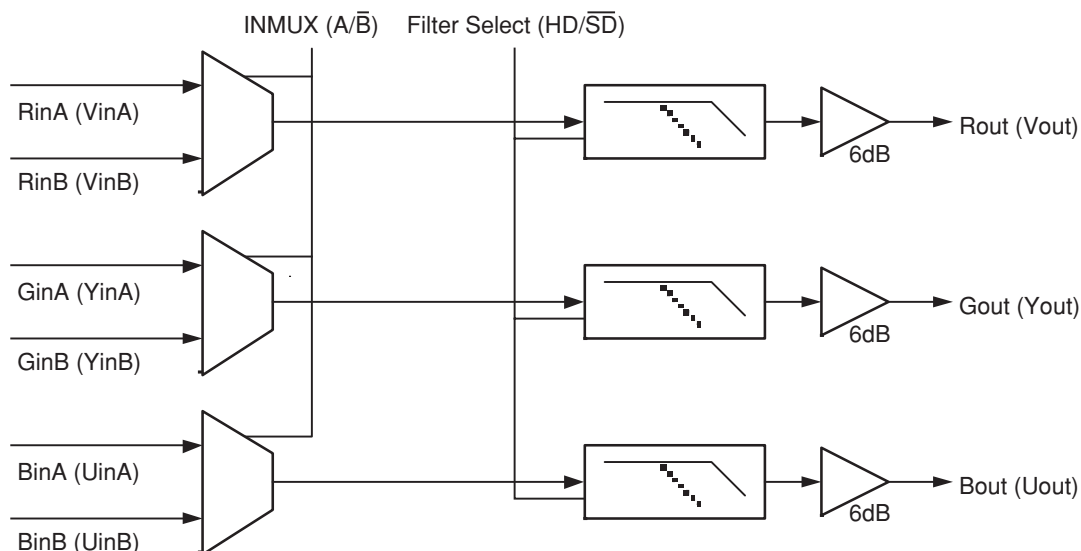
### Description

The FMS6418A offers comprehensive filtering for set top box or DVD applications. This part consists of a triple 6th order filter with selectable 30MHz or 8.0MHz frequencies.

A 2-to-1 multiplexer is provided on each filter channel. The triple filters are intended for either YUV or RGB signals. All channels accept DC coupled ground-referenced 1V signals. The filters provide 2Vpp signals into AC coupled terminated loads. The low-pass filters are powered by 3.3V and the outputs by 5.0V.

The FMS6418A is available in both 16-pin SOIC and 14-pin TSSOP packages.

### Functional Block Diagram



## Electrical Specifications

( $T_C = 25^\circ\text{C}$ ,  $V_i = 1V_{pp}$ ;  $V_{CCA} = 3.3V$ ,  $V_{CCO} = 5.0V$ , all inputs AC coupled with  $0.1\mu\text{F}$ , all outputs AC coupled with  $220\mu\text{F}$  into  $150\Omega$ , referenced to  $400\text{kHz}$ ; unless otherwise noted)

| Symbol    | Parameter                       | Conditions             | Min | Typ | Max       | Units |
|-----------|---------------------------------|------------------------|-----|-----|-----------|-------|
| $I_{CCA}$ | Supply Current <sup>1</sup>     | $V_{CCA}$ no load      | 35  | 45  | 70        | mA    |
| $I_{CCO}$ | Supply Current <sup>1</sup>     | $V_{CCO}$ no load      | 30  | 45  | 60        | mA    |
| $V_i$     | Input Voltage Max               | Reference to ground    |     | 1.3 |           | V     |
| $V_{il}$  | Digital Input Low <sup>1</sup>  | $F_{SEL}$ , $IN_{MUX}$ | 0   |     | 0.8       | V     |
| $V_{ih}$  | Digital Input High <sup>1</sup> | $F_{SEL}$ , $IN_{MUX}$ | 2.4 |     | $V_{CCO}$ | V     |
| PSSR      | PSSR (all channels)             | DC                     |     | -40 |           | dB    |

## Standard Definition Electrical Specifications

( $T_C = 25^\circ\text{C}$ ,  $V_i = 1V_{pp}$ ;  $V_{CCA} = 3.3V$ ,  $V_{CCO} = 5.0V$ ,  $F_{SEL} = 0$ , all inputs DC coupled, all outputs AC coupled with  $220\mu\text{F}$  into  $150\Omega$ , referenced to  $400\text{kHz}$ ; unless otherwise noted)

| Symbol        | Parameter                                      | Conditions                                                         | Min | Typ  | Max | Units |
|---------------|------------------------------------------------|--------------------------------------------------------------------|-----|------|-----|-------|
| $AV_{RGBSD}$  | RGB SD Gain <sup>1</sup>                       | R,G,B channels SD Mode                                             | 5.6 | 6.0  | 6.4 | dB    |
| $f_{1dBSD}$   | -1dB Bandwidth for SD <sup>1</sup>             | R,G,B channels                                                     | 4.5 | 6.0  |     | MHz   |
| $f_{CSD}$     | -3dB Bandwidth for SD                          | R,G,B channels                                                     |     | 8.2  |     | MHz   |
| $f_{SBSD}$    | Attenuation: SD (stopband reject) <sup>1</sup> | R,G,B channels at $f = 27\text{MHz}$                               | -40 | -55  |     | dB    |
| dG            | Differential Gain                              | R,G,B channels                                                     |     | 0.6  |     | %     |
| d $\phi$      | Differential Phase                             | R,G,B channels                                                     |     | 0.15 |     | °     |
| THD           | Output Distortion (all channels)               | $V_{OUT} = 1.8V_{pp}$ , RGB Out at 1MHz                            |     | 0.4  |     | %     |
| $X_{TALK}$    | Crosstalk (channel-to-channel)                 | at 1MHz                                                            |     | -70  |     | dB    |
| $IN_{MUXISO}$ | $IN_{MUX}$ Isolation                           | at 1MHz                                                            |     | -90  |     | dB    |
| SNR           | Signal-to-Noise Ratio                          | R,G,B channels, NTC-7 weighting<br>4.2MHz lowpass, 100kHz highpass |     | -73  |     | dB    |
| $t_{pdSD}$    | Prop Delay for SD                              | Delay from input to output at 4.5MHz                               |     | 70   |     | ns    |

## High Definition Electrical Specifications

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| Symbol        | Parameter                                      | Conditions                              | Min | Typ | Max | Units |
|---------------|------------------------------------------------|-----------------------------------------|-----|-----|-----|-------|
| $AV_{RGBHD}$  | RGB HD Gain <sup>1</sup>                       | R,G,B channels HD Mode                  | 5.6 | 6.0 | 6.4 | dB    |
| $f_{1dBHD}$   | -1dB Bandwidth for HD <sup>1</sup>             | R,G,B channels                          | 20  | 23  |     | MHz   |
| $f_{CHD}$     | -3dB Bandwidth for HD                          | R,G,B channels                          |     | 32  |     | MHz   |
| $f_{SBHD}$    | Attenuation: HD (stopband reject) <sup>1</sup> | R,G,B channels at $f = 74.25\text{MHz}$ | -30 | -36 |     | dB    |
| $X_{TALKB}$   | Crosstalk (channel-to-channel)                 | at 1MHz                                 |     | -70 |     | dB    |
| $IN_{MUXISO}$ | $IN_{MUX}$ Isolation                           | at 1MHz                                 |     | -90 |     | dB    |
| SNR           | Signal-to-Noise Ratio                          | R,G,B channels                          |     | -73 |     | dB    |
| $t_{pdHD}$    | Prop Delay for HD                              | Delay from input to output at 20MHz     |     | 20  |     | ns    |

### Notes:

1. 100% tested at  $25^\circ\text{C}$ .

## Absolute Maximum Ratings (beyond which the device may be damaged)

| Parameter                                      | Min  | Max             | Units |
|------------------------------------------------|------|-----------------|-------|
| DC Supply Voltage                              | -0.3 | 6.5             | V     |
| Analog and Digital I/O                         | -0.3 | $V_{CCO} + 0.3$ | V     |
| Output Current Any One Channel (Do Not Exceed) |      | 120             | mA    |

### Note

Functional operation under any of these conditions is NOT implied. Performance and reliability are guaranteed only if operating conditions are not exceeded.

## Reliability Information

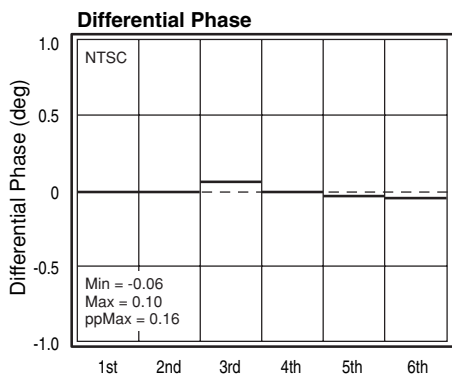
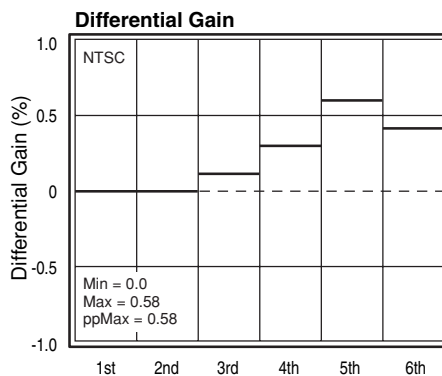
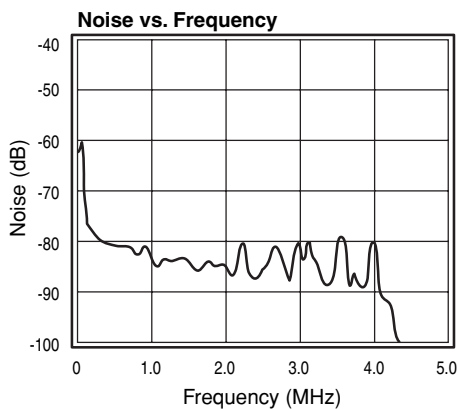
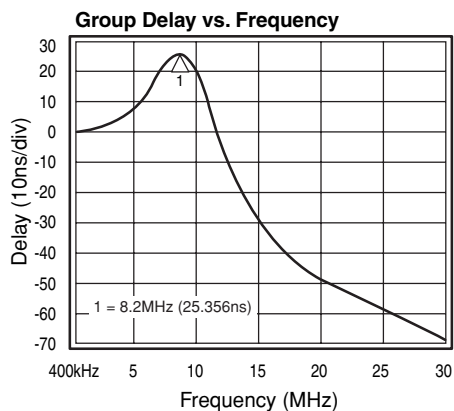
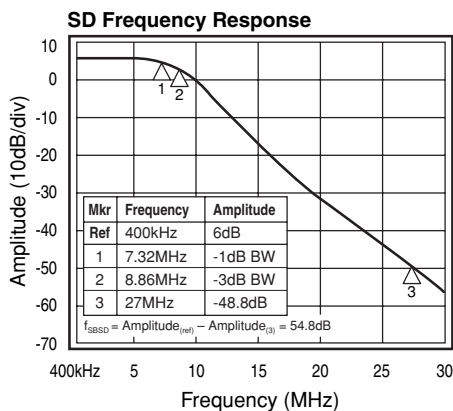
| Parameter                                                                                  | Min | Typ | Max  | Units |
|--------------------------------------------------------------------------------------------|-----|-----|------|-------|
| Junction Temperature                                                                       |     |     | +150 | °C    |
| Storage Temperature Range                                                                  | -65 |     | +150 | °C    |
| Lead Temperature (Soldering, 10s)                                                          |     |     | +300 | °C    |
| Thermal Resistance ( $\theta_{JA}$ ),<br>JEDEC Standard Multi-layer Test Boards, Still Air |     | 70  |      | °C/W  |

## Recommended Operating Conditions

| Parameter                   | Min   | Typ | Max   | Units |
|-----------------------------|-------|-----|-------|-------|
| Operating Temperature Range | 0     |     | 70    | °C    |
| $V_{CCO}$ Range             | 4.75  | 5.0 | 5.25  | V     |
| $V_{CCA}$ Range             | 3.135 | 3.3 | 3.465 | V     |

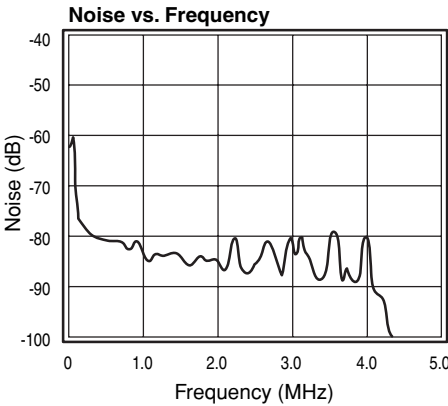
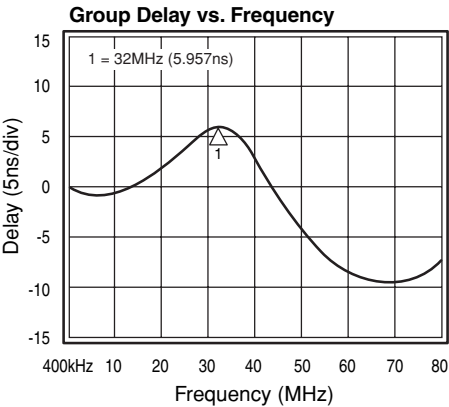
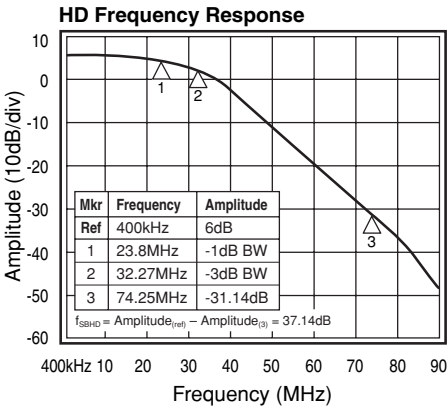
## Standard Definition Typical Performance Characteristics

( $T_C = 25^\circ\text{C}$ ,  $V_i = 1\text{V}_{pp}$ ,  $V_{CCA} = 3.3\text{V}$ ,  $V_{CCO} = 5.0\text{V}$ ,  $F_{SEL} = 0$ , all inputs AC coupled with  $0.1\mu\text{F}$ , all outputs AC coupled with  $220\mu\text{F}$  into  $150\Omega$ , referenced to  $400\text{kHz}$ ; unless otherwise noted)



# High Definition Typical Performance Characteristics

( $T_C = 25^{\circ}\text{C}$ ,  $V_i = 1V_{pp}$ ;  $V_{CCA} = 3.3V$ ,  $V_{CCO} = 5.0V$ ,  $F_{SEL} = 1$ , all inputs AC coupled with  $0.1\mu\text{F}$ , all outputs AC coupled with  $220\mu\text{F}$  into  $150\Omega$ , referenced to  $400\text{kHz}$ ; unless otherwise noted)



## General Description

The FMS6418A offers comprehensive filtering for set top box or DVD applications. This part consists of a triple 6th order filter with selectable 30MHz to 8.0MHz frequencies. A 2-to-1 multiplexer is provided on each filter. The filters are intended for either YUV or RGB signals. All channels accept DC coupled ground-referenced 1V signals. The filters provide 2Vpp signals into AC coupled terminated loads. All channels provide 6dB gain, accept 1V ground referenced inputs, and drive AC coupled loads. The RGB low-pass filters are powered by 3.3V and the output buffers are powered by 5.0V.

The FMS6418A is a next generation filter solution from Fairchild Semiconductor addressing the expanding filtering needs for set top boxes, and DVD players. The product provides selectable filtering with cutoff frequencies of 30MHz or 8.0MHz on the RGB/YUV channels. Thus, the FMS6418A addresses the requirement for a single set top box to be compatible with a variety of resolution standards. Multiplexers on the RGB channels provide further flexibility. For DVD, Set-top Box, and TV applications, the product provides filtering and output drive amplification for three channels of outputs (RGB/YUV).

## Applications

### DC Levels

At any given time, the input signal's DC levels must be between 0.0V and 1.3V to utilize the optimal headroom and to avoid clipping on the outputs.

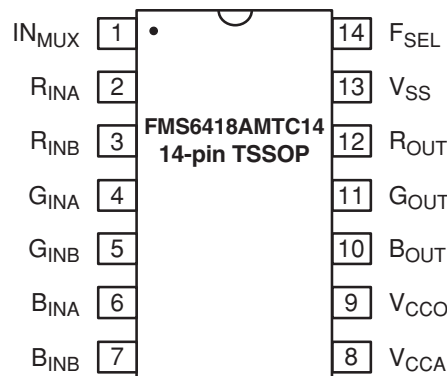
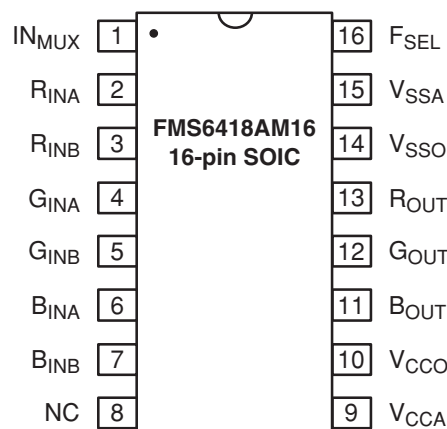
### Single Supply +5V Operation

For low power consumption, the FMS6418 was designed to operate off of 5V and 3.3V.  $V_{CCA}$  can be operated from 5V instead of 3.3V and still meet specifications, except power consumption. When the 3.3V supply is increased to 5V, the typical current consumption increases by 5mA. The net effect is the part dissipates an additional 22 percent of power.

## Driving the Digital Pins with 3.3V or 5V Logic.

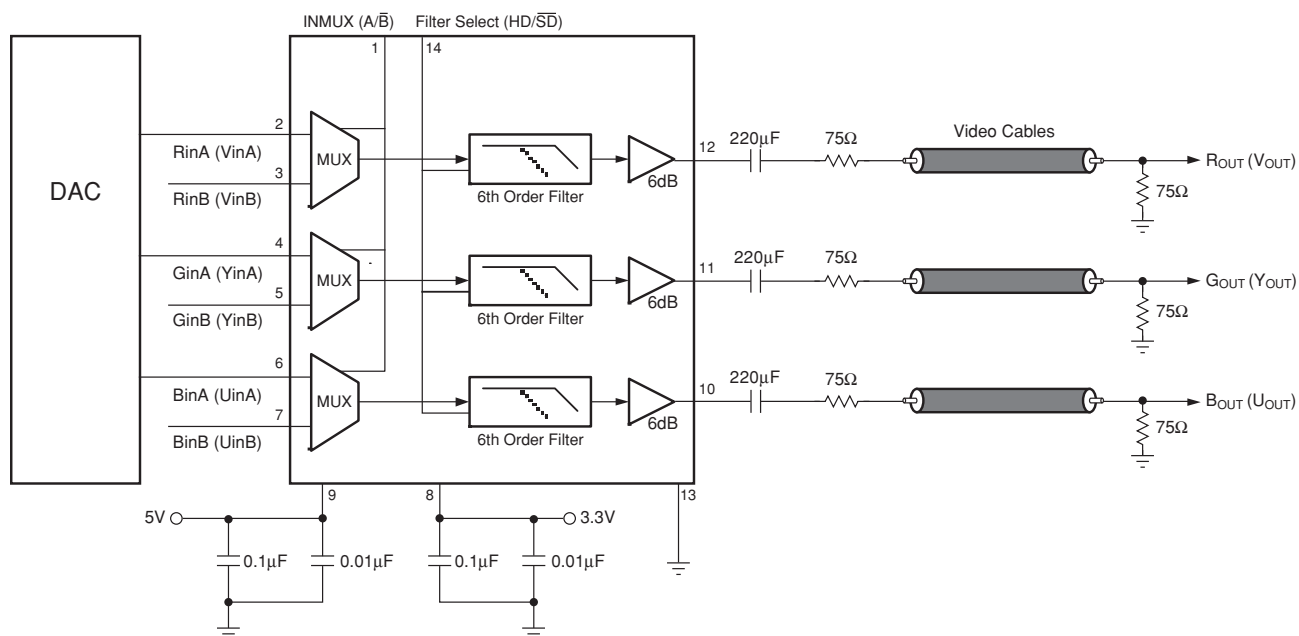
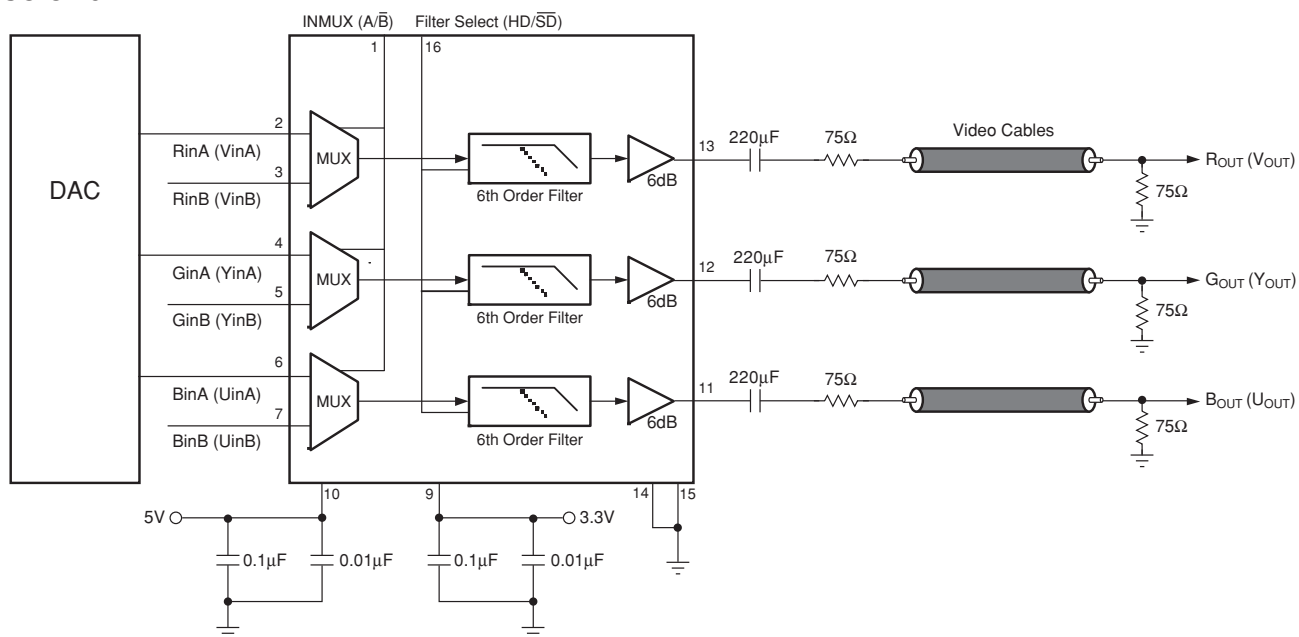
Either is allowed as long as the  $V_{ih}$  and  $V_{il}$  are adhered to.

## Pin Configurations



## Typical Application Diagrams

**TSSOP-14**

**SOIC-16**

## Pin Assignments

| TSSOP-14 | SOIC-16 | Pin Name          | Description                                                                                             | Equivalent Circuit |
|----------|---------|-------------------|---------------------------------------------------------------------------------------------------------|--------------------|
| 1        | 1       | IN <sub>MUX</sub> | Logic input selects between channel <A> or <B> of the RGB inputs.<br>(1): RGB A input, (0): RGB B input |                    |
| 14       | 16      | F <sub>SEL</sub>  | Logic Input selects between (0) SD (8.0MHz) and (1) HD (30.0MHz) filters                                |                    |
| 2        | 2       | R <sub>INA</sub>  | Analog RED video input - Channel A                                                                      |                    |
| 3        | 3       | R <sub>INB</sub>  | Analog RED video input - Channel B                                                                      |                    |
| 4        | 4       | G <sub>INA</sub>  | Analog GREEN video input - Channel A                                                                    |                    |
| 5        | 5       | G <sub>INB</sub>  | Analog GREEN video input - Channel B                                                                    |                    |
| 6        | 6       | B <sub>INA</sub>  | Analog BLUE video input - Channel A                                                                     |                    |
| 7        | 7       | B <sub>INB</sub>  | Analog BLUE video input - Channel B                                                                     |                    |
| N/A      | 8       | NC                | No Connect (float pin)                                                                                  |                    |
| 8        | 9       | V <sub>CCA</sub>  | +3.3V power supply for filters                                                                          |                    |
| 9        | 10      | V <sub>CCO</sub>  | +5V power supply for output buffers                                                                     |                    |
| 10       | 11      | B <sub>OUT</sub>  | Filtered Analog BLUE video output from either B <sub>INA</sub> or B <sub>INB</sub>                      |                    |
| 11       | 12      | G <sub>OUT</sub>  | Filtered Analog GREEN video output from either G <sub>INA</sub> or G <sub>INB</sub>                     |                    |
| 12       | 13      | R <sub>OUT</sub>  | Filtered Analog RED video output from either R <sub>INA</sub> or R <sub>INB</sub>                       |                    |
| N/A      | 14      | V <sub>SSO</sub>  | Ground for output buffers                                                                               |                    |
| N/A      | 15      | V <sub>SSA</sub>  | Ground for filters                                                                                      |                    |
| 13       | N/A     | V <sub>SS</sub>   | Ground                                                                                                  |                    |

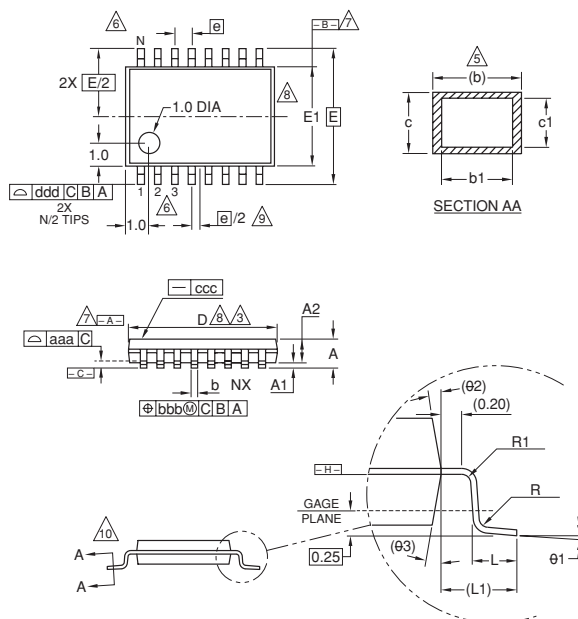
⑪ = TSSOP-14 Pin Numbers

⑪ = SOIC-16 Pin Numbers



## Package Dimensions

## TSSOP-14

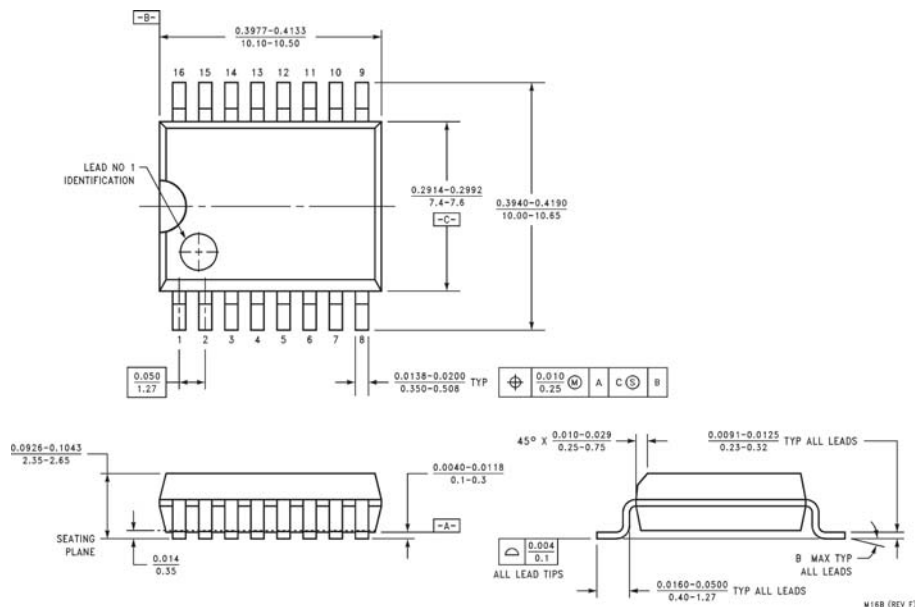


| TSSOP-14 |          |      |      |
|----------|----------|------|------|
| SYMBOL   | MIN      | NOM  | MAX  |
| A        | —        | —    | 1.10 |
| A1       | 0.05     | —    | 0.15 |
| A2       | 0.85     | 0.90 | 0.95 |
| L        | 0.50     | 0.60 | 0.75 |
| R        | 0.09     | —    | —    |
| R1       | 0.09     | —    | —    |
| b        | 0.19     | —    | 0.30 |
| b1       | 0.19     | 0.22 | 0.25 |
| c        | 0.09     | —    | 0.20 |
| c1       | 0.09     | —    | 0.16 |
| θ1       | 0°       | —    | 8°   |
| L1       | 1.0 REF  |      |      |
| aaa      | 0.10     |      |      |
| bbb      | 0.10     |      |      |
| ccc      | 0.05     |      |      |
| ddd      | 0.20     |      |      |
| e        | 0.65 BSC |      |      |
| θ2       | 12° REF  |      |      |
| θ3       | 12° REF  |      |      |
| D        | 4.90     | 5.00 | 5.10 |
| E1       | 4.30     | 4.40 | 4.50 |
| E        | 6.4 BSC  |      |      |
| e        | 0.65 BSC |      |      |
| N        | 14       |      |      |

**NOTES:**

- 1 All dimensions are in millimeters (angle in degrees).
- 2 Dimensioning and tolerancing per ASME Y14.5–1994.
- 3 Dimensions "D" does not include mold flash, protusions or gate burrs. Mold flash protusions or gate burrs shall not exceed 0.15 per side.
- 4 Dimension "E1" does not include interlead flash or protusion. Interlead flash or protusion shall not exceed 0.25 per side.
- 5 Dimension "b" does not include dambar protusion. Allowable dambar protusion shall be 0.08mm total in excess of the "b" dimension at maximum material condition. Dambars cannot be located on the lower radius of the foot. Minimum space between protusion and adjacent lead is 0.07mm for 0.5mm pitch packages.
- 6 Terminal numbers are shown for reference only.
- 7 Datums  $\boxed{-A-}$  and  $\boxed{-B-}$  to be determined at datum plane  $\boxed{-H-}$ .
- 8 Dimensions "D" and "E1" to be determined at datum plane  $\boxed{-H-}$ .
- 9 This dimension applies only to variations with an even number of leads per side. For variation with an odd number of leads per side, the "center" lead must be coincident with the package centerline, Datum A.
- 10 Cross sections A – A to be determined at 0.10 to 0.25mm from the leadtip.

## SOIC-16



## Ordering Information

| Model    | Part Number    | Lead Free                                                                         | Package      | Container   | Pack Qty |
|----------|----------------|-----------------------------------------------------------------------------------|--------------|-------------|----------|
| FMS6418A | FMS6418AMTC14  |  | 14-pin TSSOP | Tube        | 94       |
| FMS6418A | FMS6418AMTC14X |  | 14-pin TSSOP | Tape & Reel | 2,500    |
| FMS6418A | FMS6418AM16    |  | 16-pin SOIC  | Tube        | 45       |
| FMS6418A | FMS6418AM16X   |  | 16-pin SOIC  | Tape & Reel | 1,000    |

Temperature range for all parts: 0°C to +70°C.

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.