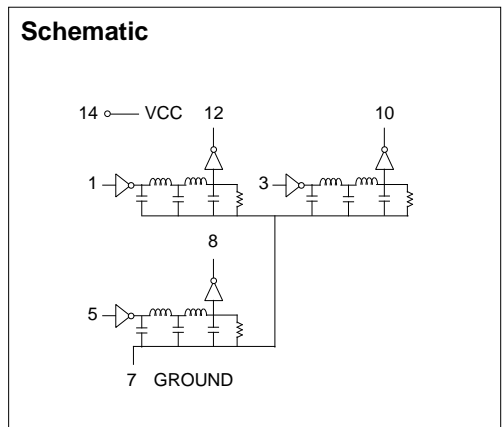


# 14 Pin DIP Triple TTL Compatible Active Delay Lines

DELAY TIME ±5% or ±2 nS†	PART NUMBER	DELAY TIME ±5% or ±2 nS†	PART NUMBER	DELAY TIME ±5% or ±2 nS†	PART NUMBER
5	EP9206-005	19	EP9206-019	65	EP9206-065
6	EP9206-006	20	EP9206-020	70	EP9206-070
7	EP9206-007	21	EP9206-021	75	EP9206-075
8	EP9206-008	22	EP9206-022	80	EP9206-080
9	EP9206-009	23	EP9206-023	85	EP9206-085
10	EP9206-010	24	EP9206-024	90	EP9206-090
11	EP9206-011	25	EP9206-025	95	EP9206-095
12	EP9206-012	30	EP9206-030	100	EP9206-100
13	EP9206-013	35	EP9206-035	125	EP9206-125
14	EP9206-014	40	EP9206-040	150	EP9206-150
15	EP9206-015	45	EP9206-045	175	EP9206-175
16	EP9206-016	50	EP9206-050	200	EP9206-200
17	EP9206-017	55	EP9206-055	225	EP9206-225
18	EP9206-018	60	EP9206-060	250	EP9206-250

† Whichever is greater. Delay Times referenced from input to leading edges at 25°C, 5.0V, with no load.

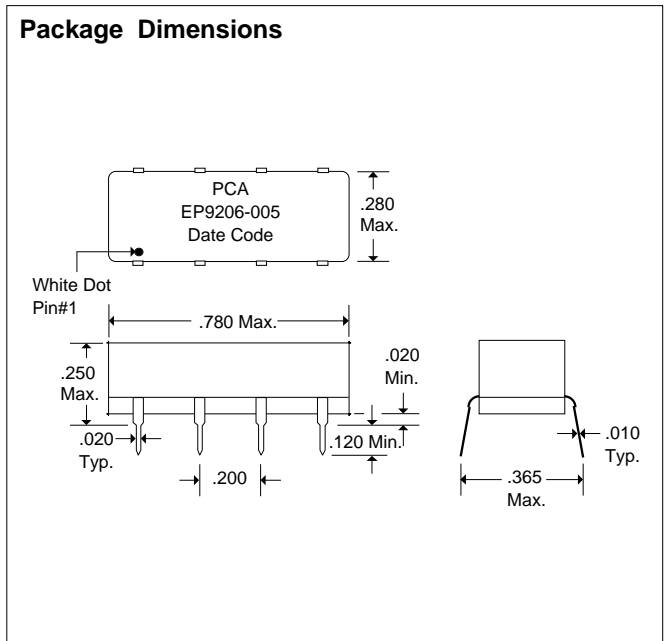
DC Electrical Characteristics		Test Conditions	Min	Max	Unit
V <sub>OH</sub>	High-Level Output Voltage	V <sub>CC</sub> = min. V <sub>IL</sub> = max. I <sub>OH</sub> = max	2.7		V
V <sub>OL</sub>	Low-Level Output Voltage	V <sub>CC</sub> = min. V <sub>IH</sub> = min. I <sub>OL</sub> = max		0.5	V
V <sub>IK</sub>	Input Clamp Voltage	V <sub>CC</sub> = min. I <sub>I</sub> = I <sub>IK</sub>		-1.2V	V
I <sub>IH</sub>	High-Level Input Current	V <sub>CC</sub> = max. V <sub>IN</sub> = 2.7V		50	µA
		V <sub>CC</sub> = max. V <sub>IN</sub> = 5.25V		1.0	mA
I <sub>IL</sub>	Low-Level Input Current	V <sub>CC</sub> = max. V <sub>IN</sub> = 0.5V		-2	mA
I <sub>OS</sub>	Short Circuit Output Current	V <sub>CC</sub> = max. V <sub>OUT</sub> = 0. (One output at a time)	-40	-100	mA
I <sub>CCH</sub>	High-Level Supply Current	V <sub>CC</sub> = max. V <sub>IN</sub> = OPEN		115	mA
I <sub>CCL</sub>	Low-Level Supply Current	V <sub>CC</sub> = max. V <sub>IN</sub> = 0		115	mA
T <sub>RO</sub>	Output Rise Time	T <sub>d</sub> 500 nS (0.75 to 2.4 Volts)		4	nS
N <sub>H</sub>	Fanout High-Level Output	V <sub>CC</sub> = max. V <sub>OH</sub> = 2.7V		20 TTL LOAD	
N <sub>L</sub>	Fanout Low-Level Output	V <sub>CC</sub> = max. V <sub>OL</sub> = 0.5V		10 TTL LOAD	



Recommended Operating Conditions		Min	Max	Unit
V <sub>CC</sub>	Supply Voltage	4.75	5.25	V
V <sub>IH</sub>	High-Level Input Voltage	2.0		V
V <sub>IL</sub>	Low-Level Input Voltage		0.8	V
I <sub>IK</sub>	Input Clamp Current		-18	mA
I <sub>OH</sub>	High-Level Output Current		-1.0	mA
I <sub>OL</sub>	Low-Level Output Current		20	mA
PW*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T <sub>A</sub>	Operating Free-Air Temperature	0	+70	°C

\*These two values are inter-dependent.

Input Pulse Test Conditions @ 25° C		Unit
E <sub>IN</sub>	Pulse Input Voltage	3.2 Volts
P <sub>W</sub>	Pulse Width % of Total Delay	110 %
T <sub>RI</sub>	Pulse Rise Time (0.75 - 2.4 Volts)	2.0 nS
P <sub>RR</sub>	Pulse Repetition Rate @ T <sub>d</sub> 200 nS	1.0 MHz
	Pulse Repetition Rate @ T <sub>d</sub> > 200 nS	100 KHz
V <sub>CC</sub>	Supply Voltage	5.0 Volts



DSD9206 Rev. A 2/5/96  
Unless Otherwise Noted Dimensions in Inches  
Tolerances:  
Fractional = ± 1/32  
.XX = ± .030 .XXX = ± .010



QAF-CS01 Rev. B 8/25/94  
16799 SCHOENBORN ST.  
NORTH HILLS, CA 91343  
TEL: (818) 892-0761  
FAX: (818) 894-5791