

## Discrete POWER & Signal Technologies

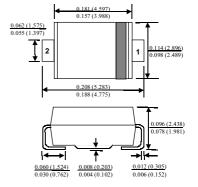
# **ES1A - ES1D**

### **Features**

- For surface mount applications.
- Glass passivated junction.
- · Low profile package.
- Easy pick and place.
- · Built-in strain relief.
- Superfast recovery times for high efficiency.



SMA/DO-214AC COLOR BAND DENOTES CATHODE



# 1.0 Ampere Superfast Rectifiers

# **Absolute Maximum Ratings\***

T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
Io	Average Rectified Current @ T <sub>A</sub> = 120°C	1.0	А
İf(surge)	Peak Forward Surge Current  8.3 ms single half-sine-wave Superimposed on rated load (JEDEC method)	30	А
P <sub>D</sub>	Total Device Dissipation  Derate above 25°C	1.47 11.76	W mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient**	85	°C/W
R <sub>θJL</sub>	Thermal Resistance, Junction to Lead**	35	°C/W
T <sub>stg</sub>	Storage Temperature Range	-50 to +150	°C
TJ	Operating Junction Temperature	-50 to +150	°C

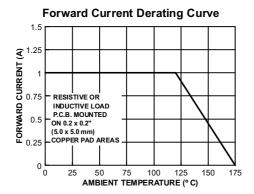
 $<sup>{\</sup>color{red}^{\bigstar}} These \ ratings \ are \ limiting \ values \ above \ which \ the \ service ability \ of \ any \ semiconductor \ device \ may \ be \ impaired.$ 

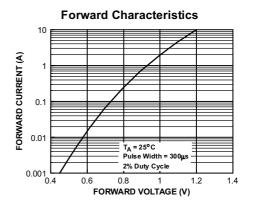
## Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise noted

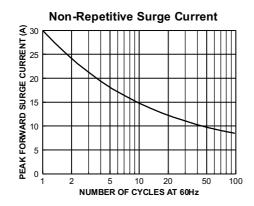
Parameter	Device				Units
	1A	1B	1C	1D	
Peak Repetitive Reverse Voltage	50	100	150	200	V
Maximum RMS Voltage	35	70	105	140	V
DC Reverse Voltage (Rated V <sub>R</sub> )	50	100	150	200	V
Maximum Reverse Current @ rated $V_R$ $T_A = 25^{\circ}C$ $T_A = 100^{\circ}C$	5.0 100				μ <b>Α</b> μ <b>Α</b>
Maximum Reverse Recovery Time $I_F = 0.5 \text{ A}$ , $I_R = 1.0 \text{ A}$ , $I_{RR} = 0.25 \text{ A}$	15				nS
Maximum Forward Voltage @ 1.0 A	0.92				V
Typical Junction Capacitance $V_R = 4.0 \text{ V}, f = 1.0 \text{ MHz}$		7.0	)		pF

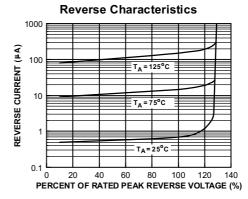
<sup>\*\*</sup>Device mounted on FR-4 PCB 0.013 mm.

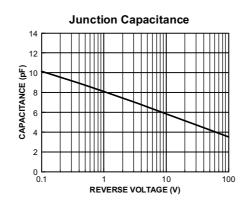
# **Typical Characteristics**

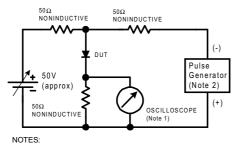


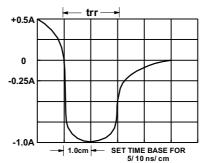












1. Rise time = 7.0 ns max; Input impedance = 1.0 megaohm 22 pf. 2. Rise time = 10 ns max; Source impedance = 50 ohms.

Reverse Recovery Time Characterstic and Test Circuit Diagram

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