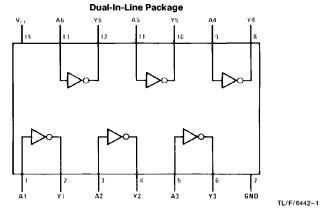
June 1989

## DM54S04/DM74S04 Hex Inverting Gates

#### **General Description**

This device contains six independent gates each of which performs the logic INVERT function.

#### **Connection Diagram**



Order Number DM54S04J, DM54S04W, DM74S04M or DM74S04N See NS Package Number J14A, M14A, N14A or W14B

#### **Function Table**

$\mathbf{Y} = \mathbf{A}$						
Input Output						
A	Y					
L	I					
Н	L					

H = High Logic LevelL = Low Logic Level

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#### **Absolute Maximum Ratings (Note)**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 5.5V
Operating Free Air Temperature Range

DM54S -55°C to +125°C DM74S 0°C to +70°C

Storage Temperature Range -65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

#### **Recommended Operating Conditions**

Symbol	Parameter	DM54S04			DM74S04			Units
		Min	Nom	Max	Min	Nom	Max	Oilles
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	4.75	5	5.25	٧
V <sub>IH</sub>	High Level Input Voltage	2			2			٧
V <sub>IL</sub>	Low Level Input Voltage			0.8			0.8	٧
Іон	High Level Output Current			-1			-1	mA
loL	Low Level Output Current			20			20	mA
T <sub>A</sub>	Free Air Operating Temperature	-55		125	0		70	°C

## **Electrical Characteristics** over recommended operating free air temperature range (unless otherwise noted)

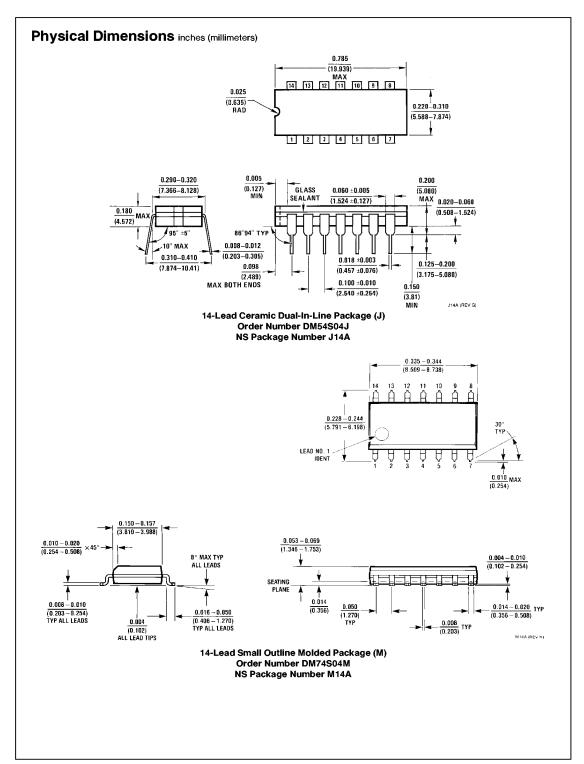
Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{\rm CC}=$ Min, $I_{\rm I}=-18$ mA				-1.2	V
V <sub>OH</sub> High Level Output Voltage	V <sub>CC</sub> = Min, I <sub>OH</sub> = Max	DM54	2.5	3.4		V	
	Voltage	V <sub>IL</sub> = Max	DM74	2.7	3.4		] '
V <sub>OL</sub>	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min$				0.5	v
l <sub>l</sub>	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
I <sub>IH</sub>	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$				50	μΑ
I <sub>IL</sub>	Low Level Input Current	$V_{CC} = Max, V_I = 0.5V$				-2	mA
los	I <sub>OS</sub> Short Circuit	V <sub>CC</sub> = Max	DM54	-40		-100	mA.
Output Current	Output Current	(Note 2)	DM74	-40		-100	
Іссн	Supply Current with Outputs High	V <sub>CC</sub> = Max			15	24	mA
IccL	Supply Current with Outputs Low	V <sub>CC</sub> = Max			30	54	mA

## $\textbf{Switching Characteristics} \text{ at V}_{CC} = 5 \text{V and T}_{A} = 25^{\circ}\text{C (See Section 1 for Test Waveforms and Output Load)}$

	Parameter		ı			
Symbol		C <sub>L</sub> =	15 p <b>F</b>	C <sub>L</sub> =	Units	
		Min	Max	Min	Max	
<sup>†</sup> PLH	Propagation Delay Time Low to High Level Output	2	4.5	2	7	ns
<sup>†</sup> PHL	Propagation Delay Time High to Low Level Output	2	5	2	8	ns

Note 1: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25$ °C.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.



# Physical Dimensions inches (millimeters) (Continued) 14 13 12 11 10 9 8 14 13 12 INGEX AREA $\frac{0.250 \pm 0.010}{(6.350 \pm 0.254)}$ 1 2 3 4 5 6 7 0.092 (2.337) DIA 0.030 MAX (0.762) DEPTH 0.125 - 0.150 (3.175 - 3.810) - 0.075 ±0.015 (1.905 ±0.381) 0.014 -0.023 TYP (0.356 - 0.584) 0.050 ± 0.010 (1.270 0.254) TYP 0.325 + 0.040 $\left(8.255 \begin{array}{c} +1.016 \\ -0.381 \end{array}\right)$ 14-Lead Molded Dual-In-Line Package (N) Order Number DM74S04N NS Package Number N14A 0.045 0.026 14-Lead Ceramic Flat Package (W) Order Number DM54S04W NS Package Number W14B

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