

SMD multilayer varistor with Ni-barrier termination

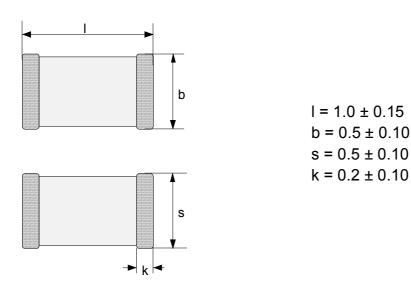
CT0402M4G

B72590T0040M060

Designation system

- CT = <u>**C**</u>hip with <u>three-layer-termination (Ag/Ni/Sn)</u>
- 0402 = Dimensions of the device $\underline{04} \times \underline{02}$ (length x width in 1/100 inch)
- L = Tolerance of the varistor voltage (± 20%)
- 4 = Maximum operating voltage (RMS voltage)
- G = Taped version (cardboard tape, 7" reel, 10000 pieces/reel)

Figure



(all dimensions in mm)

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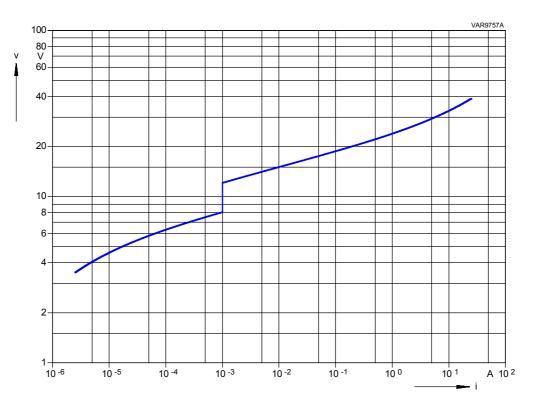


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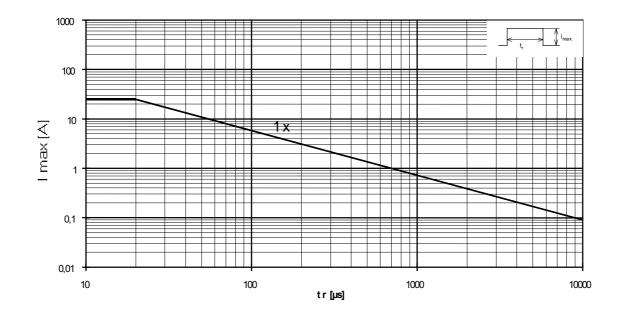
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V-I-characteristic



Derating field



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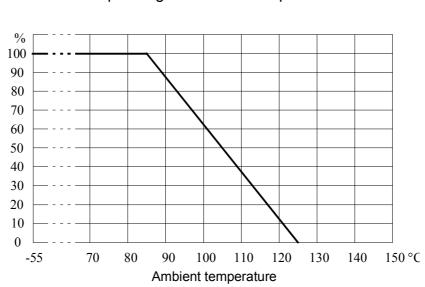


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Electrical data

| Maximum operating voltage | |
|---|------------------------------|
| RMS voltage | $V_{RMS} = 4 V$ |
| DC voltage | V _{DC} = 5.5 V |
| Varistor voltage (@ 1 mA) | V_V = 8 up to 12 V |
| Clamping voltage (@ 1 A, typical) | V _c = 19 V |
| Maximum clamping voltage (@ 1 A) | $V_{c} = 24 V$ |
| Maximum average power dissipation | P _{max} = 3 mW |
| Maximum surge current (8/20 μs) | I _{max} = 1 x 25 A |
| Maximum energy absorption (2 ms) | E _{max} = 1 x 17 mJ |
| Capacitance (@ 1 kHz, 1 V, 25 °C, typical) | C = 200 pF |
| Response time | < 0.5 ns |
| Operating temperature | –40 … +85 °C |
| Storage temperature (mounted parts) | –40 … +125 °C |
| Termination material | Ag/Ni/Sn |
| (thickness not specified, adjusted to fulfil wettability specific IEC 60068-2-58) | cation according to |



Max. current, energy and average power dissipation depending on ambient temperature

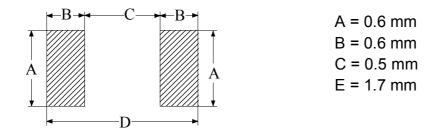
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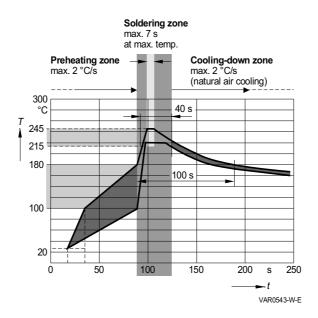
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Recommended geometry of solder pad



Recommended soldering temperature profile



This component should be soldered within 12 months after delivery from EPCOS. They should be left in their original packings to avoid soldering problems due to oxidized terminals. Storage temperature: -25 to 45 °C

Relative humidity: < 75% annual average, < 95% on maximum 30 days in a year.

The usage of mild non-activated fluxes for soldering is recommended, as well as proper cleaning of the PCB.

The components are suited for Pb-free soldering.

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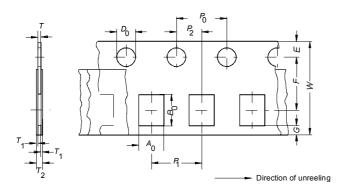
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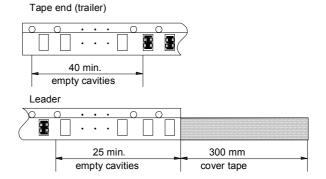
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Taping according to IEC 60286-3

Tape material: cardboard





Dimensions and tolerances:

| Definition | Symbol | Dimension | Tolerance |
|--|----------------|-----------|---------------------|
| | | [mm] | [mm] |
| Compartment width | A ₀ | 0.6 | ± 0.2 |
| Compartment length | B ₀ | 1.15 | ± 0.2 |
| Sprocket hole diameter | D ₀ | 1.5 | +0.1/0 |
| Sprocket hole pitch | P ₀ | 4.0 | ± 0.1 ¹⁾ |
| Distance center hole to center compartment | P ₂ | 2.0 | ± 0.05 |
| Pitch of the component compartments | P ₁ | 2.0 | ± 0.1 |
| Tape width | W | 8.0 | ± 0.3 |
| Distance edge to center of hole | E | 1.75 | ± 0.1 |
| Distance center hole to center compartment | F | 3.5 | ± 0.05 |
| Distance compartment to edge | G | 0.75 | min. |
| Overall thickness | T ₂ | 0.6 | max. |
| Thickness of cardboard tape | Т | 0.3 | max. |

 $^{1)} \le \pm 0.2$ mm over any 10 pitches



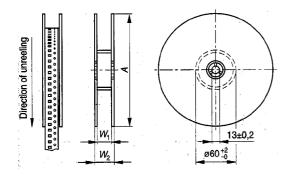
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Packing

Reel material: plastic

Packing unit: 10000 pcs./reel

Reel dimensions:



| Definition | Symbol | Dimension | Tolerance |
|-------------------------|----------------|-----------|-----------|
| | | [mm] | [mm] |
| Reel diameter | А | 180 | +0/ _3 |
| Reel width (inside) | W ₁ | 8.4 | +1.5/ –0 |
| Reel width (outside) | W ₂ | 14.4 | max. |

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