# Innovative Service Around the Globe YACEO

# **DATA SHEET**

SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS

Mid-voltage NP0/X7R

100 V TO 630 V

0.47 pF to 1 μF

**RoHS compliant & Halogen Free** 



YAGEO Phicomp



#### SCOPE

This specification describes Midvoltage NP0/X7R series chip capacitors with lead-free terminations.

#### **APPLICATIONS**

- PCs, Hard disk, Game PCs
- Power supplies
- LCD panel
- ADSL, Modem

#### FEATURES

- Supplied in tape on reel
- Nickel-barrier end termination
- RoHS compliant
- Halogen Free compliant

# ORDERING INFORMATION-GLOBAL PART NUMBER, PHYCOMP

#### **CTC & 12NC**

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value.

#### YAGEO BRAND ordering code

#### **GLOBAL PART NUMBER (PREFERRED)**

<u>xxxx x x xxx x</u> B <u>x xxx</u>

(2) (3) (4) (5)

#### (I) SIZE - INCH BASED (METRIC)

0402 (1005) / 0603 (1608) / 0805 (2012) / 1206 (3216) / 1210 (3225) 1808 (4520) / 1812 (4532)

#### (2) TOLERANCE

 $C = \pm 0.25 \text{ pF}$ 

 $D = \pm 0.5 pF$ 

 $G = \pm 2\%$ 

 $J = \pm 5\%$ 

 $K = \pm 10\%$ 

#### (3) PACKING STYLE

R = Paper/PE taping reel; Reel 7 inch

K = Blister taping reel; Reel 7 inch

P = Paper/PE taping reel; Reel 13 inch

F = Blister taping reel; Reel 13 inch

C = Bulk case

#### (4) TC MATERIAL

NPO

X7R

#### (5) RATED VOLTAGE

 $0 = 100 \vee$ 

A = 200 V

Y = 250 V

B = 500 V

Z = 630 V

#### (6) PROCESS

N = NP0

B = Class 2 MLCC

#### (7) CAPACITANCE VALUE

2 significant digits+number of zeros

The 3rd digit signifies the multiplying factor, and letter R is decimal point

Example:  $121 = 12 \times 10^{1} = 120 \text{ pF}$ 

Product specification

## **Surface-Mount Ceramic Multilayer Capacitors**

#### **PHYCOMP BRAND** ordering codes

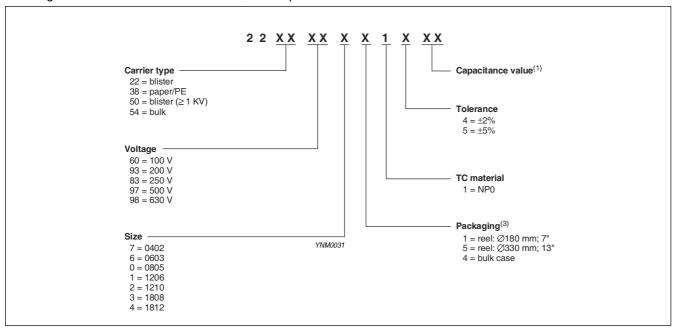
GLOBAL PART NUMBER (preferred), PHYCOMP CTC (for North America) and I2NC (traditional) codes are acceptable to order Phycomp brand products.

#### GLOBAL PART NUMBER (PREFERRED)

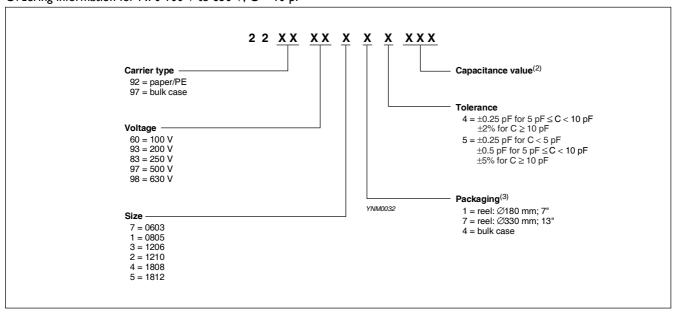
For detailed information of GLOBAL PART NUMBER and ordering example, please refer to page 2.

#### 12NC CODE

Ordering information for NP0 100 V to 630 V, C ≥ 10 pF



Ordering information for NP0 100 V to 630 V, C < 10 pF



- (I) Please refer to "Last 2-digit of I2NC" in "CAPACITANCE RANGE & THICKNESS FOR NP0"
- (2) Please refer to "Last 3-digit of 12NC" in "CAPACITANCE RANGE & THICKNESS FOR NP0"
- (3) Quantity on reel depends on thickness classification; see table 14

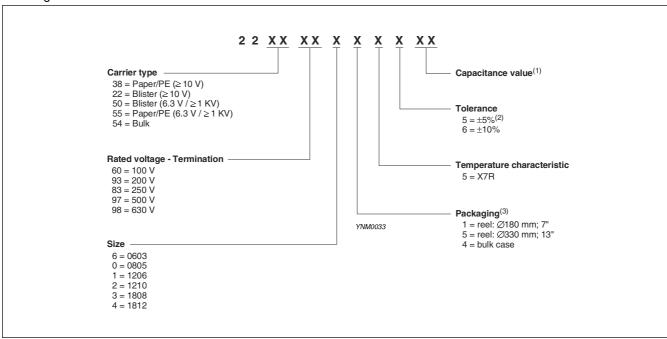


Product specification

#### **Surface-Mount Ceramic Multilaver Capacitors** Mid-voltage

NP0/X7R 100 V to 630 V

#### Ordering information for X7R 100 V to 630 V



- (I) Please refer to "Last 2-digit of I2NC" in "CAPACITANCE RANGE & THICKNESS FOR X7R"
- (2) Tolerance ±5% doesn't available for full product range, please contact local sales force before order
- (3) Quantity on reel depends on thickness classification; see table 14

## PHYCOMP CTC CODE (FOR NORTH AMERICA)

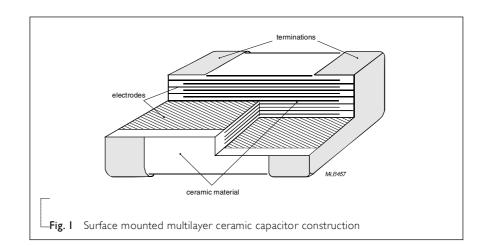
# ● Example: 0603CG101J0B200

0603	CG	101	J	0	В	2	0	0
Size code	Temp. Char.	Capacitance in pF	Tolerance	Voltage	Termination	Packing	Marking	Range identifier
0402 0603 0805 1206 1210 1808 1812	CG = NP0 2R = X7R	$101 = 100$ pF; the third digit signifies the multiplying factor: $0 = \times 1$ $1 = \times 10$ $2 = \times 100$ $3 = \times 1,000$	$C = \pm 0.25 \text{ pF}$ $D = \pm 0.5 \text{ pF}$ $G = \pm 2\%$ $J = \pm 5\%$ $K = \pm 10\%$	0 = 100  V B = 200  V C = 250  V D = 500  V Z = 630  V		2 = 180 mm 7" Paper/PE 3 = 330 mm 13" Paper/PE B = 180 mm 7" Blister F = 330 mm 13" Blister P = Bulk case	0 = no marking	0 = conv. Ceramic D = Class 2 MLCC

#### **CONSTRUCTION**

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. I.

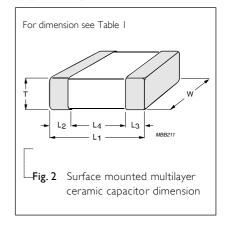


#### **DIMENSION**

**Table I** For outlines see fig. 2

TYPE	L <sub>I</sub> (mm)	W (mm)	T (MM)		3 (mm)	L <sub>4</sub> (mm)
				min.	max.	min.
0402	1.0 ±0.10	0.5 ±0.05		0.15	0.30	0.40
0603	1.6 ±0.10	0.8 ±0.10		0.20	0.60	0.40
0805	2.0 ±0.20	1.25 ±0.20		0.25	0.75	0.55
1206	3.2 ±0.30	1.6 ±0.20	Refer to table 2 to 13	0.25	0.75	1.40
1210	3.2 ±0.30	2.5 ±0.20		0.25	0.75	1.40
1808	4.5 ±0.40	2.0 ±0.30		0.25	0.75	2.20
1812	4.5 ±0.40	3.2 ±0.30		0.25	0.75	2.20

#### **OUTLINES**



#### - Trincomp

# **Surface-Mount Ceramic Multilayer Capacitors** Mid-voltage

#### d-voltage NP0/X7R 100 V to 630 V

#### CAPACITANCE RANGE & THICKNESS FOR NPO

Table 2	Sizes from	0603 to	0805
I able 2	31ZC3 11 O111	0005 10	0000

CAP.	Last 3-digit of I2NC	0402 100V	0603 100 V	200 V	250 V	0805 100 V	200 V	250 V	500 V	630V
0.47 pF	477									
0.56 pF	567									
0.68 pF	687									
0.82 pF	827									
1.0 pF	108									
1.2 pF	128									0.6±0.1
1.5 pF	158				I 0.8±0.1					
1.8 pF	188	0.5.1.0.05	00101	00101		07101	07101	0.6±0.1	06101	
2.2 pF	228	0.5±0.05	0.8±0.1	0.8±0.1		0.6±0.1	0.6±0.1 (	0.6±0.1	6±0.1 0.6±0.1	
2.7 pF	278									
3.3 pF	338									
3.9 pF	398									
4.7 pF	478									
5.6 pF	568									
6.8 pF	688									
8.2 pF	828									

#### **Table 3** Sizes from 0402 to 0805

CAP.	Last 2-digit of	0402	0603			0805				
	12NC	100 V	100 V	200 V	250 V	100 V	200 V	250 V	500 V	630V
10 pF	23		_							
12 pF	24									
15 pF	25									
18 pF	26									
22 pF	27									
27 pF	28									
33 pF	29	0.5±0.05	0.8±0.1	0.8±0.1	0.8±0.1	0.6±0.1	0.6±0.1	0.6±0.1	0.6±0.1	0.6±0.1
39 pF	31									
47 pF	32									
56 pF	33									
68 pF	34									
82 pF	35									

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request



# Surface-Mount Ceramic Multilayer Capacitors Mid-voltage

NP0/X7R 100 V to 630 V

#### CAPACITANCE RANGE & THICKNESS FOR NPO

Table 4 Sizes from 0402 to 0805 (continued)

CAP.	Last 2-digit of	0402	0603			0805				
	12NC	100 V	100 V	200 V	250 V	100 V	200 V	250 V	500 V	630 V
100 pF	36	0.5±0.05								
120 pF	37						0.6±0.1	0.6±0.1	0.6±0.1	0.6±0.1
150 pF	38						0.0±0.1	0.010.1	0.010.1	0.0±0.1
180 pF	39									
220 pF	41									
270 pF	42			0.8±0.1	0.8±0.1					
330 pF	43					0.6±0.1			0.85±0.1	0.85±0.1
390 pF	44									
470 pF	45						0.85±0.1	0.85±0.1		
560 pF	46		0.8±0.1							
680 pF	47								125102	125102
820 pF	48								1.25±0.2	1.25±0.2
I.O nF	49									
I.2 nF	51									
1.5 nF	52					0.85±0.1				
I.8 nF	53									
2.2 nF	54						125102	125102		
2.7 nF	55						1.25±0.2	1.25±0.2		
3.3 nF	56					1.25±0.2				
3.9 nF	57									
4.7 nF	58									
5.6 nF	59									
6.8 nF	61									
8.2 nF	62									
IO nF	63									
I2 nF	64									
15 nF	65									
18 nF	66									
22 nF	67									

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request



# **Surface-Mount Ceramic Multilayer Capacitors** Mid-voltage

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#### CAPACITANCE RANGE & THICKNESS FOR NPO

Table 5	Sizes from	1206 to	1210
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CAP.	Last 3-digit of	1206					1210				
	12NC	100 V	200 V	250 V	500 V	630 V	100 V	200 V	250 V	500 V	630 V
0.47 pF	477								-	-	
0.56 pF	567										
0.68 pF	687										
0.82 pF	827										
1.0 pF	108										
1.2 pF	128										
1.5 pF	158										
1.8 pF	188	07101	04101	0 ( , 0 )							
2.2 pF	228	0.6±0.1	0.6±0.1	0.6±0.1							
2.7 pF	278										
3.3 pF	338										
3.9 pF	398										
4.7 pF	478										
5.6 pF	568										
6.8 pF	688										
8.2 pF	828										

lable	6 Sizes from 12	.06 to 1210	)								
CAP.	Last 2-digit of	1206					1210				
	12NC	100 V	200 V	250 V	500 V	630 V	100 V	200 V	250 V	500 V	630 V
10 pF	23										
12 pF	24										
15 pF	25										
18 pF	26										
22 pF	27										
27 pF	28	0 ( ) 0 1	0 ( , 0 )	0 ( ) 0	0 ( ) 0 1	125.02					
33 pF	29	0.6±0.1	0.6±0.1	0.6±0.1	0.6±0.1	1.25±0.2					
39 pF	31										
47 pF	32										
56 pF	33										1.25±0.2
68 pF	34						1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	
82 pF	35										

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request

Product specification

# Surface-Mount Ceramic Multilayer Capacitors Mid-voltage

oltage NP0/X7R 100 V to 630 V

#### CAPACITANCE RANGE & THICKNESS FOR NPO

Table 7 Sizes from 1206 to 1210 (continued)

CAP.	Last 2-digit of	1206	. (	-,			1210				
	12NC	100 V	200 V	250 V	500 V	630 V	100 V	200 V	250 V	500 V	630 V
100 pF	36										
120 pF	37										
150 pF	38										1.25±0.2
180 pF	39										
220 pF	41										
270 pF	42		0.6±0.1	0.6±0.1	0.6±0.1						
330 pF	43					1.25±0.2					
390 pF	44									1.25±0.2	
470 pF	45	0.6±0.1						1.25+0.2	1.25±0.2	1.25±0.2	
560 pF	46	0.0±0.1						1.25±0.2	1.25±0.2		
680 pF	47										
820 pF	48										
I.O nF	49		0.85±0.1	0.85±0.1	0.85±0.1		1.25±0.2				
1.2 nF	51				0.05±0.1						
1.5 nF	52										
1.8 nF	53				1.25±0.2						
2.2 nF	54		125±02	1.25±0.2	1.25±0.2						
2.7 nF	55		1.25±0.2	1,23±0,2							
3.3 nF	56										
3.9 nF	57										
4.7 nF	58	0.85±0.1									
5.6 nF	59										
6.8 nF	61										
8.2 nF	62	1.25±0.2									
I0 nF	63	1,2310,2									
I2 nF	64										
15 nF	65										
18 nF	66										
22 nF	67										

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request



#### CAPACITANCE RANGE & THICKNESS FOR NPO

Table 8				
CAP.	Last 2-digit of	1812		
	12NC	100 V	200 V	500 V
10 pF	23			
12 pF	24			
15 pF	25			
18 pF	26			
22 pF	27			
27 pF	28			
33 pF	29			
39 pF	31			
47 pF	32			
56 pF	33			
68 pF	34			1.25±0.2
82 pF	35			

#### NOTE

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request

Table 9	<b>9</b> Sizes 1812 (c	ontinued)		
CAP.	Last 2-digit of	1812		
	12NC	100 V	200 V	500 V
100 pF	36			
120 pF	37			
150 pF	38			
180 pF	39			
220 pF	41			
270 pF	42			
330 pF	43			
390 pF	44			
470 pF	45			
560 pF	46			
680 pF	47			1.25±0.2
820 pF	48			
l nF	49			
I.2 nF	51	1.25±0.2	1.25±0.2	
1.5 nF	52	1.23±0.2	1,23±0,2	
1.8 nF	53			
2.2 nF	54			
2.7 nF	55			
3.3 nF	56			
3.9 nF	57			
4.7 nF	58			
5.6 nF	59			
6.8 nF	61			
8.2 nF	62			
I0 nF	63			
I2 nF	64			
15 nF	65			
18 nF	66			
22 nF	67			

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request

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# Surface-Mount Ceramic Multilayer Capacitors Mid-voltage

NP0/X7R 100 V to 630 V

# CAPACITANCE RANGE & THICKNESS FOR X7R

**Table 10** Sizes from 0603 to 0805

CAP.	Last 2-digit of	0402	0603	0805			
	12NC	100 V	100 V	100 V	200 V	250 V	500 V
100 pF	09						
150 pF	12						
220 pF	14						
330 pF	16						
470 pF	18						
680 pF	21						
1.0 nF	23	0.5±0.05		07101	0.85±0.1	0.85±0.1	0.85±0.1
1.5 nF	25			0.6±0.1 (3) 0.85±0.1			
2.2 nF	27			*****			
3.3 nF	29		0.8±0.1				
4.7 nF	32						
6.8 nF	34						
10 nF	36				1.25±0.2	1.25±0.2	1.25±0.2
15 nF	38			0.85±0.1	1.23±0.2	1.23±0.2	
22 nF	41			0.05±0.1			
33 nF	43						
47 nF	45			1.25±0.2			
68 nF	47			1.23±0.2			
100 nF	49						
150 nF	52						
220 nF	54						
330 nF	56						
470 nF	58						

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is on request
- 3. For special ordering code, please contact local sales force before order
- 4. For product with 5% tolerance, please contact local sales force before order

Product specification 12

**Surface-Mount Ceramic Multilayer Capacitors** Mid-voltage

NP0/X7R 100 V to 630 V

#### CAPACITANCE RANGE & THICKNESS FOR X7R

Table I I	Sizes from 1206 to 1210	

CAP.	Last 2-digit of I2NC	1206 100 V	200 V	250 V	500 V	630 V	1210 100 V	200 V	250 V	500 V
100 pF	09									
150 pF	12									
220 pF	14									
330 pF	16									
470 pF	18									
680 pF	21									
I.O nF	23									
1.5 nF	25		0.05.10.1	0.05.10.1		125102				
2.2 nF	27		0.85±0.1	0.85±0.1	1.25±0.2	1.25±0.2				
3.3 nF	29	0.85±0.1								
4.7 nF	32							0.05.10.1	0.05 1.0 1	
6.8 nF	34							0.85±0.1	0.85±0.1	
IO nF	36									1.25±0.2
15 nF	38				_		0.85±0.1			1,23±0,2
22 nF	41					1.6±0.2				
33 nF	43		1.25±0.2	1.25±0.2	1.6±0.2					
47 nF	45		1.23±0.2	1.23±0.2				1.25±0.2	1.25±0.2	
68 nF	47									
100 nF	49	1.25±0.2	1.6±0.2	1.6±0.2						
150 nF	52	1.25±0.2					1.25±0.2			
220 nF	54						1.25±0.2			
330 nF	56	17102								
470 nF	58	1.6±0.2								
680 nF	61									
ΙμF	63	1.6±0.2					2.0±0.2			

- I. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is on request
- 3. For product with 5% tolerance, please contact local sales force before order

# **Surface-Mount Ceramic Multilayer Capacitors** Mid-voltage

NP0/X7R 100 V to 630 V

# CAPACITANCE RANGE & THICKNESS FOR X7R

**Table 12** Sizes from 1808 to 1812

CAP.	Last 2-digit of	1808				1812				
	12NC	100 V	200 V	250 V	500 V	100 V	200 V	250 V	500 V	630 V
100 pF	09									
150 pF	12									
220 pF	14									
330 pF	16									
470 pF	18									
680 pF	21									
I.O nF	23									
I.5 nF	25									
2.2 nF	27									
3.3 nF	29									
4.7 nF	32									1.35±0.2
6.8 nF	34									
IO nF	36				1.25±0.2		0.85±0.1	0.85±0.1		
15 nF	38	1.25±0.2	1.25±0.2	1.25±0.2	1,23±0,2	0.85±0.1	0.03±0.1	0.05±0.1	1.25±0.2	
22 nF	41	1.23±0.2	1.23±0.2	1.23±0.2		0.03±0.1				
33 nF	43									1.6±0.2
47 nF	45									
68 nF	47						1.25±0.2	1.25±0.2		
100 nF	49						1.23±0.2	1.23±0.2	1.6±0.2	
150 nF	52					1.25±0.2				
220 nF	54					1.25±0.2				
330 nF	56						1.6±0.2	1.6±0.2		
470 nF	58									
680 nF	61					1.6±0.2				
ΙμF	63									

- I. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is on request
- 3. For product with 5% tolerance, please contact local sales force before order

# Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

# THICKNESS CLASSES AND PACKING QUANTITY

Table 13

CIZE	TI II CIA IECC	TARE VALID THE	Ø180 MM	/7 INCH	Ø330 MM /	/ 13 INCH	OLIANITITY
SIZE CODE	THICKNESS CLASSIFICATION	TAPE WIDTH QUANTITY PER REEL	Paper	Blister	Paper	Blister	QUANTITY PER BULK CASE
0201	0.3 ±0.03 mm	8 mm	15,000		50,000		
0402	0.5 ±0.05 mm	8 mm	10,000		50,000		50,000
0603	0.8 ±0.1 mm	8 mm	4,000		15,000		15,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		10,000
0805	0.8 / 0.85 ±0.1 mm	8 mm	4,000		15,000		8,000
	1.25 ±0.2 mm	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		
	0.8 / 0.85 ±0.1 mm	8 mm	4,000		15,000		
1206	1.00 / 1.15 ±0.1 mm	8 mm		3,000		10,000	
1206	1.25 ±0.2 mm	8 mm		3,000		10,000	
	1.6 ±0.15 mm	8 mm		2,500		10,000	
	1.6 ±0.2 mm	8 mm		2,000		8,000	
_	0.6 / 0.7 ±0.1 mm	8 mm		4,000		15,000	
	0.85 ±0.1 mm	8 mm		4,000		10,000	
	1.15 ±0.1 mm	8 mm		3,000		10,000	
	1.15 ±0.15 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000			
1210	1.5 ±0.1 mm	8 mm		2,000			
_	1.6 / 1.9 ±0.2 mm	8 mm		2,000			
	2.0 ±0.2 mm	8 mm		2,000 1,000			
	2.5 ±0.2 mm	8 mm		1,000 500			
	1.15 ±0.15 mm	I2 mm		3,000			
	1.25 ±0.2 mm	I2 mm		3,000			
1808	1.35 ±0.15 mm	I2 mm		2,000			
1000	1.5 ±0.1 mm	I2 mm		2,000			
_	1.6 ±0.2 mm	I2 mm		2,000		8,000	
	2.0 ±0.2 mm	I2 mm		2,000			
	0.6 / 0.85 ±0.1 mm	I2 mm		2,000			
	1.15 ±0.1 mm	12 mm		1,000			
	1.15 ±0.15 mm	I2 mm		1,000			
	1.25 ±0.2 mm	I2 mm		1,000			
1812	1.35 ±0.15 mm	I2 mm		1,000			
	1.5 ±0.1 mm	I2 mm		1,000			
	1.6 ±0.2 mm	I2 mm		1,000			
	2.0 ±0.2 mm	I2 mm		1,000			
	2.5 ±0.2 mm	I2 mm		500			

Mid-voltage NP0/X7R | 100 V to 630 V

#### **ELECTRICAL CHARACTERISTICS**

#### NP0/X7R DIELECTRIC CAPACITORS; NISN TERMINATIONS

**Surface-Mount Ceramic Multilayer Capacitors** 

Unless otherwise specified, all test and measurements shall be made under standard atmospheric conditions for testing as given in 5.3 of IEC 60068-1:

- Temperature: 15 °C to 35 °C - Relative humidity: 25% to 75% - Air pressure: 86 kPa to 106 kPa

Before the measurements are made, the capacitor shall be stored at the measuring temperature for a time sufficient to allow the entire capacitor to reach this temperature.

The period as prescribed for recovery at the end of a test is normally sufficient for this purpose.

Table	e 14	
DESCRIF	PTION	VALUE
Capacita	ince range	0.47 pF to I μF
Capacita	nce tolerance	
NP0	C < 10 <sub>P</sub> F	±0.25 pF, ±0.5 pF
	C ≥ 10 pF	±2%, ±5%
X7R		±5% <sup>(1)</sup> , ±10%
Dissipati	on factor (D.F.)	
NP0	C < 30 <sub>P</sub> F	≤ I / ( 400 + 20C )
	C ≥ 30 pF	≤ 0.1 %
X7R		≤ 2.5 %
Exceptio	on	0603/100V, 12nF ≤ C ≤ 100nF, ≤5%
Insulation	n resistance after 1 minute at U <sub>r</sub> (DC)	$R_{ins} \ge 10 \text{ G}\Omega$ or $R_{ins} \times C \ge 500$ seconds whichever is less
	n capacitance change as a function of temperature ature characteristic/coefficient):	
NP0		±30 ppm/°C
X7R		±15%
	ng temperature range:	
NP0/X	7R	-55 °C to +125 °C

#### NOTE

1. Capacitance tolerance ±5% doesn't available for X7R full product range, please contact local sales force before order



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	Surface-Mount Ceramic Multilayer Capacitors	Mid-voltage	NP0/X7R	100 V to 630 V

# SOLDERING RECOMMENDATION

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soldering Method	SIZE 0402	0603	0805	1206	≥ 1210
Reflow	≥ 0.1 µF	≥ 1.0 µF	≥ 2.2 µF	≥ 4.7 µF	Reflow only
Reflow/Wave	< 0.1 µF	< 1.0 µF	< 2.2 µF	< 4.7 µF	

## TESTS AND REQUIREMENTS

Table 16 Test procedures and requirements

TEST TEST M		TEST METHOD PROCEDURE		REQUIREMENTS
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage
Visual Inspection and Dimension Check		4.4	Any applicable method using × 10 magnification	In accordance with specification
Capacitance		4.5.1	Class I: $f = I \text{ MHz for C} \le I \text{ nF, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$ $f = I \text{ KHz for C} > I \text{ nF, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$ Class 2: $f = I \text{ KHz for C} \le I0  \mu\text{F, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$	Within specified tolerance
Dissipation Factor (D.F.)		4.5.2	Class I: $f = I \text{ MHz for C} \le I \text{ nF , measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$ $f = I \text{ KHz for C} > I \text{ nF, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$ Class 2: $f = I \text{ KHz for C} \le I0  \mu\text{F, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$	In accordance with specification
Insulation Resistance		4.5.3	$U_r \le 500 \text{ V: At Ur for I minute}$ $U_r > 500 \text{ V: At } 500 \text{ V for I minute}$	In accordance with specification
Temperature Coefficient		4.6	Class I:  Between minimum and maximum temperature  NP0: -55 °C to +125 °C  Normal Temperature: 20 °C	ΔC/C: Class I: NP0: ±30 ppm/°C
Temperature Characteristic			Class 2: Between minimum and maximum temperature X7R: -55 °C to +125 °C Normal Temperature: 20 °C	Class 2 X7R: ±15%

Surface-Mount Ceramic Multilayer Capacitors | Mid-voltage | NP0/X7R | 100 V to 630 V

TEST	TEST METH	HOD	PROCEDURE	REQUIREMENTS
Adhesion	IEC 60384- 21/22	4.7	A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate	Force size ≥ 0603: 5N
Bond Strength of		4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3	No visible damage
Plating on End Face			Conditions: bending I mm at a rate of I mm/s, radius jig 340 mm	$\Delta C/C$ Class 1: NP0: within $\pm 1\%$ or 0.5 pF, whichever is greater Class2: $\times 7R$ : $\pm 10\%$
Resistance to Soldering Heat		4.9	Precondition: $150 + 0/-10$ °C for 1 hour, then keep for $24 \pm 1$ hours at room temperature  Preheating: for size $\leq 1206$ : $120$ °C to $150$ °C for	Dissolution of the end face plating shall not exceed 25% of the length of the edge concerned
			I minute  Preheating: for size >1206: 100 °C to 120 °C for I minute and 170 °C to 200 °C for I minute  Solder bath temperature: 260 ±5 °C  Dipping time: 10 ±0.5 seconds  Recovery time: 24 ±2 hours	$\Delta C/C$ Class 1: NP0: within $\pm 0.5\%$ or 0.5 pF, whichever is greater Class2: $\times 7R$ : $\pm 10\%$
			-	D.F. within initial specified value R <sub>ins</sub> within initial specified value
Solderability		4.10	Preheated the temperature of 80 °C to 140 °C and maintained for 30 seconds to 60 seconds.  Test conditions for lead containing solder alloy	The solder should cover over 95% of the critical area of each termination
			Temperature: 235 ±5 °C Dipping time: 2 ±0.2 seconds Depth of immersion: 10 mm Alloy Composition: 60/40 Sn/Pb Number of immersions: 1	
			Test conditions for leadfree containing solder alloy Temperature: $245 \pm 5$ °C Dipping time: $3 \pm 0.3$ seconds Depth of immersion: $10 \text{ mm}$ Alloy Composition: SAC305 Number of immersions: $1 \pm 0.3$	

# Surface-Mount Ceramic Multilayer Capacitors | Mid-voltage | NP0/X7R | 100 V to 630 V

TEST	TEST METH	HOD	PROCEDURE	REQUIREMENTS
Rapid Change of	IEC 60384- 21/22	4.11	Preconditioning; 150 +0/–10 °C for I hour, then keep for	No visual damage
Temperature			24 ±1 hours at room temperature	ΔC/C
			5 cycles with following detail: 30 minutes at lower category temperature 30 minutes at upper category temperature	Class 1: NP0: within $\pm 1\%$ or 1 pF, whichever is greater Class2: X7R: $\pm 15\%$
			Recovery time 24 ±2 hours	D.F. meet initial specified value
				R <sub>ins</sub> meet initial specified value
Damp Heat		4.13	1. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for	No visual damage after recovery
			24 ±1 hour at room temp	∆C/C
			2. Initial measure: Spec: refer initial spec C, D, IR	Class 1: NP0: within ±2% or 1 pF, whichever is greater
			3. Damp heat test: 500 ±12 hours at 40 ±2 °C; 90 to 95% R.H.	Class2: X7R: ±15% D.F.
			4. Recovery: Class 1: 6 to 24 hours Class 2: 24 ±2 hours	Class I: NP0: $\leq 2 \times \text{specified value}$ Class2:
			5. Final measure: C, D, IR	X7R: ≥ 25 V: ≤ 5%
			P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be precondition according to "IEC 60384 4.1" and then the requirement shall be met.	$R_{ins}$ Class 1: NP0: $\geq$ 2,500 M $\Omega$ or $R_{ins} \times C_r \geq$ 25s whichever is less Class2: $\times$ 7R: $\geq$ 500 M $\Omega$ or $R_{ins} \times C_r \geq$ 25s whichever is less

# Surface-Mount Ceramic Multilayer Capacitors Mid-voltage

NP0/X7R 100 V to 630 V

TEST	TEST METH	IOD	PROCEDURE	REQUIREMENTS
Endurance	IEC 60384- 21/22	4.14	<ol> <li>Preconditioning, class 2 only:         150 +0/-10 °C /I hour, then keep for         24 ± I hour at room temp</li> <li>Initial measure:         Spec: refer initial spec C, D, IR</li> <li>Endurance test:         Temperature: NP0/X7R: 125 °C         Specified stress voltage applied for I,000 hours:         4. High voltage series follows with below stress condition:         Applied 2.0 × Ur for 100 V series         Applied 1.5 × Ur for 200/250 V series         Applied 1.3 × Ur for 500 V, 630 V series         Applied 1.2 × U<sub>r</sub> for I KV, 2 KV, 3 KV series</li> <li>Recovery time: 24 ±2 hours</li> <li>Final measure: C, D, IR</li> <li>P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be precondition according to "IEC 60384 4.1" and then the requirement shall be met.</li> </ol>	No visual damage $ \Delta C/C $ Class I: NP0: within $\pm 2\%$ or I pF, whichever is greater Class2: X7R: $\pm 15\%$ D.F. Class I: NP0: $\leq 2 \times$ specified value Class2: X7R: $\geq 25 \text{ V}: \leq 5\%$ R <sub>ins</sub> Class I: NP0: $\geq 4,000 \text{ M}\Omega$ or R <sub>ins</sub> $\times$ C <sub>r</sub> $\geq 400 \text{ M}\Omega$ or R <sub>ins</sub> $\times$ C <sub>r</sub> $\geq 400 \text{ M}\Omega$ or R <sub>ins</sub> $\times$ C <sub>r</sub> $\geq 50 \text{ S}$ whichever is less Class2: X7R: $\geq 1,000 \text{ M}\Omega$ or R <sub>ins</sub> $\times$ C <sub>r</sub> $\geq 50 \text{ S}$ whichever is less
Voltage Proof	IEC 60384-1	4.6	Specified stress voltage applied for 1 minute $U_r \le 100 \text{ V}$ : series applied 2.5 $U_r$ $100 \text{ V} < U_r \le 200 \text{ V}$ series applied (1.5 $U_r + 100$ ) $200 \text{ V} < U_r \le 500 \text{ V}$ series applied (1.3 $U_r + 100$ ) $U_r > 500 \text{ V}$ : 1.3 $U_r$ I: 7.5 mA	No breakdown or flashover

# Product specification 20 20 40 630 V

## REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 10	Nov 22, 2012	-	- Test method and procedure updated
Version 9	Feb 02, 2012	-	- Test method and procedure updated
Version 8	Apr 22, 2011	-	- NP0 0402 100V added
Version 7	Mar 01, 2011	-	- Dimension updated
Version 6	Sep 30, 2010	-	- Update the thickness of 0805 100V
Version 5	Sep 28, 2010	-	- Product range updated
			- Thickness classes and packing quantity table updated
Version 4	Jun 17, 2010	-	- Update the dimension of 0805, 1206 and 1812
Version 3	Mar 25, 2010	-	- Product range update
Version 2	Mar 15, 2010	-	- Product range update
Version I	Oct 30, 2009	-	- Change to dual brand datasheet that describe Mid-voltage NP0/X7R series with RoHS compliant
			- Replace the "100V to 630V" part of pdf files: UP-NP0X7R_MV_100-to-500V_0, UY-NP0X7R_MV_100-to-500V_0, NP0_16V-to-100V_6, NP0_50-to-500V_10, X7R_16-to-500V_9 and X7R_16V-to-100V_9
			- Define global part number
			- Description of "Halogen Free compliant" added
			- Test method and procedure updated
Version 0	Sep 08, 2005	-	- New