

V.H.F. POWER TRANSISTOR

N-P-N silicon planar epitaxial transistor for use in class-A, B and C operated mobile, industrial and military transmitters with a nominal supply voltage of 28 V. The transistor is resistance stabilized and is guaranteed to withstand severe load mismatch conditions.

It has a 1/4" capstan envelope with a moulded cap. All leads are isolated from the stud.

QUICK REFERENCE DATA

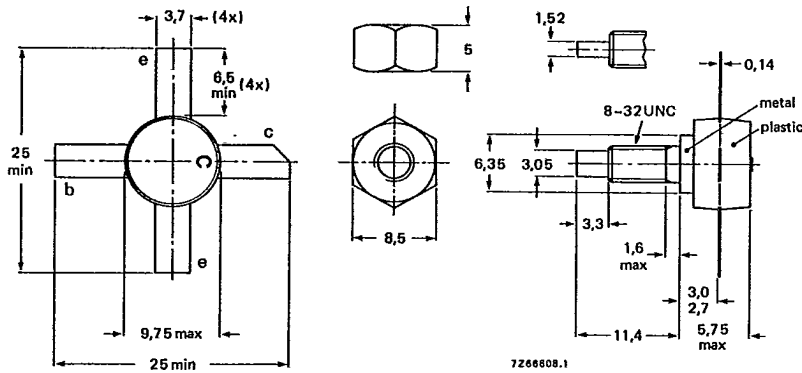
R.F. performance up to $T_{mb} = 25\text{ }^\circ\text{C}$ in an unneutralized common-emitter class-B circuit

mode of operation	V_{CE} V	f MHz	P_L W	G_p dB	η %	Z_i Ω	\bar{Y}_L mS
c.w.	28	175	8	> 12	> 65	$1,8 + j0,7$	$18 - j20$

MECHANICAL DATA

Dimensions in mm

Fig. 1 SOT-48/2



Torque on nut: min. 0,75 Nm
(7,5 kg cm)
max. 0,85 Nm
(8,5 kg cm)

Diameter of clearance hole in heatsink: max. 4,2 mm.
Mounting hole to have no burrs at either end.
De-burring must leave surface flat; do not chamfer or countersink either end of hole.

When locking is required an adhesive is preferred instead of a lock washer.

PRODUCT SAFETY This device incorporates beryllium oxide, the dust of which is toxic. The device is entirely safe provided that the BeO disc is not damaged.

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RATINGS Limiting values in accordance with the Absolute Maximum System (IEC 134)

Collector-base voltage (open emitter)
peak value

V_{CBOM} max. 65 V

Collector-emitter voltage (open base)

V_{CEO} max. 36 V

Emitter-base voltage (open collector)

V_{EBO} max. 4 V

Collector current (average)

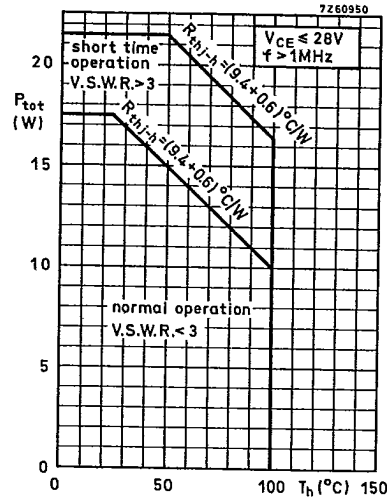
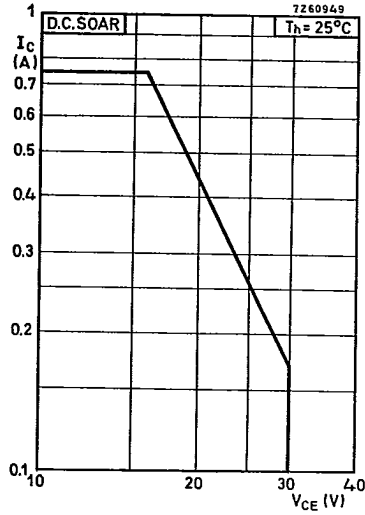
$I_{C(AV)}$ max. 0.75 A

Collector current (peak value) $f > 1$ MHz

I_{CM} max. 2.25 A

Total power dissipation up to $T_h = 25^\circ\text{C}$
 $f > 1$ MHz

P_{tot} max. 17.5 W



Storage temperature

T_{stg} -30 to +200 °C

Operating junction temperature

T_j max. 200 °C

THERMAL RESISTANCE

From junction to mounting base

$R_{th j-mb} = 9.4$ K/W

From mounting base to heatsink

$R_{th mb-h} = 0.6$ K/W

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April 1971

V.H.F. power transistor

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CHARACTERISTICS

 $T_j = 25^\circ\text{C}$ unless otherwise specified

Collector cut-off current

 $I_B = 0; V_{CE} = 28\text{ V}$ $I_{CEO} < 5\text{ mA}$

Breakdown voltages

Collector-base voltage

open emitter; $I_C = 1\text{ mA}$ $V_{(BR)CBO} > 65\text{ V}$

Collector-emitter voltage

open base, $I_C = 10\text{ mA}$ $V_{(BR)CEO} > 36\text{ V}$

Emitter-base voltage

open collector; $I_E = 1\text{ mA}$ $V_{(BR)EBO} > 4\text{ V}$

Transient energy

 $L = 25\text{ mH}; f = 50\text{ Hz}$ open base $E > 0.5\text{ ms}$
 $-V_{BE} = 1.5\text{ V}; R_{BE} = 33\Omega$ $E > 0.5\text{ ms}$

D. C. current gain

 $I_C = 500\text{ mA}; V_{CE} = 5\text{ V}$ $h_{FE} > 5$

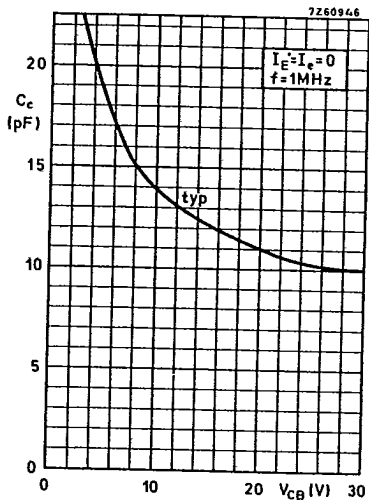
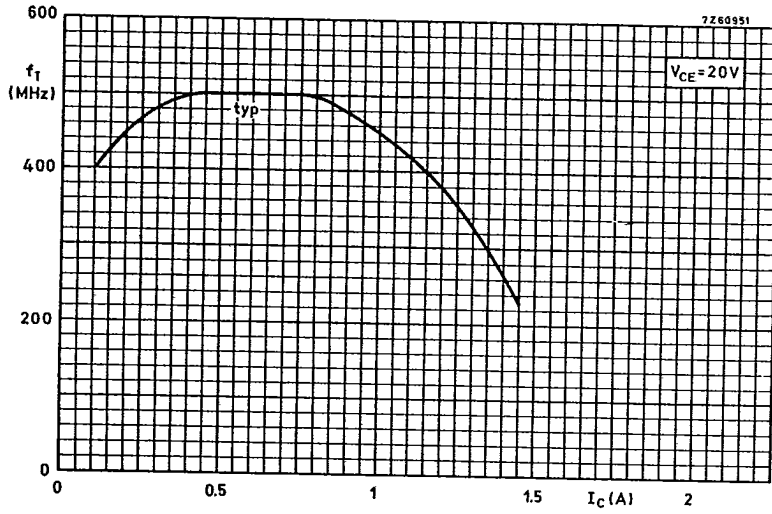
Transition frequency

 $I_C = 400\text{ mA}; V_{CE} = 20\text{ V}$ f_T typ. 500 MHzCollector capacitance at $f = 1\text{ MHz}$ $I_E = I_e = 0; V_{CB} = 30\text{ V}$ C_c typ. 10 pF
< 15 pFFeedback capacitance at $f = 1\text{ MHz}$ $I_C = 50\text{ mA}; V_{CE} = 30\text{ V}$ C_{re} typ. 7.5 pF

Collector-stud capacitance

 C_{cs} typ. 2 pF

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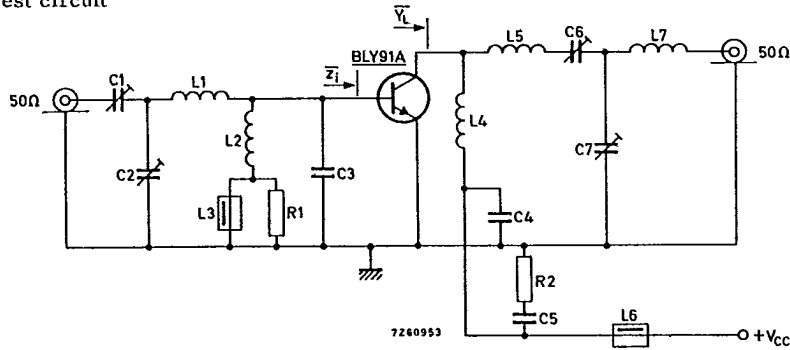
APPLICATION INFORMATION

R. F. performance in c. w. operation (unneutralised common-emitter class B circuit)

$V_{CC} = 28 \text{ V}$; T_{mb} up to $25 \text{ }^{\circ}\text{C}$

f(MHz)	P_S (W)	P_L (W)	I_C (A)	G_p (dB)	η (%)	\bar{Z}_i (Ω)	\bar{Y}_L (mS)
175	< 0.50	8	< 0.44	> 12	> 65	$1.8 + j0.7$	$18 - j20$

Test circuit



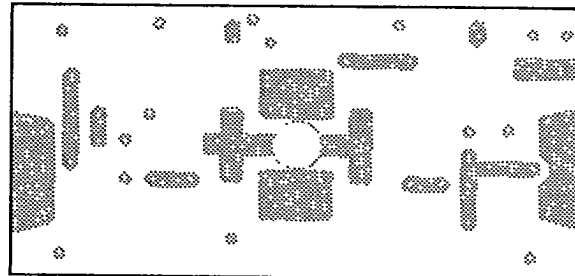
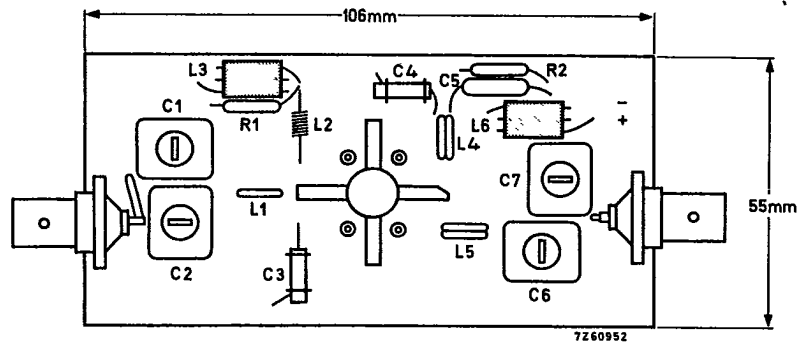
- C1 = 2.5 to 20 pF film dielectric trimmer (code number 2222 809 07004)
- C2 = C6 = C7 = 4 to 40 pF film dielectric trimmer (code number 2222 809 07008)
- C3 = 47 pF ceramic
- C4 = 100 pF ceramic
- C5 = 150 nF polyester

- L1 = 0.5 turn enamelled Cu wire (1.5 mm); int. diam. 6 mm; leads 2 x 10 mm
- L2 = 6.5 turns closely wound enamelled Cu wire (0.7 mm); int. diam. 4 mm; leads 2 x 5 mm
- L3 = L6 = ferroxcube choke (code number 4312 020 36640)
- L4 = 7.5 turns enamelled Cu wire (0.7 mm); int. diam. 4 mm; leads 2 x 5 mm
- L5 = 4.5 turns enamelled Cu wire (0.7 mm); int. diam. 6 mm; leads 2 x 7 mm
- L7 = 3.5 turns enamelled Cu wire (0.7 mm); int. diam. 6 mm; leads 2 x 7 mm
- R1 = R2 = 10 Ω carbon

Component lay-out for 175 MHz test circuit see next page.

APPLICATION INFORMATION (continued)

Component lay-out and printed circuit board for 175 MHz test circuit.



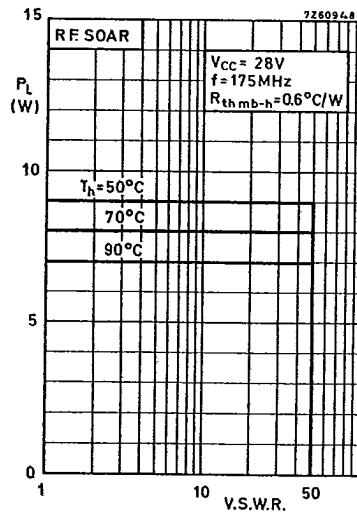
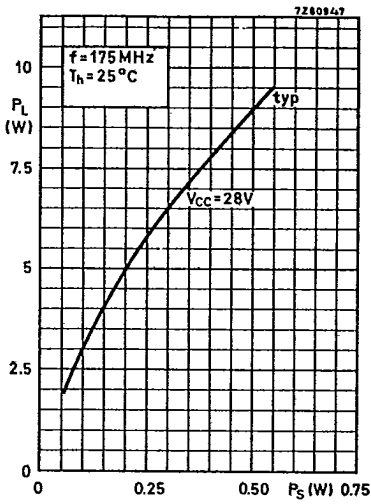
The circuit and the components are situated on one side of the epoxy fibre-glass board, the other side being fully metallised to serve as earth. Earth connections are made by means of hollow rivets.

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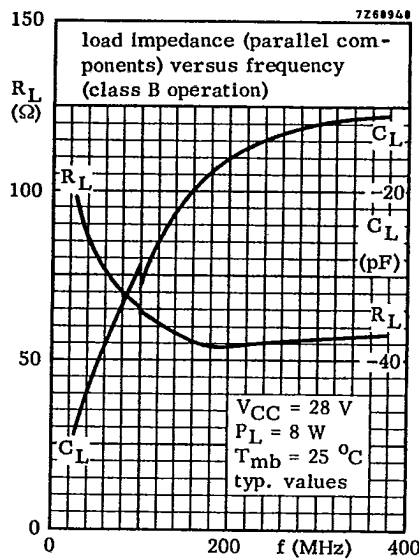
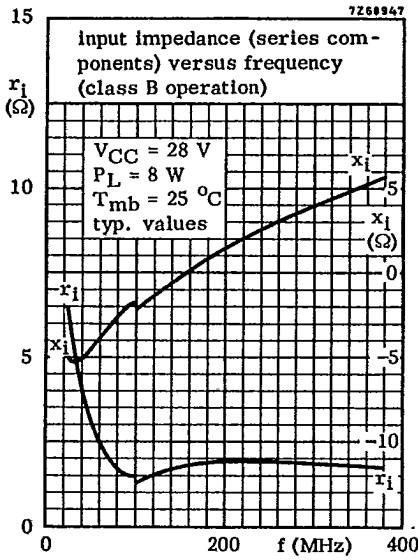
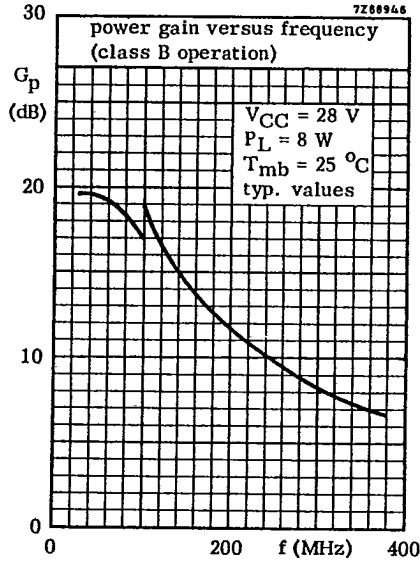
December 1973



For high voltage operation, a stabilized power supply is generally used. The graph shows the allowable output power under nominal conditions as a function of the V.S.W.R., with heat-sink temperature as parameter.

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OPERATING NOTE Below 100 MHz a base-emitter resistor of 10 Ω is recommended to avoid oscillation. This resistor must be effective for both d.c. and r.f.



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May 1974