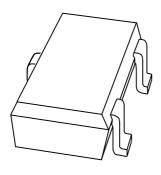
DISCRETE SEMICONDUCTORS

DATA SHEET



BC856W; BC857W; BC858W PNP general purpose transistors

Product data sheet Supersedes data of 1999 Apr 12 2002 Feb 04



PNP general purpose transistors

BC856W; BC857W;

BC858W

FEATURES

• Low current (max. 100 mA)

• Low voltage (max. 65 V).

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

PNP transistor in a SOT323 plastic package. NPN complements: BC846W, BC847W and BC848W.

MARKING

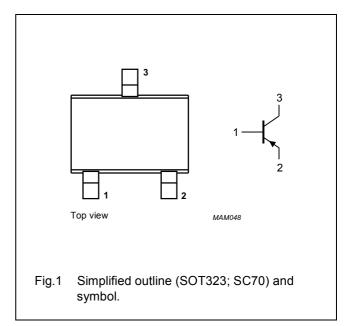
TYPE NUMBER	MARKING CODE ⁽¹⁾
BC856W	3D*
BC856AW	3A*
BC856BW	3B*
BC857W	3H*
BC857AW	3E*
BC857BW	3F*
BC857CW	3G*
BC858W	3M*

Note

1. * = -: made in Hong Kong.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



^{* =} t: made in Malaysia.

PNP general purpose transistors

BC856W; BC857W; BC858W

LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter			
	BC856W		_	-80	V
	BC857W		_	-50	V
	BC858W		-	-30	V
V_{CEO}	collector-emitter voltage	open base			
	BC856W		-	-65	V
	BC857W		_	-45	V
	BC858W		_	-30	V
V_{EBO}	emitter-base voltage	open collector	_	- 5	V
I _C	collector current (DC)		_	-100	mA
I _{CM}	peak collector current		_	-200	mA
I _{BM}	peak base current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	200	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Refer to SOT323 standard mounting conditions.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	PARAMETER CONDITIONS		UNIT	
R _{th j-a}	thermal resistance from junction to ambient	in free air; note 1	625	K/W	

Note

1. Refer to SOT323 standard mounting conditions.

PNP general purpose transistors

BC856W; BC857W; BC858W

CHARACTERISTICS

 T_{amb} = 25 °C; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	$V_{CB} = -30 \text{ V}; I_{E} = 0$	-	-1	-15	nA
		$V_{CB} = -30 \text{ V}; I_E = 0;$ $T_j = 150 ^{\circ}\text{C}$	_	_	-4	μА
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0$	_	_	-100	nA
h _{FE}	DC current gain	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$				
	BC856W		125	_	475	
	BC857W; BC858W		125	_	800	
	BC856AW; BC857AW		125	_	250	
	BC856BW; BC857BW		220	_	475	
	BC857CW		420	_	800	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	-75	-300	mV
		$I_C = -100 \text{ mA}$; $I_B = -5 \text{ mA}$; note 1	_	-250	-600	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	-700	_	mV
		$I_C = -100 \text{ mA}$; $I_B = -5 \text{ mA}$; note 1	_	-850	_	mV
V _{BE}	base-emitter voltage	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$	-600	-650	-750	mV
		$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}$	_	_	-820	mV
C _c	collector capacitance	vacitance $V_{CB} = -10 \text{ V}; I_E = I_e = 0;$ $f = 1 \text{ MHz}$		_	3	pF
C _e	emitter capacitance	$V_{EB} = -0.5 \text{ V}; I_C = I_c = 0;$ f = 1 MHz	_	-	12	pF
f _T	transition frequency	V _{CE} = -5 V; I _C = -10 mA; f = 100 MHz	100	-	_	MHz
F	noise figure	I_C = -200 μA; V_{CE} = -5 V; R_S = 2 kΩ; f = 1 kHz; B = 200 Hz		-	10	dB

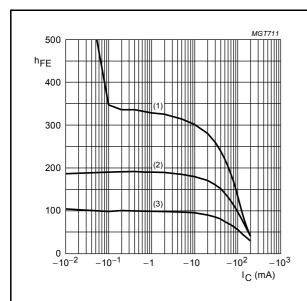
Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

2002 Feb 04

PNP general purpose transistors

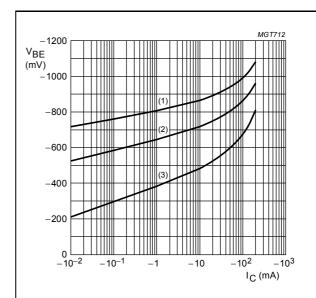
BC856W; BC857W; BC858W



BC857AW; $V_{CE} = -5 V$.

- (1) T_{amb} = 150 °C.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = -55 \,^{\circ}\text{C}$.

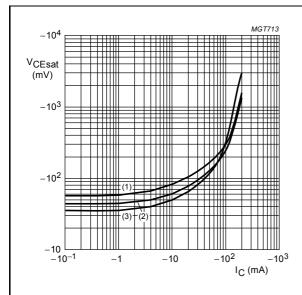
Fig.2 DC current gain as a function of collector current; typical values.



BC857AW; $V_{CE} = -5 V$.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = 150 \,^{\circ}\text{C}$.

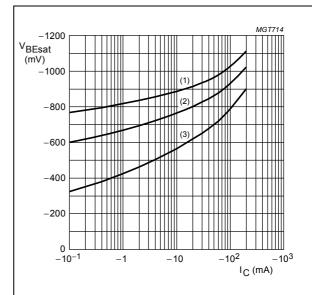
Fig.3 Base-emitter voltage as a function of collector current; typical values.



BC857AW; $I_C/I_B = 20$.

- (1) T_{amb} = 150 °C.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) T_{amb} = −55 °C.

Fig.4 Collector-emitter saturation voltage as a function of collector current; typical values.



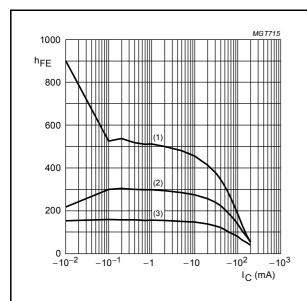
BC857AW; $I_C/I_B = 20$.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = 150 \, ^{\circ}C$.

Fig.5 Base-emitter saturation voltage as a function of collector current; typical values.

PNP general purpose transistors

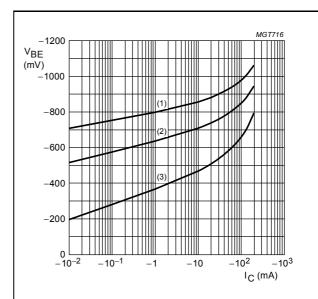
BC856W; BC857W; BC858W



BC857BW; $V_{CE} = -5 \text{ V}$.

- (1) T_{amb} = 150 °C.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = -55 \,^{\circ}\text{C}$.

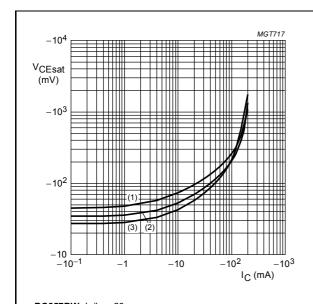
Fig.6 DC current gain as a function of collector current; typical values.



BC857BW; $V_{CE} = -5 V$.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = 150 \,^{\circ}\text{C}$.

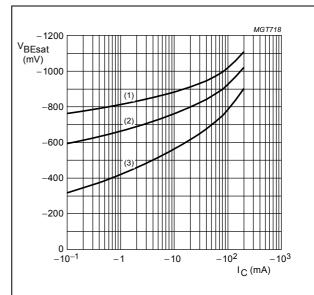
Fig.7 Base-emitter voltage as a function of collector current; typical values.



BC857BW; $I_C/I_B = 20$.

- (1) T_{amb} = 150 °C.
- (2) T_{amb} = 25 °C.
- (3) T_{amb} = −55 °C.

Fig.8 Collector-emitter saturation voltage as a function of collector current; typical values.



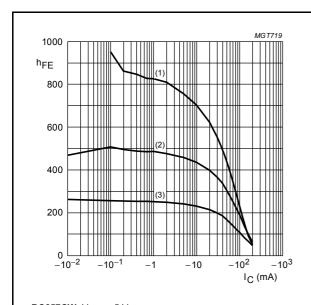
BC857BW; $I_C/I_B = 20$.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = 150 \, ^{\circ}C$.

Fig.9 Base-emitter saturation voltage as a function of collector current; typical values.

PNP general purpose transistors

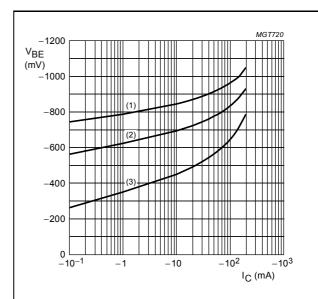
BC856W; BC857W; BC858W



BC857CW; $V_{CE} = -5 \text{ V}.$

- (1) $T_{amb} = 150 \,^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = -55 \,^{\circ}\text{C}$.

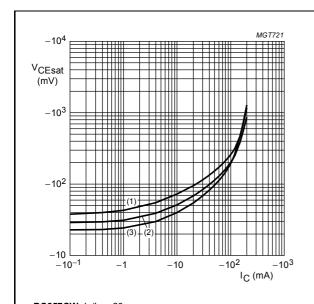
Fig.10 DC current gain as a function of collector current; typical values.



BC857CW; $V_{CE} = -5 V$.

- (1) $T_{amb} = -55$ °C.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = 150 \,^{\circ}\text{C}$.

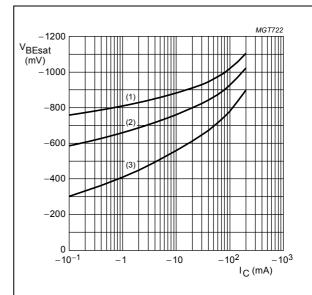
Fig.11 Base-emitter voltage as a function of collector current; typical values.



BC857CW; $I_C/I_B = 20$.

- (1) T_{amb} = 150 °C.
- (2) T_{amb} = 25 °C.
- (3) T_{amb} = −55 °C.

Fig.12 Collector-emitter saturation voltage as a function of collector current; typical values.



BC857CW; $I_C/I_B = 20$.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) T_{amb} = 25 °C.
- (3) $T_{amb} = 150 \, ^{\circ}C$.

Fig.13 Base-emitter saturation voltage as a function of collector current; typical values.

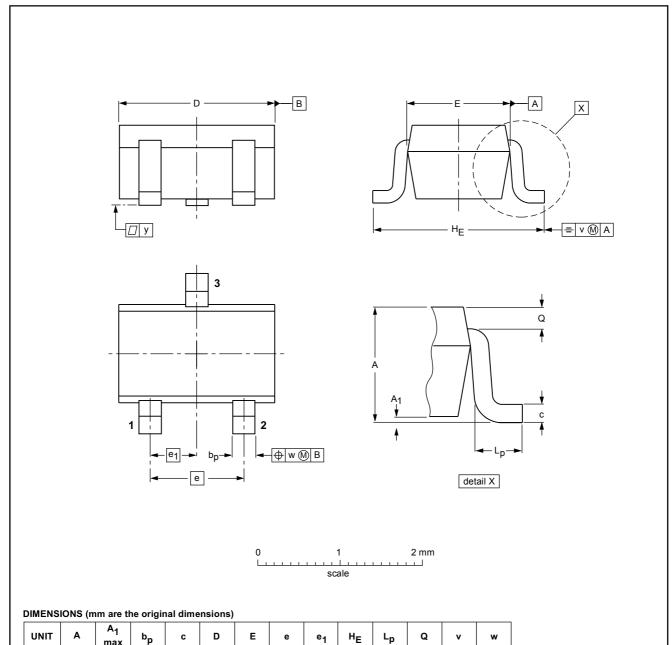
PNP general purpose transistors

BC856W; BC857W; BC858W

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT323



OUTLINE	REFERENCES		EUROPEAN	ICCUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT323			SC-70		$\bigoplus \bigoplus$	97-02-28

0.65

0.45 0.15 0.23 0.13

0.2

2002 Feb 04 8

0.25 0.10

0.4

0.1

1.1

mm

2.2 1.8 1.35 1.15

1.3

PNP general purpose transistors

BC856W; BC857W; BC858W

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- 2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

DISCLAIMERS

General — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions

above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors. No changes were made to the content, except for the legal definitions and disclaimers.

Contact information

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

© NXP B.V. 2009

 $All\ rights\ are\ reserved.\ Reproduction\ in\ whole\ or\ in\ part\ is\ prohibited\ without\ the\ prior\ written\ consent\ of\ the\ copyright\ owner.$

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands 613514/04/pp10 Date of release: 2002 Feb 04 Document order number: 9397 750 09168

