



Tantalum Chip Capacitors

Low Profile, Standard

Ordering code: B45192E0227+40*

Date: July 2006



© EPCOS AG 2006. Reproduction, publication and dissemination of this publication, enclosures hereto and the information contained therein without EPCOS' prior express consent is prohibited.

Features

- High volumetric efficiency
- Reduced height
- Excellent solderability
- Stable temperature and frequency characteristics
- Low leakage current, low dissipation factor
- Low self-inductance
- High resistance to shock and vibration
- Suitable for use without series resistor (recommended operating conditions see www.epcos.com/tantalum_gti, section 4.4)
- Lead-free and material content compatible with RoHS


Technical data and ordering code

Ordering code	(tinned terminals)		B45192E0227+40*	
Rated capacitance	(20 °C, 120 Hz)	C_R	220	μF
Rated voltage	(up to 85 °C) (up to 125 °C)	V_R	4 2.5	VDC
Operating temperature range		T_{op}	-55 ... +125	°C
Maximum leakage current	(20 °C, V_R , 5 min)	$I_{leak,max}$	8.8	μA
Maximum dissipation factor	(20 °C, 120 Hz)	DF_{max}	8	%
Maximum impedance	(20 °C, 100 kHz)	Z_{max}	0.8	Ω
Case size			X	
EIA code			7343-15	
Dimensions	(L x W x H)		7.3 ±0.3 x 4.3 ±0.3 x 1.5 max (0.287 ±0.012 x 0.169 ±0.012 x 0.059 max)	mm (inch)
Failure rate	(at 40 °C; $\leq V_R$, (1 fit = 1 · 10 ⁻⁹ failures/h) $R_S \geq 3 \Omega/V$)		≤ 24	fit

* = Code number for reel diameter

6 = 330-mm reel

9 = 180-mm reel

+ = Capacitance tolerance

M = ±20%

K = ±10%



Ordering code structure

B45	194R	1	337	+	40	*
<p>Tantalum capacitor</p>						
<p>Series 190E = Low Profile, H_{max} = 1.2 mm, Standard, tinned terminals 192E = Low Profile, H_{max} = 1.5 mm, Standard, tinned terminals 194E = Low Profile, H_{max} = 2.0 mm, Standard, tinned terminals 190R = Low Profile, H_{max} = 1.2 mm, Low ESR, tinned terminals 192R = Low Profile, H_{max} = 1.5 mm, Low ESR, tinned terminals 194R = Low Profile, H_{max} = 2.0 mm, Low ESR, tinned terminals</p>						
<p>Rated voltage 0 = 4 V, 1 = 6.3 V, 2 = 10 V, 3 = 16 V, 4 = 20 V, 5 = 25 V, 6 = 35 V</p>						
<p>Rated capacitance + exponent C [pF] · 10^x E.g.: 686 = 68 pF · 10⁶ = 68 μF, 157 = 15 pF · 10⁷ = 150 μF</p>						
<p>Capacitance tolerance M = ±20%, K = ±10%</p>						
<p>Case size 20 = R 30 = W, U 40 = X, V</p>						
<p>Reel diameter 9 = 180 mm, 6 = 330 mm</p>						



Cautions and warnings

When using tantalum capacitors with a manganese dioxide coating, the following cautions and warnings should be taken into account:

Polarity

Because tantalum capacitors are *polar capacitors*, it is important to observe their polarity markings (positive pole on the anode, negative pole on the cathode). Any incorrect polarity resulting from the sum of the AC and DC voltage components must be smaller than or equal to the permitted *polarity reversal voltage* (see “General technical information”, section 4.6 and section 4.8 under www.epcos.com/tantalum_capacitors). To avoid reducing their reliability, this voltage may only occur for a short time, at most five times for a duration of one minute per hour.

Voltage

The *maximum continuous voltage* depends on the ambient temperature. Within the temperature range of -55 to $+85$ °C, the rated voltage is equal to the maximum continuous voltage. Between $+85$ and $+125$ °C the maximum continuous voltage must be reduced linearly from the full rated voltage to 2/3 of it (derating). At 150 °C (only B4519*P series) the voltage must be reduced to 1/2 and at 175 °C (only B4519*T series) to 1/3 of the rated voltage. Operation below the maximum continuous voltage has a positive effect on the capacitor’s failure rate. The maximum continuous voltage must not be exceeded (see “Quality and environment”, section 1.11 under www.epcos.com/tantalum_capacitors).

All unfavourable operating conditions (such as possible line overvoltages, unfavourable tolerances of the transformation ratio of the line transformer in the equipment, repeated overvoltages when the equipment is switched on, high ambient temperatures) must be taken into account when determining the *operating voltage*.

The *surge voltage* is the maximum voltage (peak value) that may be applied to the capacitor for short periods, at most five times for a total duration of up to 1 minute per hour. The surge voltage must not be applied for periodic charging and discharging in the course of normal operation and cannot be part of the operating voltage. The permissible surge voltage for all capacitors in this data book is 1.3 x the rated voltage. The occurrence of voltage impulses (transient voltages) that exceed the surge voltage may lead to irreparable damage.

Capacitance

The actual *capacitance* of a capacitor can deviate from the rated capacitance by as much as the full magnitude of the tolerance at delivery. The capacitance varies with the temperature (at $+125$ °C $+12\%$ up to $+20\%$) and decreases with increasing frequency (see “General technical information”, section 5 under www.epcos.com/tantalum_capacitors).

SMD**Low-resistance applications and voltage networks**

For *low-resistance applications*, EPCOS recommends a maximum operating voltage of half the permissible maximum continuous voltage, so that the capacitors have sufficient tolerance to withstand voltage peaks. Depending on the conditions of use, the early failure rate is higher here by a factor of 2 to 20 than in the range with a constant failure rate as specified in the data book (see “Quality and environment”, section 1.11 under www.epcos.com/tantalum_capacitors).

When operated directly in a *voltage network*, the capacitor should be protected against overvoltage, e.g. by a suppressor diode, and against polarity reversal by a diode. If a capacitor is operated in an unprotected low-impedance circuit and fails because the permissible conditions for the forward DC voltage, reverse DC voltage, surge current, power dissipation or temperature are exceeded, the continued current flow through the overstressed capacitor may produce overheating. The overheated capacitor may damage the surrounding components and the circuit board (see “Quality and environment”, section 1.11 and “General technical information”, section 4.11 under www.epcos.com/tantalum_capacitors).

Storage conditions

Capacitors with solid electrolyte may be stored at temperatures down to -80 °C . The upper *storage temperature* must not exceed $+85\text{ °C}$ (for blister tape the temperature limit is $+40\text{ °C}$). The storage conditions should not exceed $+40\text{ °C}$ and 70% R. H. A drypack is useful to prevent the effects of humidity.

Note

For more detailed information about cautions and warnings for tantalum electrolytic capacitors (e.g. other electrical values beside those listed above), see “General technical information” and “Quality and environment” under www.epcos.com/tantalum_capacitors.

Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of passive electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of a passive electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of a passive electronic component.
3. **The warnings, cautions and product-specific notes must be observed.**
4. In order to satisfy certain technical requirements, **some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as “hazardous”)**. Useful information on this will be found in our Material Data Sheets on the Internet (www.epcos.com/material). Should you have any more detailed questions, please contact our sales offices.
5. We constantly strive to improve our products. Consequently, **the products described in this publication may change from time to time**. The same is true of the corresponding product specifications. Please check therefore to what extent product descriptions and specifications contained in this publication are still applicable before or when you place an order.
We also **reserve the right to discontinue production and delivery of products**. Consequently, we cannot guarantee that all products named in this publication will always be available.
6. Unless otherwise agreed in individual contracts, **all orders are subject to the current version of the “General Terms of Delivery for Products and Services in the Electrical Industry” published by the German Electrical and Electronics Industry Association (ZVEI)**.
7. The trade names EPCOS, EPCOS-JONES, Baoke, CeraDiode, CSSP, MLSC, PhaseCap, PhaseMod, SIFI, SIKOREL, SilverCap, SIMID, SIOV, SIP5D, SIP5K, UltraCap, WindCap are **trademarks registered or pending** in Europe and in other countries. Further information will be found on the Internet at www.epcos.com/trademarks.