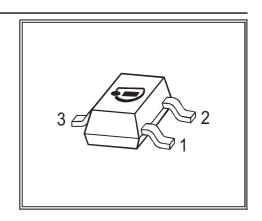


NPN Silicon RF Transistor

- For linear broadband amplifier application up to 500 MHz
- SAW filter driver in TV tuners
- Pb-free (RoHS compliant) package 1)
- Qualified according AEC Q101







Туре	Marking	Pin Configuration Package			Package
BF799	LKs	1 = B	2 = E	3 = C	SOT23

Maximum Ratings

Parameter	Symbol	Value	Unit	
Collector-emitter voltage	$V_{\sf CEO}$	20	V	
Collector-emitter voltage	V_{CES}	30		
Collector-base voltage	V_{CBO}	30		
Emitter-base voltage	V_{EBO}	3		
Collector current	I _C	35	mA	
Peak collector current,	I _{CM}	50		
Peak base current	I _{BM}	15		
Total power dissipation	P _{tot}	280	mW	
$T_{\rm S} \le 69 ^{\circ}{\rm C}^{2)}$				
Junction temperature	T_{i}	150	°C	
Storage temperature	T _{stg}	-65 150		

Thermal Resistance

Junction - soldering point ³⁾	R _{thJS}	≤ 290	K/W
			•

¹Pb-containing package may be available upon special request

 $^{{}^2}T_{
m S}$ is measured on the collector lead at the soldering point to the pcb

 $^{^3}$ For calculation of R_{thJA} please refer to Application Note Thermal Resistance

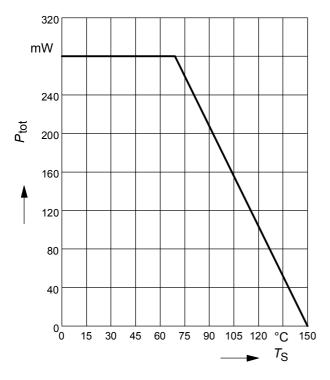


Electrical Characteristics at T_A = 25 °C, unless otherwise specified.

Parameter	Symbol	Values			Unit
			typ.	max.	
DC characteristics	•				•
Collector-emitter breakdown voltage	V _{(BR)CEO}	20	_	-	V
$I_{\rm C}$ = 1 mA, $I_{\rm B}$ = 0					
Collector-base breakdown voltage	$V_{(BR)CBO}$	30	-	-	
$I_{\rm C} = 10 \ \mu A, I_{\rm E} = 0$					<u> </u>
Base-emitter breakdown voltage	$V_{(BR)EBO}$	3	_	-	
$I_{\rm E}$ = 10 μ A, $I_{\rm C}$ = 0					
Collector-base cutoff current	I _{CBO}	-	-	100	nA
$V_{\rm CB} = 20 \text{ V}, I_{\rm E} = 0$					
DC current gain	h _{FE}				-
$I_{\rm C}$ = 5 mA, $V_{\rm CE}$ = 10 V		35	95	_	
$I_{\rm C}$ = 20 mA, $V_{\rm CE}$ = 10 V		40	100	250	
Collector-emitter saturation voltage	V _{CEsat}	-	0.1	0.3	V
$I_{\rm C}$ = 20 mA, $I_{\rm B}$ = 2 mA					
Base-emitter saturation voltage	V _{BEsat}	-	-	0.95	1
$I_{\rm C}$ = 20 mA, $I_{\rm B}$ = 2 mA					
AC characteristics					
Transition frequency	f _T				MHz
$I_{\rm C}$ = 5 mA, $V_{\rm CE}$ = 10 V, f = 100 MHz		-	800	-	
$I_{\rm C}$ = 20 mA, $V_{\rm CE}$ = 8 V, f = 100 MHz		-	1100	-	
Output capacitance	C _{ob}	_	0.96	_	pF
V_{CB} = 10 V, I_{E} = 0 mA, f = 1 MHz					
Collector-base capacitance	C _{cb}	-	0.7	_	
V _{CB} = 10 V, <i>f</i> = 1 MHz					
Collector-emitter capacitance	C _{ce}	_	0.28	_	
V _{CE} = 10 V, f = 1 MHz					
Noise figure	F	-	3	-	dB
$I_{\rm C}$ = 5 mA, $V_{\rm CE}$ = 10 V, f = 100 MHz,					
$Z_{\rm S} = 50 \ \Omega$					
Output conductance	g 22e	-	60	-	μS
$I_{\rm C}$ = 20 mA, $V_{\rm CE}$ = 10 V, f = 35 MHz					



Total power dissipation $P_{tot} = f(T_S)$

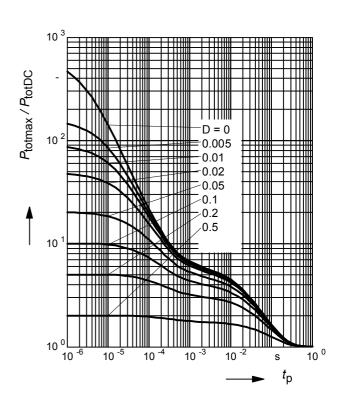


Permissible Pulse Load $R_{thJS} = f(t_p)$

Permissible Pulse Load

 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_{\text{p}})$

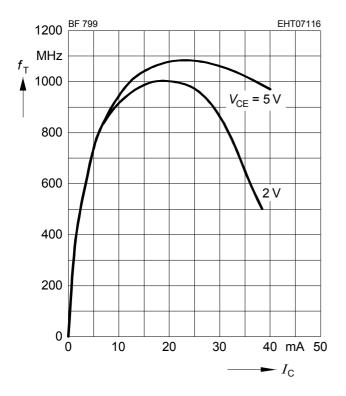
3



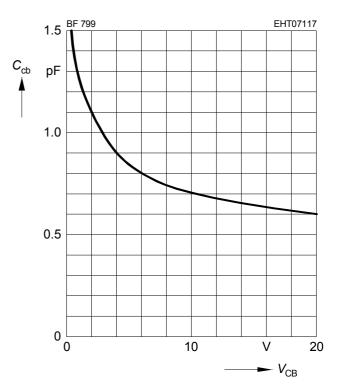


Transition frequency $f_T = f(I_C)$

f = 100MHz



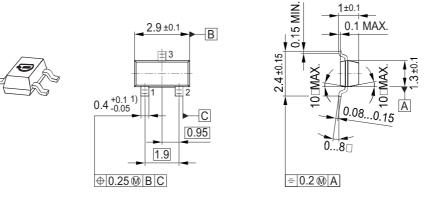
Collector-base capacitance $C_{cb} = f(V_{CB})$ f = 1 MHz



4



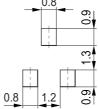
Package Outline



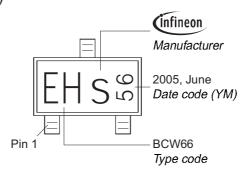
Foot Print



1) Lead width can be 0.6 max. in dambar area

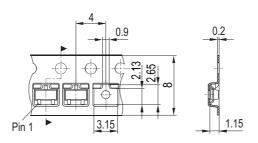


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel



5



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6

2007-04-20