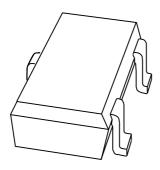
DISCRETE SEMICONDUCTORS

DATA SHEET



BAP64-05W Silicon PIN diode

Product specification

2000 Jul 13



Silicon PIN diode BAP64-05W

FEATURES

- High voltage, current controlled
- RF resistor for RF attenuators and switches
- Low diode capacitance
- · Low diode forward resistance
- Low series inductance
- For applications up to 3 GHz.

APPLICATIONS

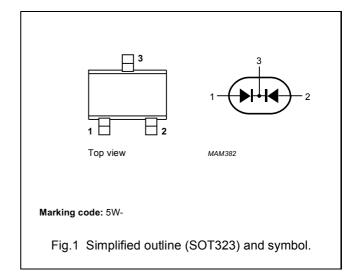
• RF attenuators and switches.

GENERAL DESCRIPTION

Two planar PIN diodes in common cathode configuration in a SOT323 small SMD plastic package.

PINNING

PIN	DESCRIPTION
1	anode (a1)
2	anode (a2)
3	common cathode



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per diode					
V _R	continuous reverse voltage		_	100	V
l _F	continuous forward current		-	100	mA
P _{tot}	total power dissipation	T _s = 90 °C	-	240	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-65	+150	°C

Silicon PIN diode BAP64-05W

ELECTRICAL CHARACTERISTICS

 T_i = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	PARAMETER CONDITIONS			
Per diode		·			
V _F	forward voltage	I _F = 50 mA	0.95	1.1	V
I _R	reverse current	V _R = 100 V	_	10	μА
		V _R = 20 V	_	1	μА
C _d	diode capacitance	V _R = 0; f = 1 MHz	0.52	_	pF
		V _R = 1 V; f = 1 MHz	0.37	_	pF
		V _R = 20 V; f = 1 MHz	0.23	0.35	pF
r _D	diode forward resistance	I _F = 0.5 mA; f = 100 MHz; note 1	20	40	Ω
		I _F = 1 mA; f = 100 MHz; note 1	10	20	Ω
		I _F = 10 mA; f = 100 MHz; note 1	2	3.8	Ω
		I _F = 100 mA; f = 100 MHz; note 1	0.7	1.35	Ω
τ∟	charge carrier life time	when switched from I_F = 10 mA to I_R = 6 mA; R_L = 100 Ω ; measured at I_R = 3 mA	1.55	_	μS
L _S	series inductance		1.2	-	nH

Note

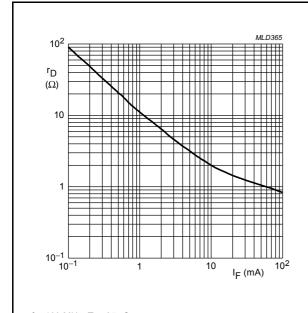
THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	250	K/W

^{1.} Guaranteed on AQL basis: inspection level S4, AQL 1.0.

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GRAPHICAL DATA



f = 100 MHz; T_j = 25 °C.

Fig.2 Forward resistance as a function of forward current; typical values.

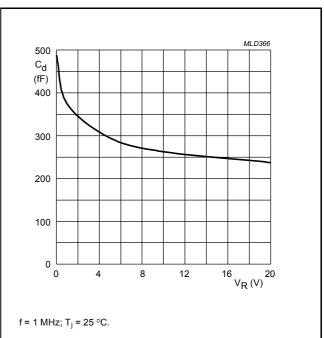
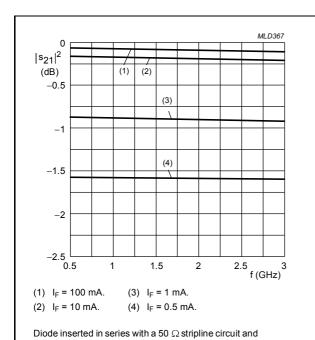


Fig.3 Diode capacitance as a function of reverse voltage; typical values.



biased via the analyzer Tee network. T_{amb} = 25 °C.

Fig.4 Insertion loss $(|s_{21}|^2)$ of the diode as a function of frequency; typical values.

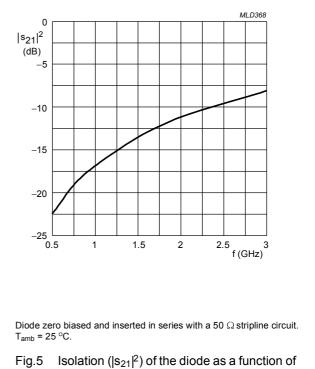
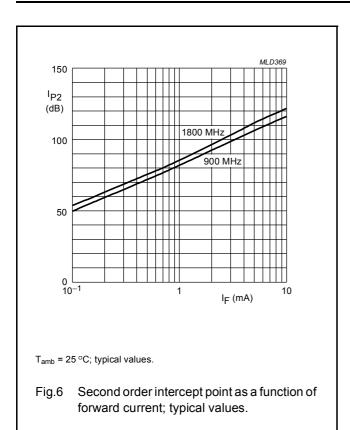


Fig.5 Isolation ($|s_{21}|^2$) of the diode as a function of frequency; typical values.

Silicon PIN diode BAP64-05W

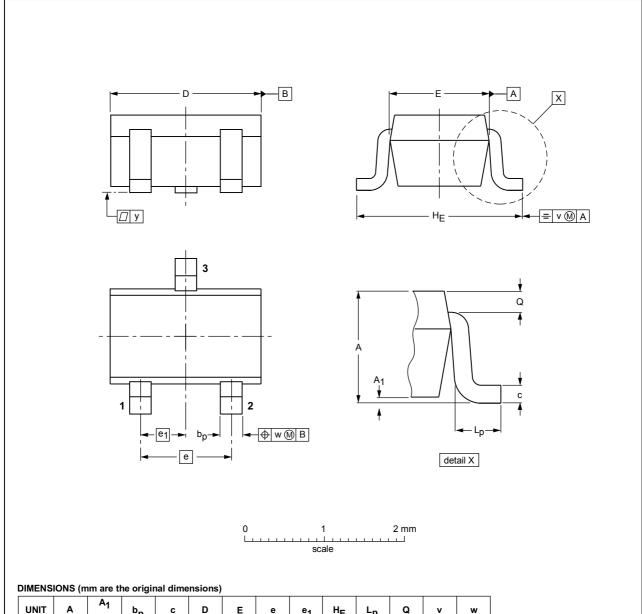


Silicon PIN diode BAP64-05W

PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT323



UNIT	Α	A ₁ max	bр	С	D	E	е	e ₁	HE	Lp	Q	٧	w
mm	1.1 0.8	0.1	0.4 0.3	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.23 0.13	0.2	0.2

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT323			SC-70			-04-11-04- 06-03-16

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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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