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54F/74F10 Triple 3-Input NAND Gate

December 1994

54F/74F10

## National Semiconductor

### 54F/74F10 Triple 3-Input NAND Gate

#### **General Description**

This device contains three independent gates, each of which performs the logic NAND function.

#### Ordering Code: See Section Section 0

Commercial	Military	Package	Package Description
		Number	
74F10PC		N14A	14-Lead (0.300" Wide) Molded Dual-In-Line
	54F10DM (Note 2)	J14A	14-Lead Ceramic Dual-In-Line
74F10SC (Note 1)		M14A	14-Lead (0.150" Wide) Molded Small Outline, JEDEC
74F10SJ (Note 1)		M14D	14-Lead (0.300" Wide) Molded Small Outline, EIAJ
	54F10FM (Note 2)	W14B	14-Lead Cerpack
	54F10LM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C

Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

IEEE/IEC

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Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB, FMQB and LMQB.

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#### Logic Symbol

А<sub>0</sub> В<sub>0</sub>

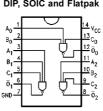
C<sub>0</sub> A<sub>1</sub> B<sub>1</sub>

С<sub>1</sub> А<sub>2</sub> В<sub>2</sub>

C2

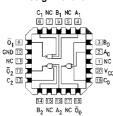
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#### Connection Diagrams Pin Assignment for DIP, SOIC and Flatpak



DS009458-2

Pin Assignment for LCC



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# Unit Loading/Fan Out See Section 0 for U.L. definitions

		54F/74F			
Pin Names	Description	U.L.	Input I <sub>IH</sub> /I <sub>IL</sub>		
		HIGH/LOW	Output I <sub>OH</sub> /I <sub>OL</sub>		
A <sub>n</sub> , B <sub>n</sub> , C <sub>n</sub>	Inputs	1.0/1.0	20 µA/-0.6 mA		
$\overline{O}_n$	Outputs	50/33.3	−1 mA/20 mA		

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#### Absolute Maximum Ratings (Note 3)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Storage Temperature Ambient Temperature under Bias	−65°C to +150°C −55°C to +125°C
Junction Temperature under Blas	-55°C to +175°C
Plastic	−55°C to +150°C
V <sub>CC</sub> Pin Potential to	
Ground Pin	-0.5V to +7.0V
Input Voltage (Note 4)	-0.5V to +7.0V
Input Current (Note 4)	-30 mA to +5.0 mA
Voltage Applied to Output	
in HIGH State (with $V_{CC}$ = 0V)	
Standard Output	–0.5V to $V_{\rm CC}$
TRI-STATE <sup>®</sup> Output	-0.5V to +5.5V

Current Applied to Output in LOW State (Max)

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twice the rated  $I_{\text{OL}}$  (mA)

## Recommended Operating Conditions

Free Air Ambient Temperature	
Military	−55°C to +125°C
Commercial	0°C to +70°C
Supply Voltage	
Military	+4.5V to +5.5V
Commercial	+4.5V to +5.5V
Note 3: Absolute maximum ratings are value be damaged or have its useful life impaired. If conditions is not implied.	

Note 4: Either voltage limit or current limit is sufficient to protect inputs.

#### DC Electrical Characteristics

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Symbol	Parameter			54F/74F		Units	V <sub>cc</sub>	Conditions	
			Min	Тур	Мах				
VIH	Input HIGH Voltage	9	2.0			V		Recognized as a HIGH Signal	
VIL	Input LOW Voltage				0.8	V		Recognized as a LOW Signal	
V <sub>CD</sub>	Input Clamp Diode	Voltage			-1.2	V	Min	I <sub>IN</sub> = -18 mA	
V <sub>OH</sub>	Output HIGH	54F 10% V <sub>CC</sub>	2.5					I <sub>OH</sub> = -1 mA	
	Voltage	74F 10% V <sub>CC</sub>	2.5			V	Min	I <sub>OH</sub> = -1 mA	
		74F 5% $V_{\rm CC}$	2.7					I <sub>OH</sub> = -1 mA	
V <sub>OL</sub>	Output LOW	54F 10% V <sub>CC</sub>			0.5	V	Min	I <sub>OL</sub> = 20 mA	
	Voltage	74F 10% $V_{\rm CC}$			0.5			I <sub>OL</sub> = 20 mA	
I <sub>IH</sub>	Input HIGH	54F			20.0	μA	Max	V <sub>IN</sub> = 2.7V	
	Current	74F			5.0				
I <sub>BVI</sub>	Input HIGH	54F			100	μA	Max	V <sub>IN</sub> = 7.0V	
	Current					μΛ	IVIAA	VIN 7.00	
	Breakdown Test	74F			7.0				
ICEX	Output HIGH	54F			250	μA	Max	V <sub>OUT</sub> = V <sub>CC</sub>	
	Leakage Current	74F			50				
V <sub>ID</sub>	Input Leakage	74F	4.75			V	0.0	I <sub>ID</sub> = 1.9 μA	
	Test							All other pins grounded	
I <sub>OD</sub>	Output Leakage	74F			3.75	μA	0.0	V <sub>IOD</sub> = 150 mV	
	Circuit Current							All other pins grounded	
I <sub>IL</sub>	Input LOW Current				-0.6	mA	Max	V <sub>IN</sub> = 0.5V	
I <sub>os</sub>	Output Short-Circui	t Current	-60		-150	mA	Max	V <sub>OUT</sub> = 0V	
I <sub>CCH</sub>	Power Supply Curre	ent		1.4	2.1	mA	Max	V <sub>o</sub> = HIGH	
I <sub>CCL</sub>	Power Supply Curre	ent		5.1	7.7	mA	Max	V <sub>o</sub> = LOW	

#### **AC Electrical Characteristics**

See Section 0 for Waveforms and Load Configurations

		74F			54F		74F			
Symbol	Parameter	, v	T <sub>A</sub> = +25°C V <sub>CC</sub> = +5.0 C <sub>L</sub> = 50 pl	v		<sub>:c</sub> = Mil 50 pF		<sub>c</sub> = Com 50 pF	Units	Fig. No.
		Min	Тур	Max	Min	Max	Min	Max		
t <sub>PLH</sub>	Propagation Delay	2.4	3.7	5.0	2.0	7.0	2.4	6.0	ns	**-**

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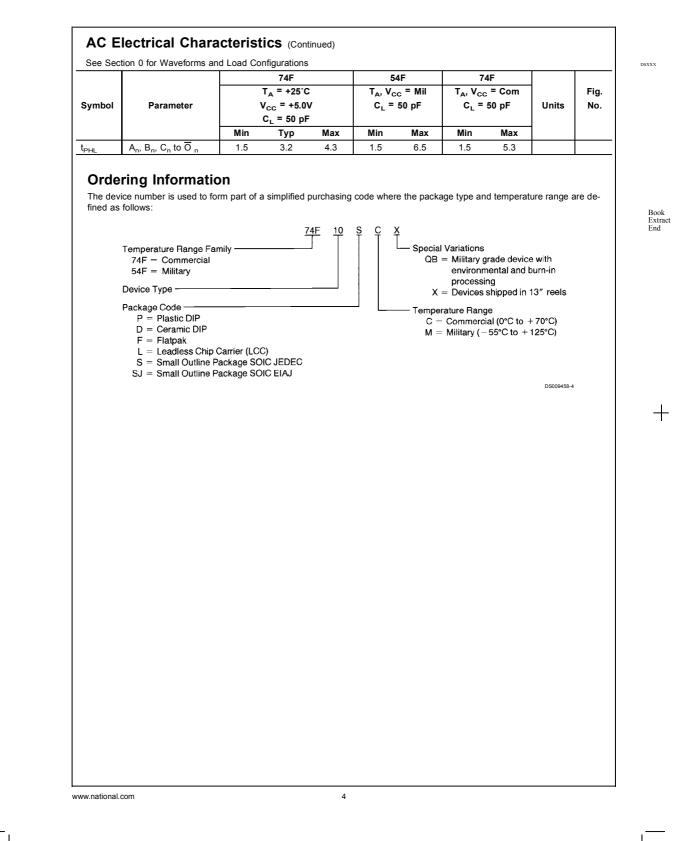
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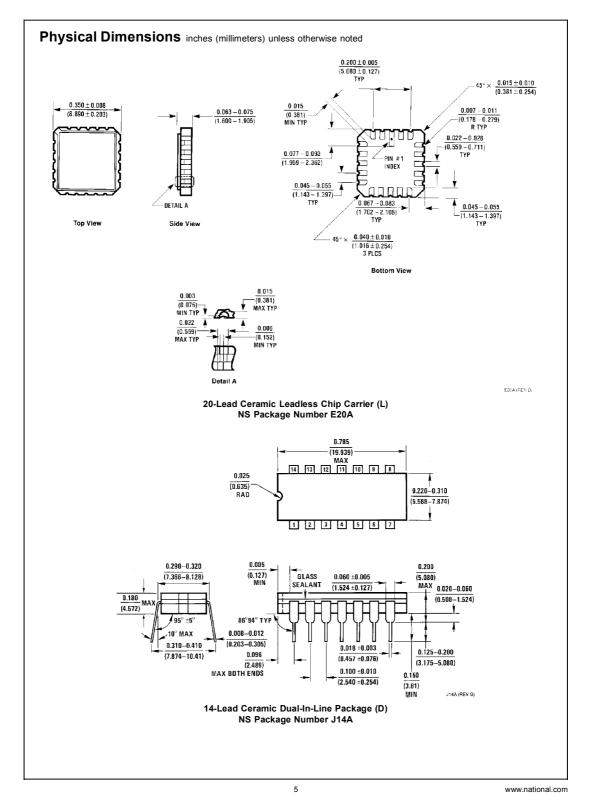
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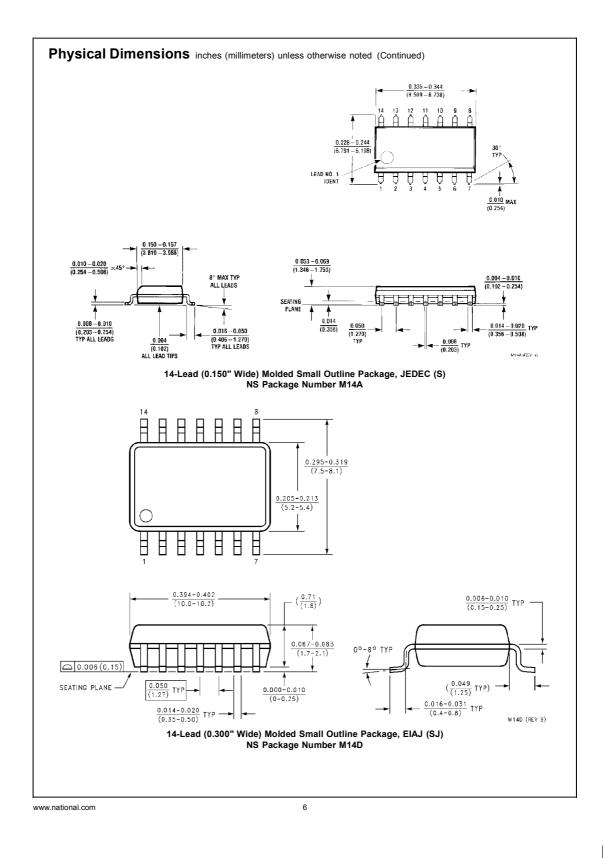
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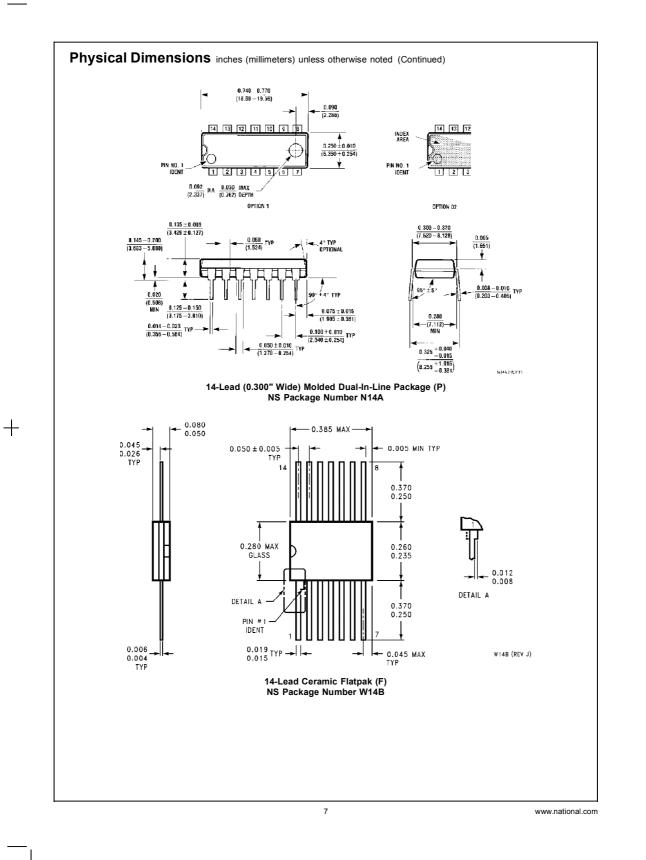


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