

April 1988 Revised September 2000

# 74F08 Quad 2-Input AND Gate

### **General Description**

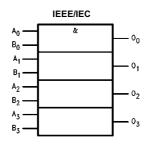
This device contains four independent gates, each of which performs the logic AND function.

# **Ordering Code:**

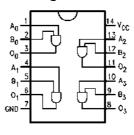
Order Number	Package Number	Package Description
74F08SC	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow
74F08SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74F08PC	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

# **Logic Symbol**



# **Connection Diagram**



# **Unit Loading/Fan Out**

Pin Names	Description	U.L.	Input I <sub>IH</sub> /I <sub>IL</sub>	
r iii rtailioo	Becompaion	HIGH/LOW	Output I <sub>OH</sub> /I <sub>OL</sub>	
A <sub>n</sub> , B <sub>n</sub>	Inputs	1.0/1.0	20 μA/–0.6 mA	
O <sub>n</sub>	Outputs	50/33.3	-1 mA/20 mA	

#### Absolute Maximum Ratings(Note 1)

-65°C to +150°C Storage Temperature Ambient Temperature under Bias -55°C to +125°C

–55°C to +150°C Junction Temperature under Bias V<sub>CC</sub> Pin Potential to Ground Pin -0.5V to +7.0V Input Voltage (Note 2) -0.5V to +7.0V

Input Current (Note 2) -30 mA to +5.0 mA

Voltage Applied to Output

in HIGH State (with  $V_{CC} = 0V$ )

–0.5V to  $V_{\rm CC}$ Standard Output 3-STATE Output -0.5V to +5.5V

Current Applied to Output

in LOW State (Max) twice the rated  $I_{OL}$  (mA) ESD Last Passing Voltage (Min)

4000V

## **Recommended Operating Conditions**

Free Air Ambient Temperature 0°C to +70°C Supply Voltage +4.5V to +5.5V

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

#### **DC Electrical Characteristics**

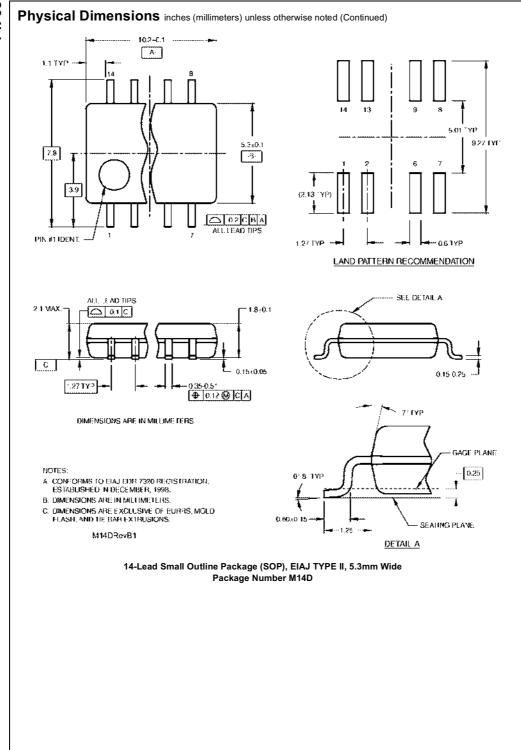
Symbol	Parameter	Min	Тур	Max	Units	v <sub>cc</sub>	Conditions	
V <sub>IH</sub>	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal	
V <sub>IL</sub>	Input LOW Voltage			0.8	V		Recognized as a LOW Signal	
V <sub>CD</sub>	Input Clamp Diode Voltage			-1.2	V	Min	I <sub>IN</sub> = -18 mA	
V <sub>OH</sub>		2.5 2.7			V	Min	I <sub>OH</sub> = -1 mA I <sub>OH</sub> = -1 mA	
V <sub>OL</sub>	Output LOW 10% V <sub>CC</sub> Voltage			0.5	V	Min	I <sub>OL</sub> = 20 mA	
I <sub>IH</sub>	Input HIGH Current			5.0	μА	Max	V <sub>IN</sub> = 2.7V	
I <sub>BVI</sub>	Input HIGH Current Breakdown Test			7.0	μА	Max	V <sub>IN</sub> = 7.0V	
I <sub>CEX</sub>	Output HIGH Leakage Current			50	μА	Max	V <sub>OUT</sub> = V <sub>CC</sub>	
V <sub>ID</sub>	Input Leakage Test	4.75			٧	0.0	I <sub>ID</sub> = 1.9 μA All Other Pins Grounded	
I <sub>OD</sub>	Output Leakage Circuit Current			3.75	μА	0.0	V <sub>IOD</sub> = 150 mV All Other Pins Grounded	
I <sub>IL</sub>	Input LOW Current			-0.6	mA	Max	V <sub>IN</sub> = 0.5V	
I <sub>OS</sub>	Output Short-Circuit Current	-60		-150	mA	Max	$V_{OUT} = 0V$	
I <sub>CCH</sub>	Power Supply Current		5.5	8.3	mA	Max	$V_O = HIGH$	
I <sub>CCL</sub>	Power Supply Current		8.6	12.9	mA	Max	$V_O = LOW$	

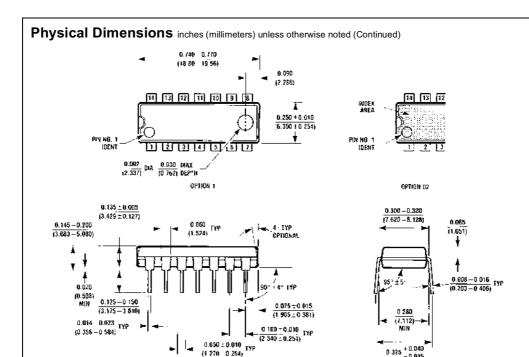
#### **AC Electrical Characteristics**

		$T_A = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$			T <sub>A</sub> = -55°C	to +125°C	T <sub>A</sub> = 0°C to +70°C		
Cb. a.l	Danamatan.				$egin{aligned} \mathbf{V_{CC}} = +5.0\mathbf{V} \\ \mathbf{C_L} = 50 \ \mathbf{pF} \end{aligned}$		$V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$		Units
Symbol	Parameter								
		Min	Тур	Max	Min	Max	Min	Max	
	Propagation Delay	3.0	4.2	5.6	2.5	7.5	3.0	6.6	
t <sub>PLH</sub>	Fiopagation Delay	3.0	7.2	0.0	2.0	7.0	0.0	0.0	ns

# Physical Dimensions inches (millimeters) unless otherwise noted E 804 (0 102) ALL LEAD TIPS 14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow Package Number M14A

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14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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 $0.325 + 0.040 \\ -0.015 \\ (0.255 + 1.016) \\ -0.381)$ 

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