

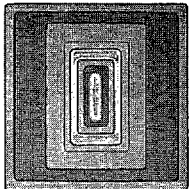


MOTOROLA

Semiconductors

BOX 955 • PHOENIX, ARIZONA 85001

DUAL NPN SILICON ANNULAR* TRANSISTORS



... especially designed for low-level, low-noise differential amplifier applications.

- High Breakdown Voltage
 $V_{CE0} = 70$ Vdc typical
- Very High Beta Guaranteed from $10 \mu\text{A}$ dc to 1.0 mAdc
- Beta Match as tight as 0.9 to 1
- Base-Voltage Differential as low as
3 mV max at $I_C = 100 \mu\text{A}$ dc
- Excellent Noise Characteristics
as low as 3.0 db max at $f = 1$ kc

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristics	Symbol	Rating		Unit
		2N2913-18 2N2972-77	2N2919-20 2N2978-79	
Collector-Base Voltage	V_{CBO}	45	60	Vdc
Collector-Emitter Voltage	V_{CEO}	45	60	Vdc
Emitter-Base Voltage	V_{EBO}	6		Vdc
DC Collector Current	I_C	30		mA
Junction Temperature	T_J	+200		$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-65 to +200		$^\circ\text{C}$
		ONE SIDE	BOTH SIDES	
Total Device Dissipation @ $T_A = 25^\circ\text{C}$	P_D			
TO-5 Case		300	600	mW
Derate above 25°C		1.7	3.4	mW/ $^\circ\text{C}$
TO-18 Case		250	300	mW
Derate above 25°C		1.43	1.72	mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$	P_D			
TO-5 Case		750	1500	mW
Derate above 25°C		4.3	8.6	mW/ $^\circ\text{C}$
TO-18 Case		500	750	mW
Derate above 25°C		2.85	4.3	mW/ $^\circ\text{C}$

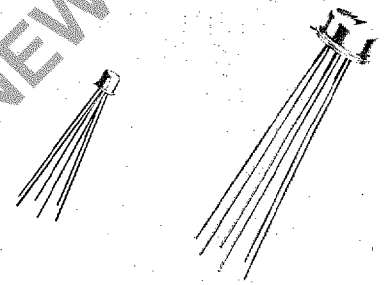
* Patents pending

MULTIPLE DEVICES

2N2913-2N2920
2N2972-2N2979

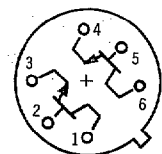
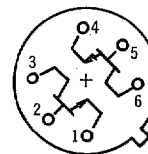
NPN SILICON DUAL TRANSISTORS

AUGUST 1965 — DS 4522

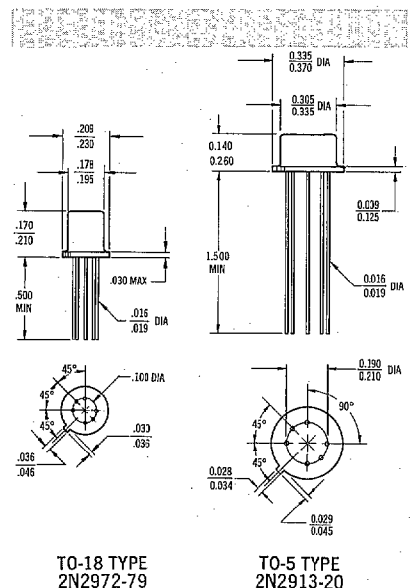


6-LEAD TO-18
2N2972
THRU
2N2979

6-LEAD TO-5
2N2913
THRU
2N2920



Pin Connections, Bottom View
All Leads Electrically Isolated From Case



MULTIPLE DEVICES
2N2913-20, 2N2972-79
DS 4522

MOTOROLA Semiconductor Products Inc.



A SUBSIDIARY OF MOTOROLA INC.

2N2913 thru 2N2920 / 2N2972 thru 2N2979

ELECTRICAL CHARACTERISTICS (At $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristics	Symbol	Min	Typ	Max	Unit
Collector-Base Breakdown Voltage ($I_C = 10\text{ }\mu\text{Adc}$, $I_E = 0$) 2N2913 thru 2N2918, 2N2972 thru 2N2977 2N2919, 2N2920, 2N2978, 2N2979	BV_{CBO}	45 60	— 90	—	Vdc
Collector-Emitter Sustaining Voltage ($I_C = 10\text{ mAdc}$, $I_B = 0$) 2N2913, thru 2N2918, 2N2972 thru 2N2977 2N2919, 2N2920, 2N2978, 2N2979	$BV_{CEO(sus)}$	45 60	— 70	—	Vdc
Emitter-Base Breakdown Voltage ($I_E = 10\text{ }\mu\text{Adc}$, $I_C = 0$) All Types	BV_{EBO}	6	7	—	Vdc
Collector-Base Cutoff Current ($V_{CB} = 45\text{ Vdc}$, $I_E = 0$) ($V_{CB} = 45\text{ Vdc}$, $I_E = 0$, $T_A = 150^\circ\text{C}$) 2N2913 thru 18, 2N2972 thru 77 2N2919, 2N2920, 2N2978, 2N2979 All Types	I_{CBO}	— — —	— — —	0.010 0.002 10	μAdc
Collector-Emitter Cutoff Current ($V_{CE} = 5\text{ Vdc}$, $I_B = 0$) All Types	I_{CEO}	—	—	0.002	μAdc
Emitter-Base Cutoff Current ($V_{EB} = 5\text{ Vdc}$, $I_C = 0$) All Types	I_{EBO}	—	—	0.002	μAdc
Collector-Emitter Saturation Voltage ($I_C = 1\text{ mAdc}$, $I_B = 0.1\text{ mAdc}$) All Types	$V_{CE(sat)}$	—	—	0.35	Vdc
Base-Emitter "ON" Voltage ($I_C = 100\text{ }\mu\text{Adc}$, $V_{CE} = 5\text{ Vdc}$) All Types	$V_{BE(ON)}$	—	—	0.7	Vdc
DC Current Gain* ($I_C = 10\text{ }\mu\text{Adc}$, $V_{CE} = 5\text{ Vdc}$) ($I_C = 10\text{ }\mu\text{Adc}$, $V_{CE} = 5\text{ Vdc}$, $T_A = -55^\circ\text{C}$) ($I_C = 100\text{ }\mu\text{Adc}$, $V_{CE} = 5\text{ Vdc}$) ($I_C = 1\text{ mAdc}$, $V_{CE} = 5\text{ Vdc}$) 2N2913, 15, 17, 19, 2N2972, 74, 76, 78 2N2914, 16, 18, 20, 2N2973, 75, 77, 79 2N2913, 15, 17, 19, 2N2972, 74, 76, 78 2N2914, 16, 18, 20, 2N2973, 75, 77, 79 2N2913, 15, 17, 19, 2N2972, 74, 76, 78 2N2914, 16, 18, 20, 2N2973, 75, 77, 79 2N2913, 15, 17, 19, 2N2972, 74, 76, 78 2N2914, 16, 18, 20, 2N2973, 75, 77, 79	h_{FE}	60 150 15 30 100 225 150 300	— — — — — — — —	240 800	—
Output Capacitance ($V_{CB} = 5\text{ Vdc}$, $I_E = 0$, $f = 140\text{ kc}$) All Types	C_{obo}	—	4	6	pf
High Frequency Current Gain ($I_C = 500\text{ }\mu\text{A}$, $V_{CE} = 5\text{ V}$, $f = 20\text{ mc}$) All Types	$ h_{fe} $	3.0	—	—	—
Input Impedance ($I_C = 1.0\text{ mA}$, $V_{CB} = 5\text{ V}$, $f = 1\text{ kc}$) All Types	h_{ib}	25	28	32	ohms
Output Admittance ($I_C = 1.0\text{ mA}$, $V_{CB} = 5\text{ V}$, $f = 1\text{ kc}$) All Types	h_{ob}	—	—	1.0	μmhos
Noise Figure ($I_C = 10\text{ }\mu\text{A}$, $V_{CE} = 5\text{ V}$, $R_G = 10\text{ kohms}$) $f = 1\text{ kc}$, BW = 200 cps $f = 10\text{ cps to }15.7\text{ kc}$, BW = 10 kc 2N2914, 16, 18, 20, 73, 75, 77, 79 2N2913, 15, 17, 19, 72, 74, 76, 78 2N2914, 16, 18, 20, 73, 75, 77, 79 2N2913, 15, 17, 19, 72, 74, 76, 78	NF	— — — —	2 3 2 3	3 4	db

MATCHING CHARACTERISTICS

DC Current Gain Ratio** ($I_C = 100\text{ }\mu\text{Adc}$, $V_{CE} = 5\text{ Vdc}$) 2N2917, 18, 76, 77 2N2915, 16, 19, 20, 74, 75, 78, 79	h_{FE1}/h_{FE2}^{**}	0.8 0.9	— —	1.0 1.0	—
Base Voltage Differential ($I_C = 10\text{ }\mu\text{A}$, to 1.0 mA , $V_{CE} = 5\text{ Vdc}$) ($I_C = 100\text{ }\mu\text{Adc}$, $V_{CE} = 5\text{ Vdc}$) 2N2917, 18, 76, 77 2N2915, 16, 19, 20, 74, 75, 78, 79 2N2917, 18, 76, 77 2N2915, 16, 19, 20, 74, 75, 78, 79	$ V_{BE1} - V_{BE2} $	— — — —	— — — —	10 5 5 3	mVdc
Base Voltage Differential Change ($I_C = 100\text{ }\mu\text{Adc}$, $V_{CE} = 5\text{ Vdc}$, $T_A = -55\text{ to }+25^\circ\text{C}$) ($I_C = 100\text{ }\mu\text{Adc}$, $V_{CE} = 5\text{ Vdc}$, $T_A = 25\text{ to }125^\circ\text{C}$) 2N2917, 18, 76, 77 2N2915, 16, 19, 20, 74, 75, 78, 79 2N2917, 18, 76, 77 2N2915, 16, 19, 20, 74, 75, 78, 79	$\Delta(V_{BE1} - V_{BE2})$	— — — —	— — — —	1.6 0.8 2.0 1.0	mVdc

* Pulse Test $\leq 300\text{ }\mu\text{sec}$, duty cycle $\leq 2\%$

**The lowest h_{FE} reading is taken as h_{FE1} for this ratio



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